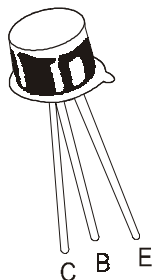


**NPN SILICON PLANAR TRANSISTOR**

**2N718A**



**TO-18  
Metal Can Package**

**General Purpose Transistor.**

**ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)**

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Emitter Voltage	$V_{CER}$	50	V
Collector Base Voltage	$V_{CBO}$	75	V
Emitter Base Voltage	$V_{EBO}$	7.0	V
Power Dissipation @Ta=25°C	$P_D$	500	mW
Derate Above 25°C		2.86	mW/°C
Power Dissipation @ Tc=25°C	$P_D$	1.8	W
Derate Above 25°C		10.3	mW/°C
Operating and Storage Junction Temperature Range	$T_j, T_{stg}$	-65 to +200	°C

**ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise )**

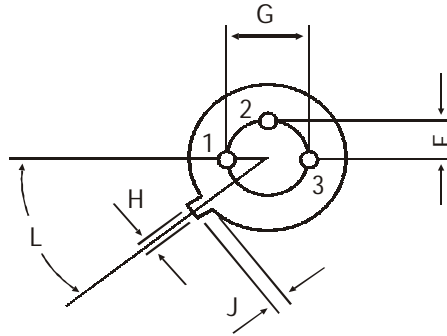
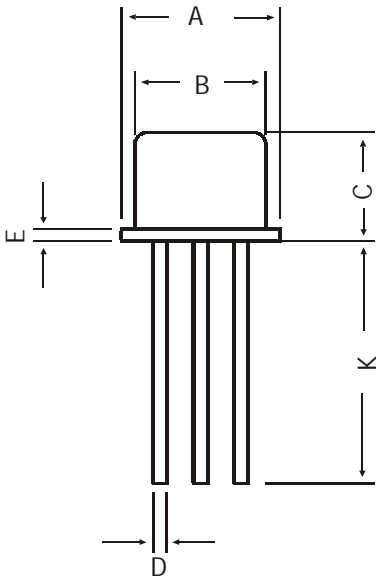
DESCRIPTION	SYMBOL	TEST CONDITION	VALUE		UNIT
			MIN	MAX	
Collector Emitter Voltage	$V_{CER(SUS)}$ *	$I_C=100mA, R_{BE}<10\Omega$	50		V
Collector Base Voltage	$V_{CBO}$	$I_C=100\mu A, I_E=0$	75		V
Emitter Base Voltage	$V_{EBO}$	$I_E=100\mu A, I_C=0$	7.0		V
Collector Cut off Current	$I_{CBO}$	$V_{CB}=60V, I_E=0$		10	nA
		$V_{CB}=60V, I_E=0$			
		Ta=150°C		10	μA
Emitter Cut off Current	$I_{EBO}$	$V_{EB}=5V, I_C=0$		10	nA
DC Current Gain	$h_{FE}$	$I_C=0.1mA, V_{CE}=10V$	20		
		$I_C=10mA, V_{CE}=10V$	35		
		$I_C=10mA, V_{CE}=10V$	20		
		Ta= -55 °C			
		$I_C=150mA, V_{CE}=10V$	40	120	
		$I_C=500mA, V_{CE}=10V$	20		
Collector Emitter Saturation Voltage	$V_{CE(Sat)}$ *	$I_C=150mA, I_B=15mA$		1.5	V
Base Emitter Saturation Voltage	$V_{BE(Sat)}$ *	$I_C=150mA, I_B=15mA$		1.3	V

\* Pulse Test : Pulse Width < 300μs, Duty cycle < 2%

2N718A

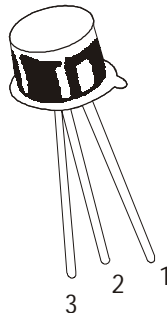
TO-18  
Metal Can Package

TO-18 Metal Can Package



All dimensions in mm.

DIM	MIN	MAX
A	5.24	5.84
B	4.52	4.97
C	4.31	5.33
D	0.40	0.53
E	—	0.76
F	—	1.27
G	—	2.97
H	0.91	1.17
J	0.71	1.21
K	12.70	—
L	45 DEG	



PIN CONFIGURATION

1. EMITTER
2. BASE
3. COLLECTOR

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-18	1K/polybag	350 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	34 kgs

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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