RoHS COMPLIANT

Vishay High Power Products

Schottky Rectifier, 1.0 A



- Small foot print, surface mountable
- · Low forward voltage drop
- · High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

The MBRA140TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.0	A		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	120	A		
V _F	1.0 Apk, T _J = 125 °C	0.49	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	MBRA140TRPbF	UNITS		
Maximum DC reverse voltage	V _R	40	V		
Maximum working peak reverse voltage	V _{RWM}	40	v		

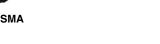
ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4	$I_{F(AV)}$ 50 % duty cycle at T _L = 118 °C, rectat On PC board 9 mm ² island (0.013 mm			1.0	
Maximum peak one cycle non-repetitive surge current See fig. 6	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	120	А
	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	30	
Non-repetitive avalanche energy	E _{AS}	T_J = 25 °C, I_{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}			1.0	А

For technical questions, contact: diodes-tech@vishay.com









104
1.0 A
40 V
A at 125 °C



MBRA140TRPbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T 05 %C	0.55	
		2 A	– T _J = 25 °C	0.71	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T.I = 100 °C	0.5	v
See fig. 1	VFM \''	2 A	- IJ = 100 C	0.65 0.49	v
		1 A	T - 105 °C		
		2 A	– T _J = 125 °C	0.63	
		T _J = 25 °C	V _R = Rated V _R	0.5	mA
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 100 °C		10	
000 ng. 2		T _J = 125 °C		26	
Threshold voltage	V _{F(TO)}	$T_J = T_J maximum$		0.36	V
Forward slope resistance	r _t			104	mΩ
Typical junction capacitance	CT	V_R = 10 V_{DC} , T_J = 25 °C, test signal = 1 MHz		38	pF
Typical series inductance	Ls	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	35	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	°C/w
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Device marking		Case style SMA (similar D-64)	V1	4

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

 $^{(2)}\,$ Mounted 1" square PCB, thermal probe connected to lead 2 mm from package



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Allowable Case Temperature (°C)

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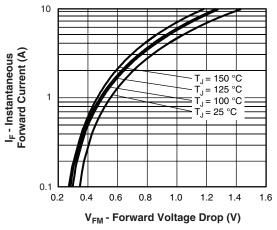
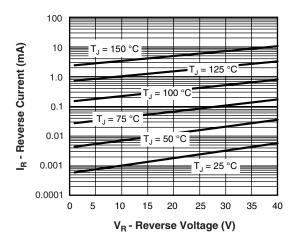
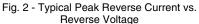
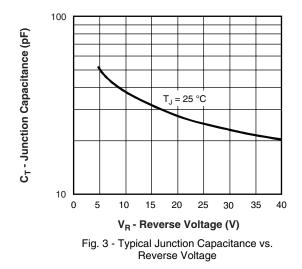
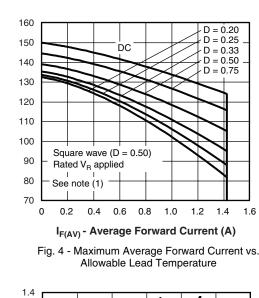


Fig. 1 - Maximum Forward Voltage Drop Characteristics









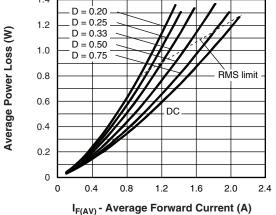
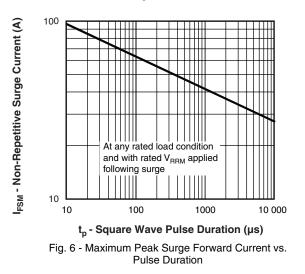


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

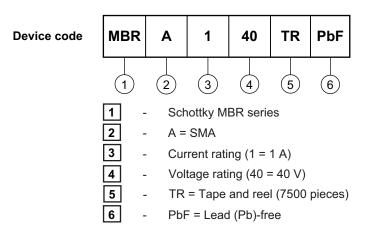
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

MBRA140TRPbF

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95018		
Part marking information	http://www.vishay.com/doc?95029		
Packaging information	http://www.vishay.com/doc?95034		



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