

TLP665J(S)

Office Equipment
Household Appliances
Triac Drivers
Solid State Relays

The TOSHIBA TLP665J(S) consists of a gallium arsenide infrared emitting diode optically coupled to a triac-output photocoupler housed in a 6-pin DIP package.

- Peak Off-State Voltage: 600 V (min)
- Trigger LED Current: 10 mA (max)
- On-State Current: 100 mA (max)
- Isolation Voltage: 5000 Vrms (min)
- UL approved: UL1577, File No.E67349
- cUL approved :CSA Component Acceptance Service No. 5A, File No.E67349
- CQC approved: GB4943.1,GB8898 Japan Factory
- Option (D4) VDE approved : DIN EN60747-5-5 ,EN60065,EN60950-1 (Note1) EN62368-1(Pending) (Note1)

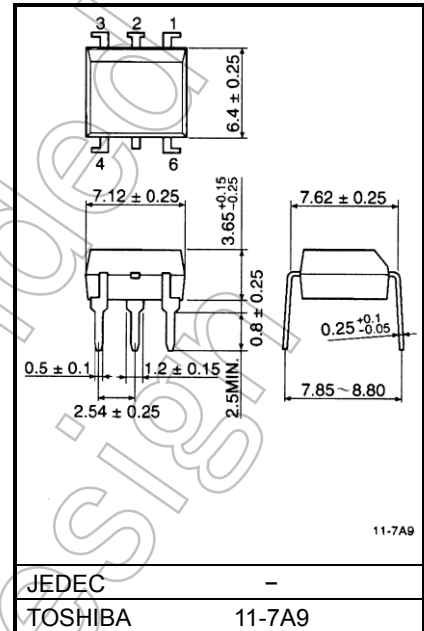
(Note 1) : When a EN60747-5-5 approved type is needed, please designate "Option(D4)"

Maximum operating insulation voltage: 890 VPK
Maximum permissible overvoltage: 8000 VPK

• Construction Mechanical Rating

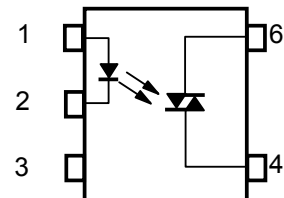
	7.62 mm pitch Standard Type	10.16 mm pitch TLPxxxxF Type
Creepage Distance	7.0 mm (min)	8.0 mm (min)
Clearance	7.0 mm (min)	8.0 mm (min)
Insulation Thickness	0.5 mm (min)	0.5 mm (min)

Unit: mm



Weight: 0.39 g (typ.)

Pin Configuration (top view)



- 1: Anode
- 2: Cathode
- 3: N.C.
- 4: Triac Terminal
- 6: Triac Terminal

Start of commercial production
1987-06

Absolute Maximum Ratings (Ta=25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	I _F	50	mA
	Forward Current Derating (Ta ≥ 53°C)	ΔI _F /°C	-0.7	mA/°C
	Peak Forward Current (100 μs pulse, 100 pps)	I _{FP}	1	A
	Reverse Voltage	V _R	5	V
	Power Dissipation	P _D	100	mW
	Power Dissipation Derating (Ta ≥ 53°C)	ΔP _D /°C	-1.4	mW/°C
	Junction Temperature	T _j	125	°C
DETECTOR	Off-State Output Terminal Voltage	V _{DRM}	600	V
	On-State RMS Current	Ta=25°C	100	mA
		Ta=70°C	50	
	On-State Current Derating (Ta ≥ 25°C)	ΔI _T /°C	-1.1	mA/°C
	Peak On-State Current (100 μs pulse, 120 pps)	I _{TP}	2	A
	Peak Nonrepetitive Surge Current (Pw=10 ms)	I _{TSM}	1.2	A
	Output Power Dissipation	P _O	300	mW
	Output Power Dissipation Derating (Ta ≥ 25°C)	ΔP _O /°C	-3.0	mW/°C
	Junction Temperature	T _j	115	°C
	Storage Temperature Range	T _{stg}	-55 to 125	°C
Operating Temperature Range	T _{opr}	-40 to 100	°C	
Lead Soldering Temperature (10 s)	T _{sol}	260	°C	
Total Package Power Dissipation	P _T	330	mW	
Total Package Power Dissipation Derating (Ta ≥ 25°C)	ΔP _T /°C	-4.4	mW/°C	
Isolation Voltage (AC, 1minute, R.H.≤ 60%)	(Note 2) BV _S	5000	V _{rms}	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2) Device considered a two terminal device :Pins1, 2 and 3 shorted together and pin 4 and pin 6 shorted together.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	V _{AC}	—	—	240	V _{ac}
Forward Current	I _F	15	20	25	mA
Peak On-State Current	I _{TP}	—	—	1	A
Operating Temperature	T _{opr}	-25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

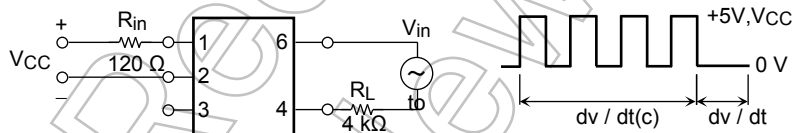
Individual Electrical Characteristics (Ta=25°C)

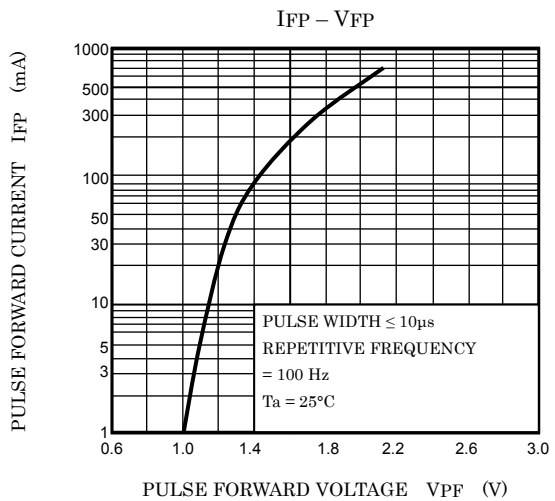
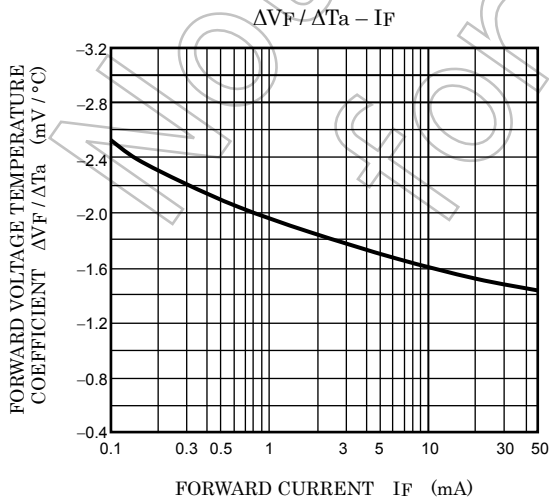
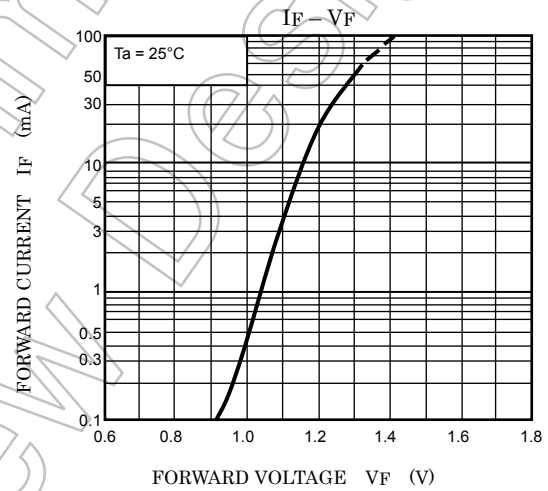
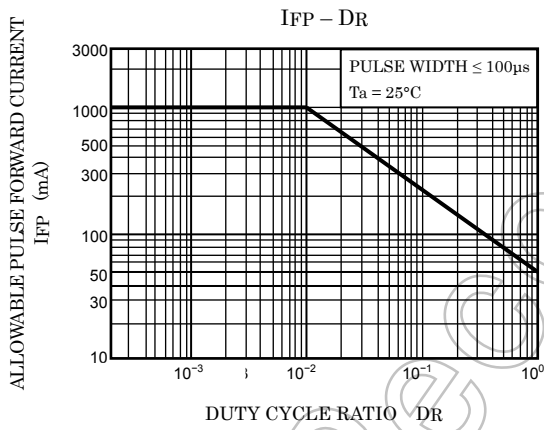
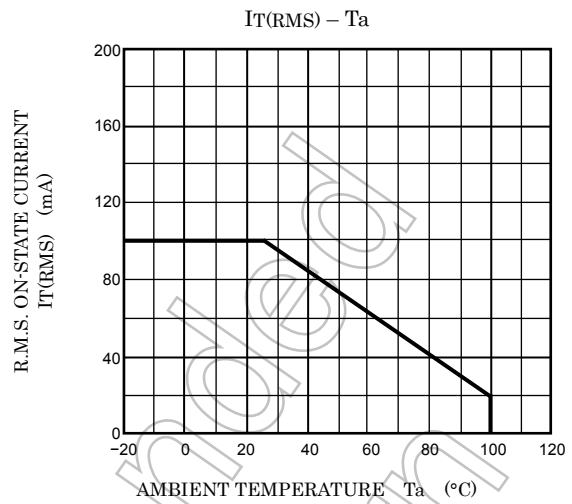
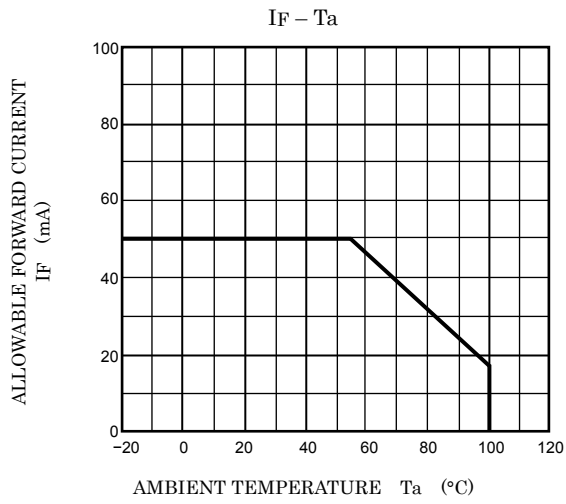
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse Current	I _R	V _R = 5 V	—	—	10	μA
	Capacitance	C _T	V _F = 0 V, f=1 MHz	—	30	—	pF
DETECTOR	Peak Off-State Current	I _{DRM}	V _{DRM} = 600 V	—	10	1000	nA
	Peak On-State Voltage	V _{TM}	I _{TM} = 100 mA	—	1.7	3.0	V
	Holding Current	I _H	—	—	1.0	—	mA
	Critical Rate of Rise of Off-State Voltage	dv/dt	V _{in} = 240 Vrms, Ta=85°C (Fig.1)	—	500	—	V/μs
	Critical Rate of Rise of Commutating Voltage	dv/dt(c)	V _{in} = 60 Vrms, I _T =15 mA (Fig.1)	—	0.2	—	V/μs

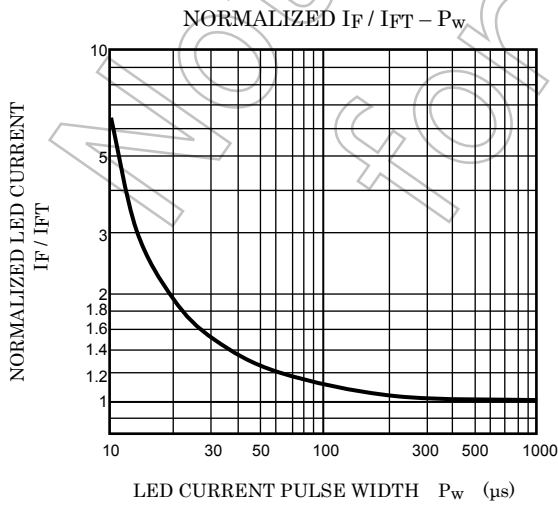
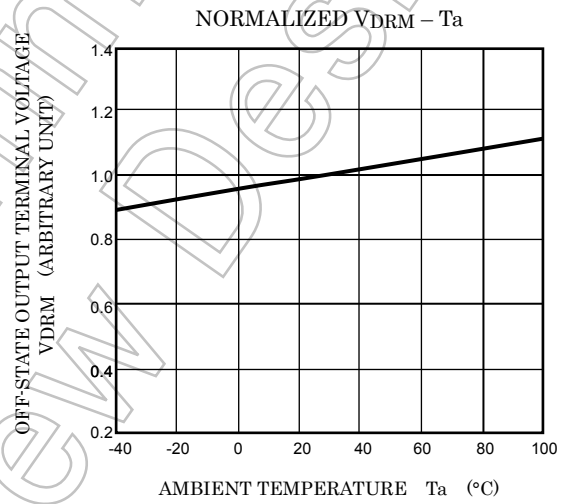
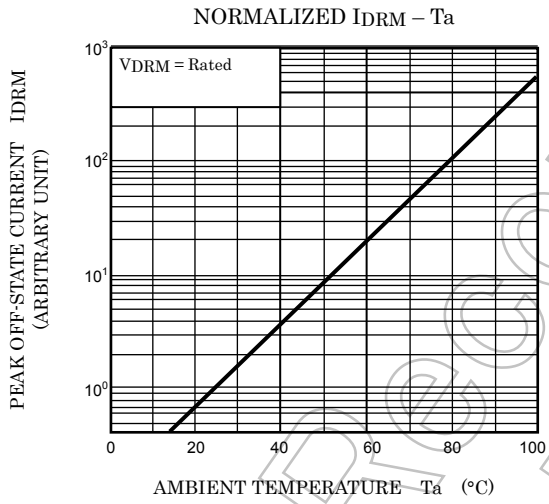
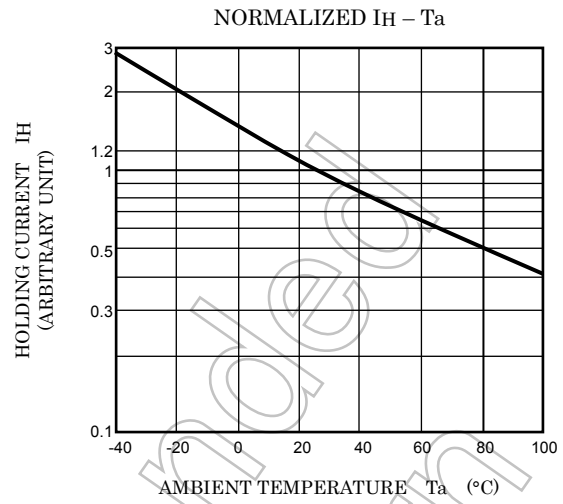
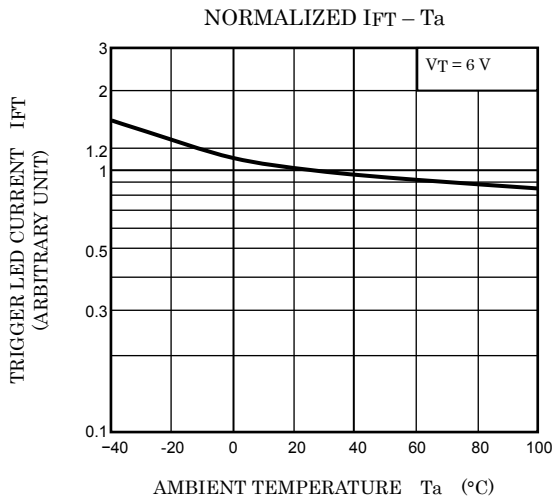
Coupled Electrical Characteristics (Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Trigger LED Current	I _{FT}	V _T =6 V	—	5	10	mA
Capacitance (Input to Output)	C _S	V _S =0 V, f=1 MHz	—	0.8	—	pF
Isolation Resistance	R _S	V _S =500 V (R.H.≤ 60%)	5×10 ¹⁰	10 ¹⁴	—	Ω
Isolation Voltage	BV _S	AC, 1 minute	5000	—	—	Vrms
		AC, 1 second, in oil	—	10000	—	Vrms
		DC, 1 minute, in oil	—	10000	—	Vdc

Fig. 1 dv / dt test circuit







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