ROHS COMPLIANT

FEATURES

Schottky Rectifier, 1.0 A

- Small foot print, surface mountable
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The MBRS130TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	А	
V _{RRM}		30	V	
I _{FSM}	t _p = 5 μs sine	230	А	
V _F	1.0 Apk, T _J = 125 °C	0.42	V	
TJ	Range	- 55 to 125	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	MBRS130TRPbF	UNITS		
Maximum DC reverse voltage	V _R	30	V		
Maximum working peak reverse voltage	V _{RWM}	30	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$ 50 % duty cycle at T _L = 147 °C, rectangular waveform		1.0		
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	870	А
non-repetitive surge current, $T_J = 25 \ ^{\circ}C$		10 ms sine or 6 ms rect. pulse		50	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 6 \text{ mH}$		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s1.0Frequency limited by T _J maximum V _A = 1.5 x V _B typical1.0		А	

For technical questions, contact: diodes-tech@vishay.com

	o——∣⊲ Cathode
SMB	

-0

Anode

PRODUCT SUMMARY	1

PRODUCT SUMMARY			
I _{F(AV)}	1.0 A		
V _R	30 V		
I _{RM}	15 mA at 125 °C		





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Vishay High Power Products Schottky Rectifier, 1.0 A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.6	v
Maximum forward voltage drop		2 A		0.67	
Maximum forward voltage drop		1 A	T _J = 125 °C	0.42	
		2 A		0.52	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.5	mA
		T _J = 100 °C		5.0	
		T _J = 125 °C		15	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J (1)		- 55 to 125	°C
Maximum storage temperature range	T _{Stg}		- 55 to 150	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	25	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	
Approvimete weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	V13	

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



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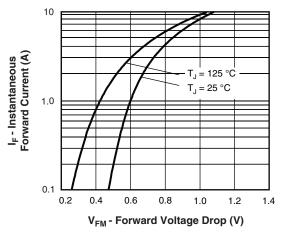
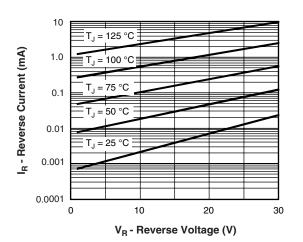
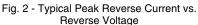


Fig. 1 - Maximum Forward Voltage Drop Characteristics





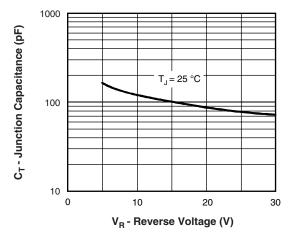
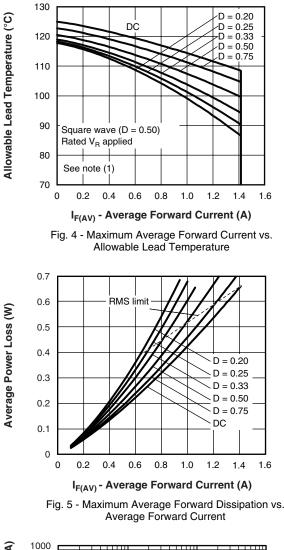
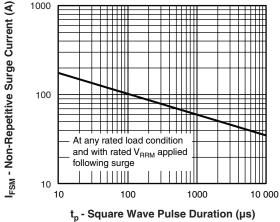
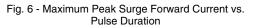


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage







Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

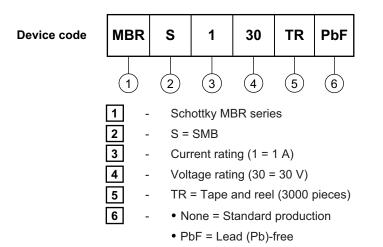
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

MBRS130TRPbF

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS		
Dimensions	http://www.vishay.com/doc?95017	
Part marking information http://www.vishay.com/doc?95029		
Packaging information http://www.vishay.com/doc?95034		



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