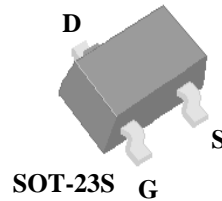




- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device
- ▼ RoHS Compliant & Halogen-Free

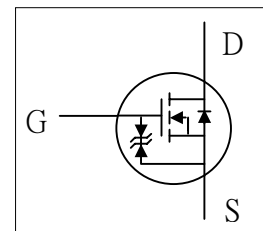


| | |
|--------------|-------|
| BV_{DSS} | 60V |
| $R_{DS(ON)}$ | 2Ω |
| I_D | 450mA |

Description

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The SOT-23S package is universally used for all commercial-industrial applications.



Absolute Maximum Ratings @ $T_J=25^\circ\text{C}$ (unless otherwise specified)

| Symbol | Parameter | Rating | Units |
|------------------------------|---------------------------------------------|------------|-------|
| V_{DS} | Drain-Source Voltage | 60 | V |
| V_{GS} | Gate-Source Voltage | +20 | V |
| $I_D @ T_A=25^\circ\text{C}$ | Drain Current ³ , V_{GS} @ 10V | 450 | mA |
| $I_D @ T_A=70^\circ\text{C}$ | Drain Current ³ , V_{GS} @ 10V | 360 | mA |
| I_{DM} | Pulsed Drain Current ¹ | 950 | mA |
| $P_D @ T_A=25^\circ\text{C}$ | Total Power Dissipation | 0.7 | W |
| T_{STG} | Storage Temperature Range | -55 to 150 | °C |
| T_J | Operating Junction Temperature Range | -55 to 150 | °C |

Thermal Data

| Symbol | Parameter | Value | Unit |
|--------|-----------------------------------------------------------|-------|------|
| Rthj-a | Maximum Thermal Resistance, Junction-ambient ³ | 180 | °C/W |



Electrical Characteristics @T_j=25°C(unless otherwise specified)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|---------------------|------------------------------------------------|----------------------------------------------------------|------|------|------|-------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250uA | 60 | - | - | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =10V, I _D =450mA | - | - | 2 | Ω |
| | | V _{GS} =4.5V, I _D =200mA | - | - | 4 | Ω |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250uA | 1 | - | 2.5 | V |
| g _{fs} | Forward Transconductance | V _{DS} =10V, I _D =450mA | - | 400 | - | mS |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =48V, V _{GS} =0V | - | - | 25 | uA |
| I _{GSS} | Gate-Source Leakage | V _{GS} =±20V, V _{DS} =0V | - | - | ±30 | uA |
| Q _g | Total Gate Charge | I _D =450mA | - | 1 | 1.6 | nC |
| Q _{gs} | Gate-Source Charge | V _{DS} =50V | - | 0.5 | - | nC |
| Q _{gd} | Gate-Drain ("Miller") Charge | V _{GS} =4.5V | - | 0.5 | - | nC |
| t _{d(on)} | Turn-on Delay Time | V _{DS} =30V | - | 12 | - | ns |
| t _r | Rise Time | I _D =450mA | - | 10 | - | ns |
| t _{d(off)} | Turn-off Delay Time | R _G =3.3Ω | - | 56 | - | ns |
| t _f | Fall Time | V _{GS} =10V | - | 29 | - | ns |
| C _{iss} | Input Capacitance | V _{GS} =0V | - | 32 | 50 | pF |
| C _{oss} | Output Capacitance | V _{DS} =25V | - | 8 | - | pF |
| C _{rss} | Reverse Transfer Capacitance | f=1.0MHz | - | 6 | - | pF |

Source-Drain Diode

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|-----------------|---------------------------------|--------------------------------------------|------|------|------|-------|
| V _{SD} | Forward On Voltage ² | I _S =450mA, V _{GS} =0V | - | - | 1.2 | V |

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board t ≤ 10sec; 400°C/W when mounted on min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

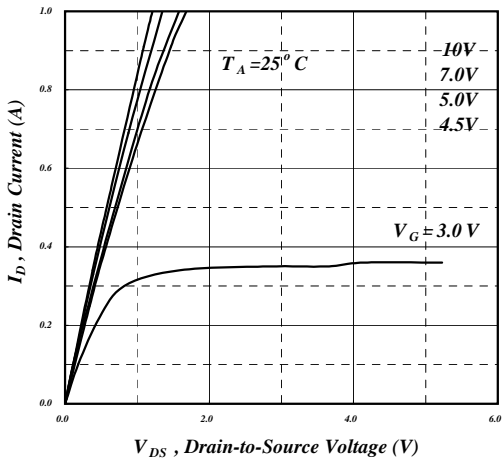


Fig 1. Typical Output Characteristics

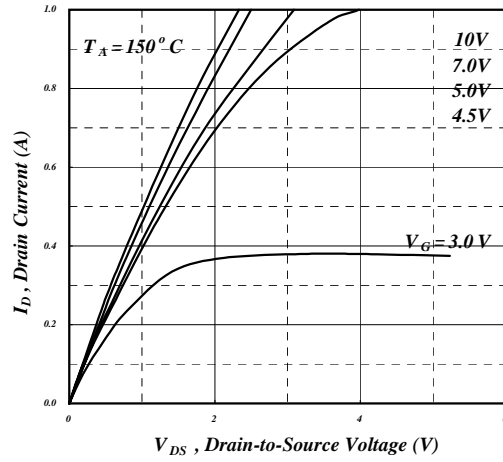


Fig 2. Typical Output Characteristics

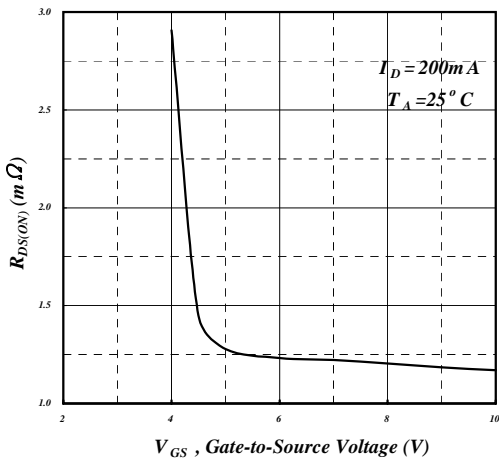


Fig 3. On-Resistance v.s. Gate Voltage

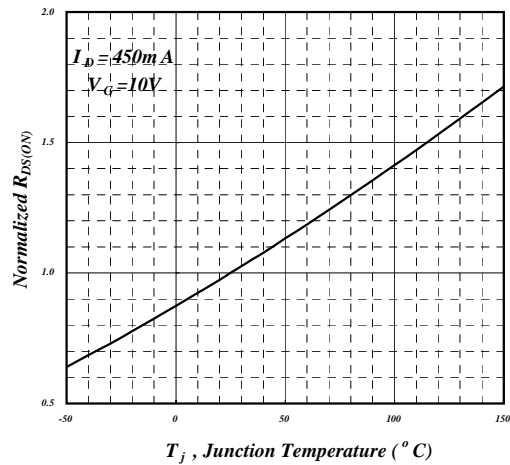


Fig 4. Normalized On-Resistance v.s. Junction Temperature

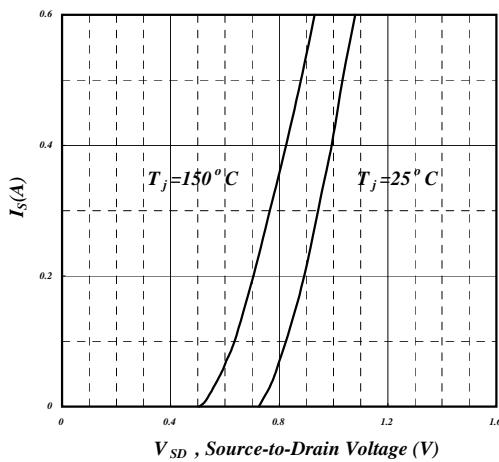


Fig 5. Forward Characteristic of Reverse Diode

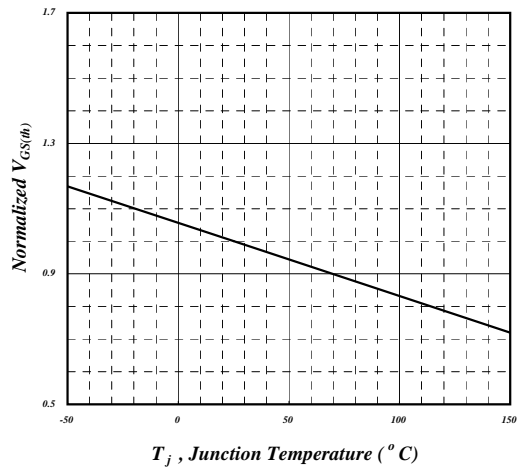


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

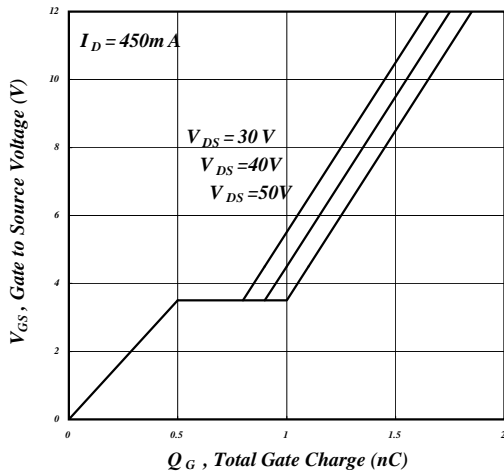


Fig 7. Gate Charge Characteristics

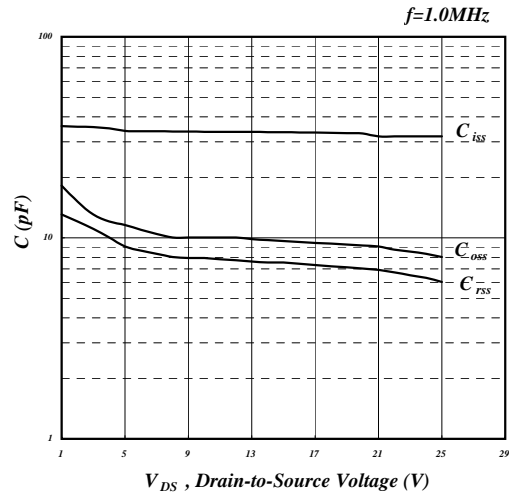


Fig 8. Typical Capacitance Characteristics

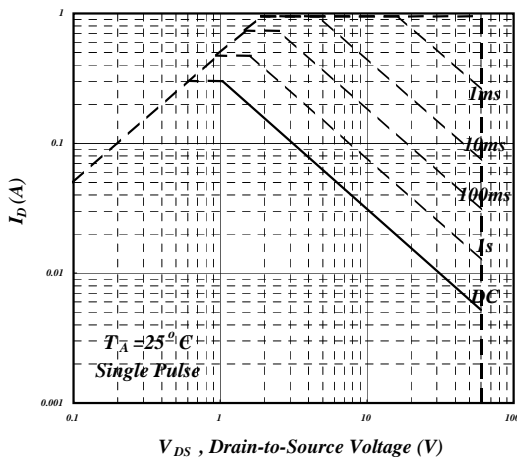


Fig 9. Maximum Safe Operating Area

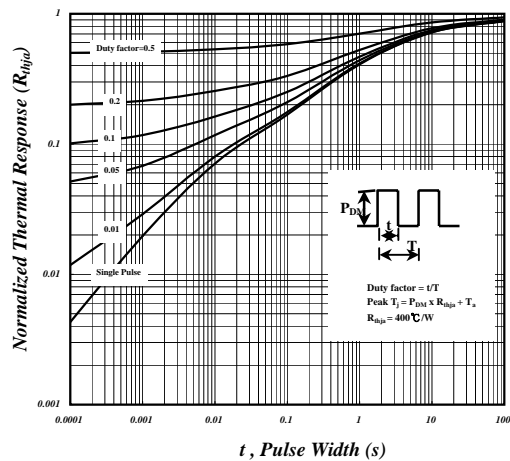


Fig 10. Effective Transient Thermal Impedance

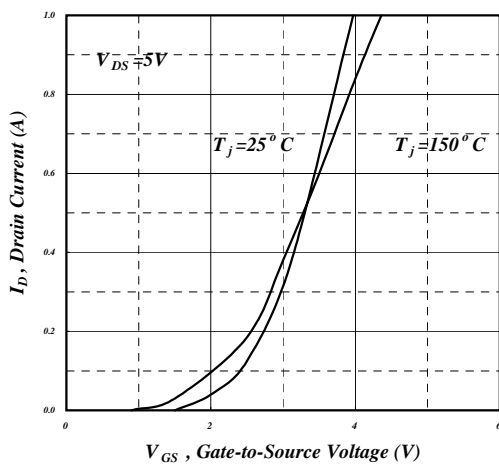


Fig 11. Transfer Characteristics

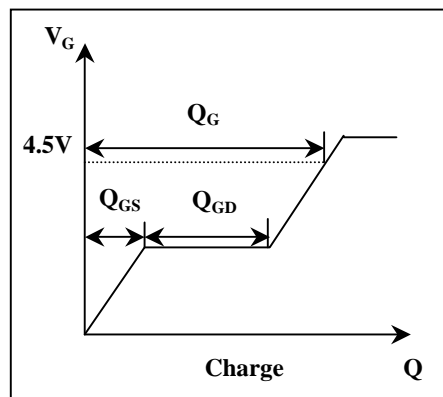


Fig 12. Gate Charge Waveform



MARKING INFORMATION

