

VS-ST180S...VPbF Series

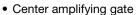
Vishay Semiconductors

Phase Control Thyristors (Stud Version), 200 A



PRIMARY CHARACTERISTICS						
I _{T(AV)} 200 A						
V _{DRM} /V _{RRM}	400 V, 800 V, 1200 V					
V _{TM}	1.75 V					
I _{GT}	150 mA					
TJ	-40 °C to +125 °C					
Package	TO-93 (TO-209AB)					
Circuit configuration	Single SCR					

FEATURES



- International standard case TO-93 (TO-209AB)
- Glass-metal seal up to 1200 V
- Compression bonded encapsulation for heavy duty operations such as severe thermal cycling
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- DC motor controls
- Controlled DC power supplies
- AC controllers

MAJOR RATINGS AND CHARACTERISTICS							
PARAMETER	TEST CONDITIONS	VALUES	UNITS				
1		200	A				
I _{T(AV)}	T _C	85	°C				
I _{T(RMS)}		314	A				
1	50 Hz	5000	•				
ITSM	60 Hz	5230	- A				
l ² t	50 Hz	125	1.42-				
1-1	60 Hz	114	- kA ² s				
V _{DRM} /V _{RRM}		400 to 1200	V				
tq	Typical	100	μs				
TJ		-40 to +125	°C				

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS								
TYPE NUMBER	VOLTAGE CODE	V _{DRM} /V _{RRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK VOLTAGE	I_{DRM}/I_{RRM} MAXIMUM AT T _J = T _J MAXIMUM				
	0021	V	V	mA				
	04	400	500					
VS-ST180S	08	800	900	30				
	12	1200	1300					

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ROHS COMPLIANT

VS-ST180S...VPbF Series



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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL		TEST CONDITIONS			UNITS	
Maximum average on-state current	1	190° condu	ction, half sine	Novo	200	А	
at case temperature	I _{T(AV)}	180 condu	ction, nail sine	wave	85	°C	
Maximum RMS on-state current	I _{T(RMS)}	DC at 76 °C	case temperat	ure	314		
		t = 10 ms	No voltage		5000		
Maximum peak, one-cycle		t = 8.3 ms	reapplied		5230	A kA ² s	
non-repetitive surge current	I _{TSM}	t = 10 ms	100 % V _{RRM}		4200		
		t = 8.3 ms	reapplied	Sinusoidal half wave, initial $T_J = T_J$ maximum	4400		
Maximum I ² t for fusing		t = 10 ms	No voltage reapplied		125		
	l ² t	t = 8.3 ms			114		
		t = 10 ms	100 % V _{RRM}		88		
		t = 8.3 ms	reapplied		81		
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10	1250	kA²√s			
Low level value of threshold voltage	V _{T(TO)1}	(16.7 % x π x I _{T(AV)} < I < π x I _{T(AV)}), T _J = T _J maximum			1.08	v	
High level value of threshold voltage	V _{T(TO)2}	(I > π x I _{T(AV)}), T _J = T _J maximum			1.14	v	
Low level value of on-state slope resistance	r _{t1}	(16.7 % x π x $I_{T(AV)}$ < I < π x $I_{T(AV)}$), T _J = T _J maximum			1.18		
High level value of on-state slope resistance	r _{t2}	$(I > \pi x I_{T(AV)}), T_J = T_J maximum$			1.14	mΩ	
Maximum on-state voltage	V _{TM}	$I_{pk} = 570 \text{ A}, T_{J} = 125 \text{ °C}, t_{p} = 10 \text{ ms sine pulse}$			1.75	V	
Maximum holding current	Ι _Η	т т	inauna anada a	upply 10 V registive loss	600		
Maximum (typical) latching current	١L	$T_J = T_J$ maximum, anode supply 12 V resistive load 1000 (3			1000 (300)	mA	

SWITCHING								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum non-repetitive rate of rise of turned-on current	dl/dt	Gate drive 20 V, 20 $\Omega,t_r \leq 1~\mu s$ T_J = T_J maximum, anode voltage $\leq 80~\%~V_{DRM}$	1000	A∕µs				
Typical delay time t _d		Gate current 1 A, dl _g /dt = 1 A/ μ s V _d = 0.67 % V _{DRM} , T _J = 25 °C	1.0	110				
Typical turn-off time t _q		$\begin{split} I_{TM} &= 300 \text{ A}, T_J = T_J \text{ maximum, dI/dt} = 20 \text{ A/}\mu\text{s}, \\ V_R &= 50 \text{ V}, \text{ dV/dt} = 20 \text{ V/}\mu\text{s}, \text{ gate } 0 \text{ V} 100 \ \Omega, t_p = 500 \ \mu\text{s} \end{split}$	100	μs				

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum critical rate of rise of off-state voltage	dV/dt	$T_J = T_J$ maximum linear to 80 % rated V_{DRM}	500	V/µs
Maximum peak reverse and off-state leakage current	I _{RRM} , I _{DRM}	$T_J = T_J$ maximum, rated V_{DRM}/V_{RRM} applied	30	mA



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TRIGGERING			
IRIGGERING	-		

TRIGGERING							
PARAMETER	SYMBOL	т	EST CONDITIONS	VAL	UES	UNITS	
PANAMETEN	TER STMBOL TEST CONDITIONS		TYP.	MAX.	UNITS		
Maximum peak gate power	P _{GM}	$T_J = T_J$ maximum,	, $t_p \le 5 \text{ ms}$	1	0	W	
Maximum average gate power	P _{G(AV)}	$T_J = T_J$ maximum,	, f = 50 Hz, d% = 50	2	.0	vv	
Maximum peak positive gate current	I _{GM}	$T_J = T_J$ maximum,	, $t_p \le 5 \text{ ms}$	3	.0	А	
Maximum peak positive gate voltage	$+V_{GM}$	T. T. M. M. L. C. M.		2	0	V	
Maximum peak negative gate voltage	-V _{GM}	$T_J = T_J$ maximum, $t_p \le 5$ ms			.0	v	
	I _{GT}	T _J = - 40 °C		180	-	mA	
DC gate current required to trigger		T _J = 25 °C	Maximum required acts trigger (90	150		
		T _J = 125 °C	Maximum required gate trigger / current / voltage are the lowest	40	-		
		T _J = - 40 °C	value which will trigger all units 12 V anode to cathode applied	2.9	-		
DC gate voltage required to trigger	V _{GT}	T _J = 25 °C	12 V anode to cathode applied	1.8	3.0	V	
		T _J = 125 °C		1.2	-		
DC gate current not to trigger	I _{GD}		Maximum gate current/voltage not			mA	
DC gate voltage not to trigger	V _{GD}	$T_J = T_J maximum$	to trigger is the maximum value which will not trigger any unit with rated V _{DRM} anode to cathode applied	0.05		V	

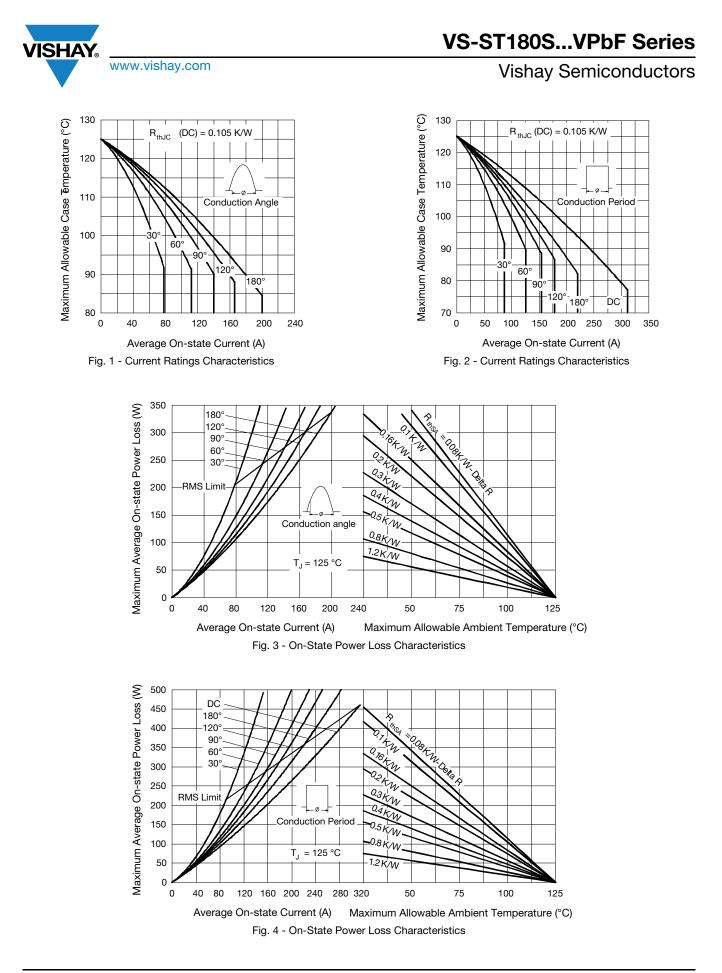
THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	. TEST CONDITIONS VALUE		UNITS			
Maximum operating junction temperature range	TJ		-40 to +125	°C			
Maximum storage temperature range	T _{Stg}		-40 to +150				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation 0.105		K/W			
Maximum thermal resistance, case to heatsink	R _{thC-hs}	Mounting surface, smooth, flat and greased	0.04	1// 1/			
Mounting torque + 10.0/		Non-lubricated threads	31 (275)	N⋅m			
Mounting torque, ± 10 %		Lubricated threads	24.5 (210)	(lbf · in)			
Approximate weight			280	g			
Case style		See dimensions - link at the end of datasheeet TO-93 (TO-209A		09AB)			

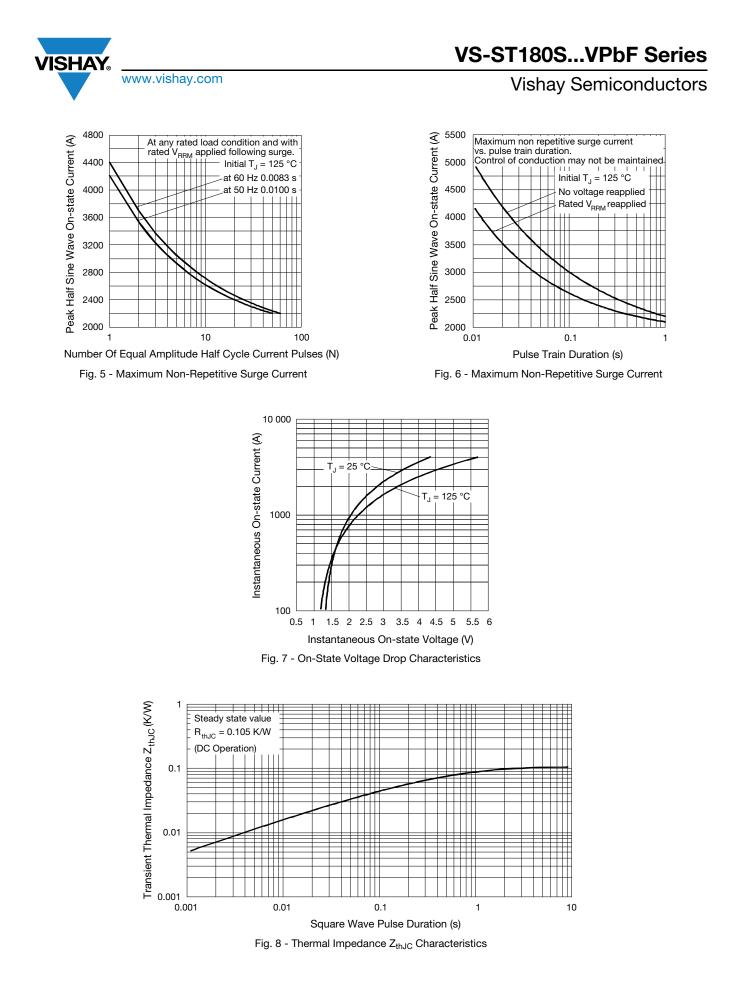
$\Delta \mathbf{R}_{\text{thJC}}$ CONDUCTION				
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS
180°	0.015	0.012		
120°	0.019	0.020		
90°	0.025	0.027	T _J = T _J maximum	
60°	0.036	0.037		
30°	0.060	0.060		

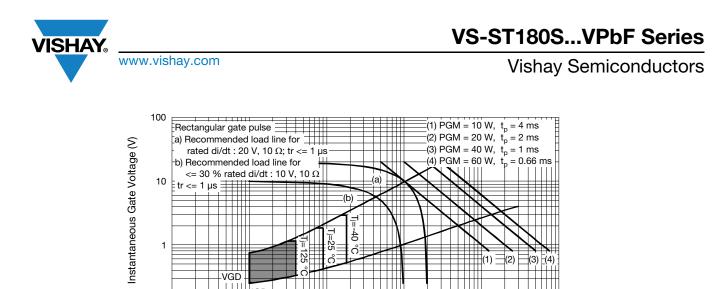
Note

• The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

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0.1

Instantaneous Gate Current (A) Fig. 9 - Gate Characteristics

Frequency Limited by PG(AV)

10

100



0.1

IGD

0.01

Device code	VS-	ST	18	0	S	20	Р	0	V	PbF
	1	2	3	4	5	6	7	8	9	10
	3 - 4 - 5 - 6 - 7 - 8 -	 Vishay Semiconductors product Thyristor Essential part number 0 = converter grade S = compression bonding stud Voltage code x 100 = V_{RRM} (see Voltage Ratings table) P = stud base 3/4"-16UNF2A threads 0 = eyelet terminals (gate and auxiliary cathode leads) 1 = fast-on terminals (gate and auxiliary cathode leads) V = glass-metal seal (only up to 1200 V) 								
	10 -			ndard pr (Pb)-fre		n				

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95082					

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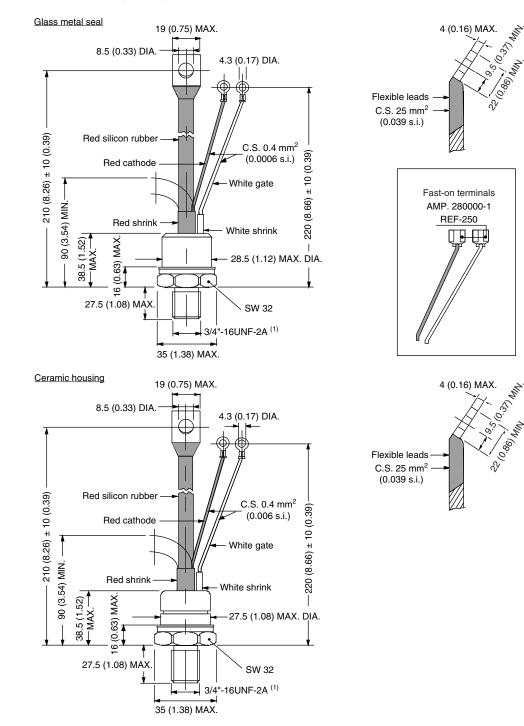
NI.

NIN,



DIMENSIONS in millimeters (inches)

www.vishay.com



Note

⁽¹⁾ For metric device: M16 x 1.5 - length 21 (0.83) maximum

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