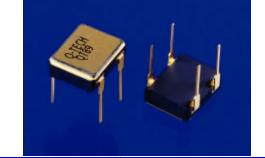
QT89 Series

5.0Vdc and 3.3Vdc, STRAIGHT LEADS HIGH-RELIABILITY CLOCK OSCILLATORS

FEATURES

- Broad frequency range from 500 kHz to 160 MHz.
- Rugged design for high shock and vibration.
- ACMOS, HCMOS, TTL, LVHCMOS logic.
- 5.0V or 3.3V Tri-State Output Option (D).
- High temperature reliability for Down-Hole applications.
- Straight leads.



ELECTRICAL CHARACTERISTICS

PARAMETERS $(5.0V \pm 10\%)$	QT89AC	QT89HC	QT89T
Output frequency range (Fo)	500.00 kHz – 125.00MHz (Low frequency down to 1kHz available in No-Tristate)		500.00 kHz – 85.000MHz
Supply voltage (Vdd)	5.0Vdc ± 10%		
Frequency stability vs. temperature	See Option codes		
Operating temperature (Topr)	See Option codes		
Storage temperature (Tsto)	-62°C to +125°C		
Symmetry (15pF at 50% output level AC or HC) (6TTL load at 1.4Vdc TTL)	45/55% max. (500kHz ~ <16MHz) 40/60% max. (16MHz ~ 125MHz) (Tighter symmetry available)		
Operating supply current (Idd)	20 mA max. (500kHz ~ < 16MHz)		
(No Load)	25 mA max. (16MHz ~ < 32MHz)		
(Lower current available)	35 mA max. (32MHz ~ < 60MHz) 45 mA max. (60MHz ~ = 125MHz)		
Rise and Fall times (Tr/Tf) (at 5.0Vdc supply, 15pF//10kohms load)	6ns max. (Fo < 30M) 3ns max. (30M = Fo = 125M) (between 10% to 90%)	7ns max. (Fo < 30M) 5ns max. (30M = Fo = 125M) (between 10% to 90%)	5ns max. (Fo < 30M) 3ns max. (30M = Fo = 85M) (between 0.8V to 2V)
Output load (CL)	15pF // 10kohms (50pF max. or 10TTL for F = 60M) (30pF max. or 6TTL for F = 85M)	15pF // 10kohms (2LSTTL)	10TTL (F < 60MHz) 6TTL (60MHz to 85MHz)
Start-up time (Tstup) 15pF load	5ms max.		
Output voltage (Voh/Vol)	0.9xVdd min.; 0.1xVdd max. 2.4V min.; 0.4V max.		
Enable/Disable Tristate function (* D)	Pin 1 VIH = 2.2V Oscillation; VIL = 0.8V High Impedance		
Jitter RMS 1s(at 25°C)	8ps typ. (<40MHz); 3ps typ. (= 40MHz)		
Aging (at 70°C)	± 5ppm/year max.		
Freq. vs. supply voltage tolerance (±10%)	± 4ppm max.		

PARAMETERS (3.3V ± 10%)	QT89L	
Output frequency range (Fo)	500.000 kHz – 160.000MHz (Low frequency down to 1kHz available in No-Tristate)	
Supply voltage (Vdd)	$3.3 \text{Vdc} \pm 10\%$	
Frequency stability vs. temperature	See Option codes	
Operating temperature (Topr)	See Option codes	
Storage temperature (Tsto)	-62°C to +125°C	
Operating supply current (Idd)	$6 \text{ mA max.} (500 \text{kHz} \sim < 16 \text{MHz})$	
(No Load)	$10 \text{ mA max.} (16\text{MHz} \sim < 32\text{MHz})$	
(Lower current available for	20 mA max. (32MHz ~ < 60MHz)	
high-temperature applications)	$30 \text{ mA max.} (60 \text{MHz} \sim < 100 \text{MHz})$	

Q-Tech Corporation

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	40 mA max. (100MHz ~ < 130MHz) 45 mA max. (130MHz ~ 160MHz)	
Symmetry (15pF, at 50% output level)	45/55% max. (500kHz ~ <16MHz); 40/60% max. (16MHz ~ 160MHz)	
	(Tighter symmetry available)	
Rise and Fall times (Tr/Tf)	6ns max. (500kHz to < 40MHz)	
(at 3.3Vdc supply, 15pF //10kohms)	3ns max. (40MHz ~ 160MHz)	
(Between 10% and 90% of output level)		
Output load (CL)	15pF // 10kohms	
	$(30pF\ max.\ for\ F=50MHz)$	
Start-up time (Tstup)	5ms max.	
Output voltage (Voh/Vol)	0.9xVdd min.; 0.1xVdd max.	
Enable/Disable Tristate function (* D)	Pin 1 VIH = 2.2V Oscillation; VIL = 0.8V High Impedance	
Jitter RMS 1s(at 25°C, 3.3Vdc supply)	15ps typ. (< 40MHz); 8ps typ. (= 40MHz)	
Aging (at $70^{\circ}C$)	± 5ppm/year max.	
Freq. vs. supply voltage tolerance (±10%)	± 4ppm max.	

ORDERING INFORMATION

OT89 AC D 10 M - 85.000MHz

Logic	Tristate Option	Freq. stability vs. Temperature	Screening option
AC = ACMOS	D(*)		M
HC = HCMOS	(Left blank if	$1 = \pm 100$ ppm at 0°C to +70°C	(Designate M for Product Level B
T = TTL	No Tristate)	$4 = \pm 50$ ppm at 0°C to +70°C	MIL-PRF-55310 environmental
		$5 = \pm 25$ ppm at -20°C to +70°C	screening).
		$6 = \pm 50$ ppm at -55°C to +105°C	(Left blank if unscreened)
		$9 = \pm 50$ ppm at -55°C to +125°C	
		$10 = \pm 100$ ppm at -55°C to +125°C	
I - I WICMOS		$11 = \pm 50$ ppm at - 40 °C to + 85 °C	
L = LVHCMOS		$12 = \pm 100$ ppm at - 40 °C to + 85 °C	
		$14 = \pm 20$ ppm at -20°C to +70°C	
		$15 = \pm 25$ ppm at - 40 °C to + 85 °C	
		\pm 40ppm to \pm 350ppm for High-temperature applications at – 55°C to +200°C.	
		Contact Q-Tech for details.	

For frequency stability vs. temperature options not listed herein, please request a custom part number. Part numbering examples: QT89ACD9M-100.000MHz; QT89T10-16.000MHz.

Q-Tech will assign a custom part number for high-temperature applications with frequency-temperature stability tailored to specific requirements.

STANDARD SCREENING PER MIL-PRF-55310, LEVEL B

Internal Visual: MIL-STD-883, Method 2017 and 2032. Stabilization bake: MIL-STD-883, Method 1008, Cond. C. Temperature cycling: MIL-STD-883, Method 1010, Cond. B.

Constant acceleration: MIL-STD-883, Method 2001, Cond. A, Y1 axis.

Seal Fine Leak: MIL-STD-883, Method 1014, Cond. A. Seal Gross Leak: MIL-STD-883, Method 1014, Cond. C.

Burn-in: 160 hours, 125°C with load.

Final Electrical tests.



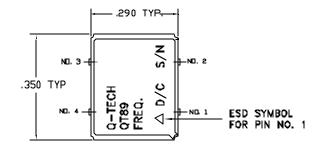
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OUTLINE DRAWING

PIN NO.	FUNCTION	
1	TRISTATE or NC	
2	GND/CASE	
3	OUTPUT	
4	VDD	



.130 MAX

.315 TYP-

STANDARD QCI ENVIRONMENTAL SPECIFICATIONS: (Custom requirements available upon request)

Vibration sinusoidal: MIL-STD-202, Method 204, Cond. D. Shock, non operating: MIL-STD-202, Method 213, Cond. I.

Thermal shock, non operating: MIL-STD-202, Method 107, Cond. B.

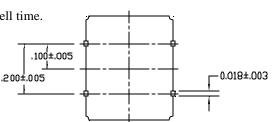
Ambient pressure, non operating: MIL-STD-202, 105, Cond. C, 5 min. dwell time.

Resistance to solder heat: MIL-STD-202, Method 210, Cond. E.

Moisture resistance: MIL-STD-202, Method 106.

Terminal strength: MIL-STD-202, Method 211, Cond. C. Resistance to solvents: MIL-STD-202, Method 215.

Solderability: MIL-STD-202, Method 208.



.004

Standard packaging in Anti-Static Foam, unless otherwise specified. Leads solder dipped available.

Dimensions: inch

Remark: Specifications subject to change without prior notice.

Please contact our factory or visit our website, www.q-tech.com, for technical notes and updates.

Please contact us for variations on these specifications.

