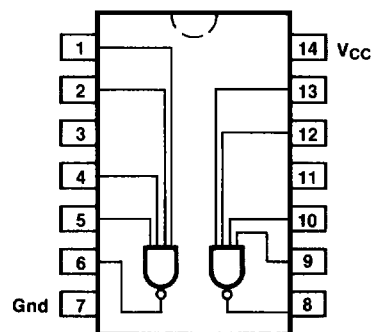
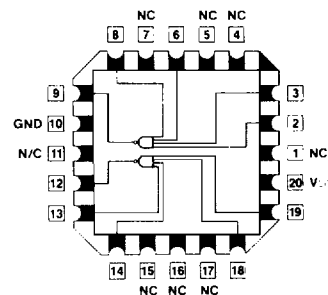


54F/74F40

Dual 4-Input NAND Buffer

PRELIMINARY

Connection Diagrams

Pin Assignment
for DIP and SOICPin Assignment
for LCC and PCC

Ordering Code: See Section 5

Input Loading/Fan-Out: See Section 3 for U.L. definitions

Pin Names	Description	54F/74F(U.L.) HIGH/LOW
A,B,C,D	Inputs	0.5/0.375
O	Outputs	75/40 (30)

Function
Table

Inputs				Outputs
A	B	C	D	O
L	X	X	X	H
X	L	X	X	H
X	X	L	X	H
X	X	X	L	H
H	H	H	H	L

H = HIGH Voltage Level
 L = LOW Voltage Level
 X = Immaterial

DC Characteristics over Operating Temperature Range (unless otherwise specified)

Symbol	Parameter	54F/74F			Units	Conditions
		Min	Typ	Max		
I_{CC}	Power Supply Current			22	mA	$V_{CC} = \text{Max}$, Outputs LOW

AC Characteristics: See Section 3 for waveforms and load configurations

Symbol	Parameter	54F/74F			54F		74F		Units	Fig. No.
		$T_A = +25^\circ\text{C}$ $V_{CC} = +5.0\text{ V}$ $C_L = 50\text{ pF}$			$T_A, V_{CC} = \text{Mil}$ $C_L = 50\text{ pF}$		$T_A, V_{CC} = \text{Com}$ $C_L = 50\text{ pF}$			
		Min	Typ	Max	Min	Max	Min	Max		
t_{PLH} t_{PHL}	Propagation Delay			5.0 4.3				ns	3-1 3-3	