

# 2SC4617G, S2SC4617G

## NPN Silicon General Purpose Amplifier Transistor

This NPN transistor is designed for general purpose amplifier applications. This device is housed in the SC-75/SOT-416 package which is designed for low power surface mount applications, where board space is at a premium.

### Features

- Reduces Board Space
- High  $h_{FE}$ , 210–460 (typical)
- Low  $V_{CE(sat)}$ , < 0.5 V
- Available in 8 mm, 7 inch/3000 Unit Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant\*

### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ )

Rating	Symbol	Value	Unit
Collector-Base Voltage	$V_{(BR)CBO}$	50	Vdc
Collector-Emitter Voltage	$V_{(BR)CEO}$	50	Vdc
Emitter-Base Voltage	$V_{(BR)EBO}$	5.0	Vdc
Collector Current – Continuous	$I_C$	100	mA <sub>dc</sub>

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 1)	$P_D$	125	mW
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-55 ~ +150	$^\circ\text{C}$

1. Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



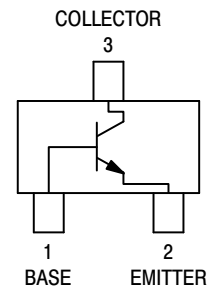
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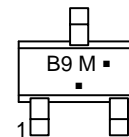
## NPN GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT



SC-75  
CASE 463-01  
STYLE 1



### MARKING DIAGRAM



B9 = Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

### ORDERING INFORMATION

Device	Package	Shipping†
2SC4617G	SC-75 (Pb-Free)	3,000/Tape & Reel
S2SC4617G	SC-75 (Pb-Free)	3,000/Tape & Reel
2SC4617T1G	SC-75 (Pb-Free)	3,000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## 2SC4617G, S2SC4617G

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

Characteristic	Symbol	Min	Typ	Max	Unit
Collector-Base Breakdown Voltage (I <sub>C</sub> = 50 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	50	–	–	Vdc
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 1.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	50	–	–	Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 50 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)EBO</sub>	5.0	–	–	Vdc
Collector-Base Cutoff Current (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	–	–	0.5	μA
Emitter-Base Cutoff Current (V <sub>EB</sub> = 4.0 Vdc, I <sub>B</sub> = 0)	I <sub>EBO</sub>	–	–	0.5	μA
Collector-Emitter Saturation Voltage (Note 2) (I <sub>C</sub> = 60 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>CE(sat)</sub>	–	–	0.4	Vdc
DC Current Gain (Note 2) (V <sub>CE</sub> = 6.0 Vdc, I <sub>C</sub> = 1.0 mAdc)	h <sub>FE</sub>	120	–	560	–
Transition Frequency (V <sub>CE</sub> = 12 Vdc, I <sub>C</sub> = 2.0 mAdc, f = 30 MHz)	f <sub>T</sub>	–	180	–	MHz
Output Capacitance (V <sub>CB</sub> = 12 Vdc, I <sub>C</sub> = 0 Adc, f = 1 MHz)	C <sub>OB</sub>	–	2.0	–	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width ≤ 300 μs, D.C. ≤ 2%.

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## TYPICAL ELECTRICAL CHARACTERISTICS

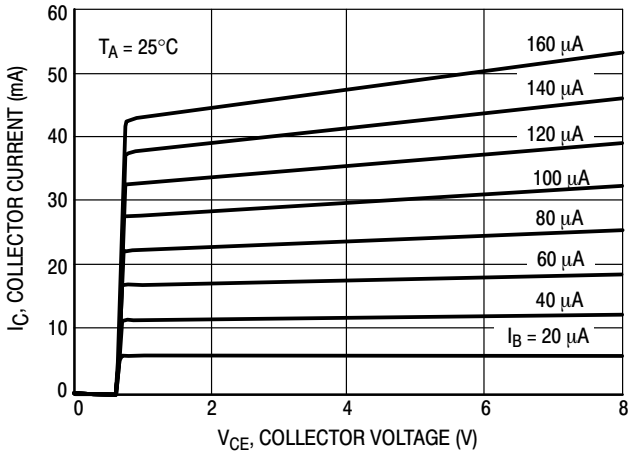


Figure 1.  $I_C - V_{CE}$

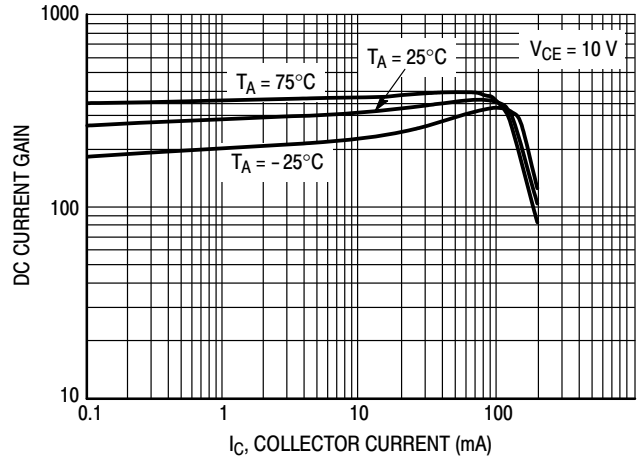


Figure 2. DC Current Gain

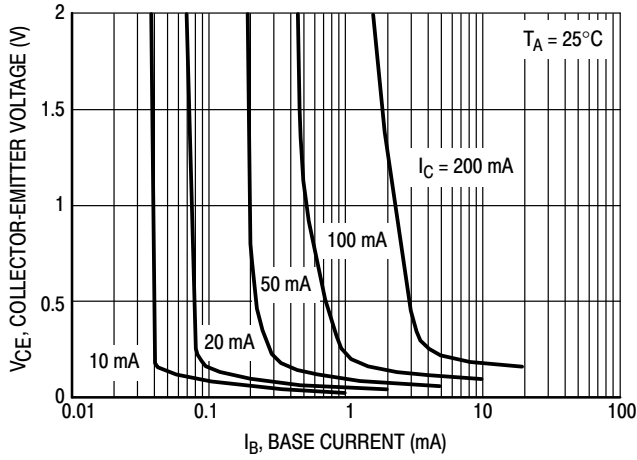


Figure 3. Collector Saturation Region

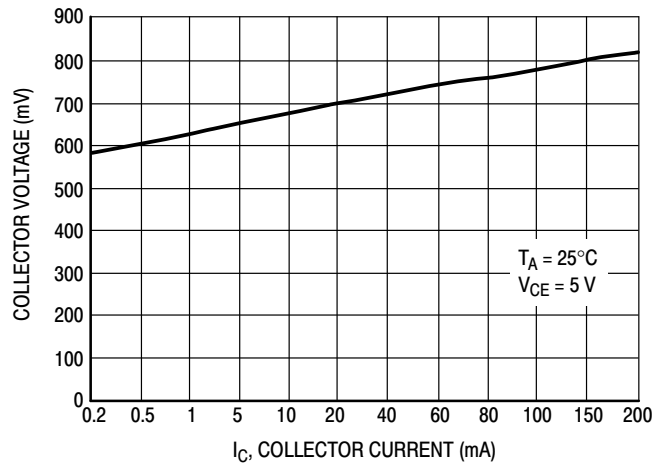


Figure 4. On Voltage

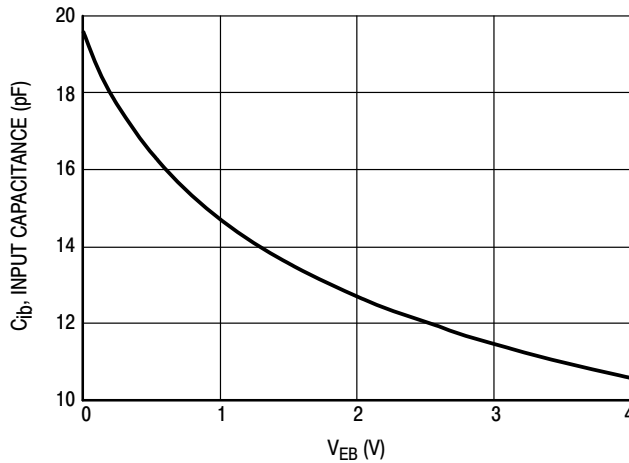


Figure 5. Capacitance

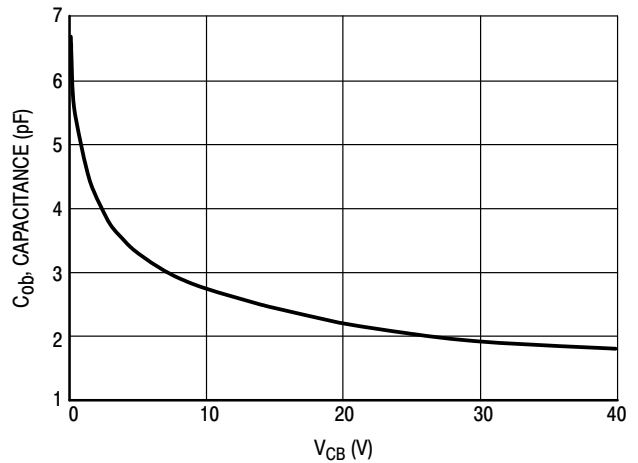
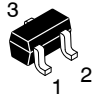


Figure 6. Capacitance

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

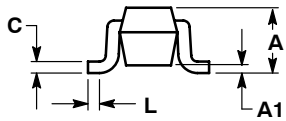
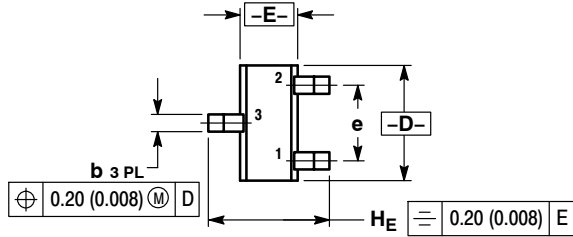
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**SC-75/SOT-416**  
CASE 463-01  
ISSUE G

DATE 07 AUG 2015

SCALE 4:1



STYLE 1:  
PIN 1. BASE  
2. EMITTER  
3. COLLECTOR

STYLE 2:  
PIN 1. ANODE  
2. N/C  
3. CATHODE

STYLE 3:  
PIN 1. ANODE  
2. ANODE  
3. CATHODE

STYLE 4:  
PIN 1. CATHODE  
2. CATHODE  
3. ANODE

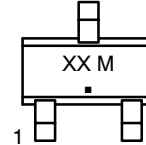
STYLE 5:  
PIN 1. GATE  
2. SOURCE  
3. DRAIN

**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
C	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.061	0.063	0.065
E	0.70	0.80	0.90	0.027	0.031	0.035
e	1.00 BSC			0.04 BSC		
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.060	0.063	0.067

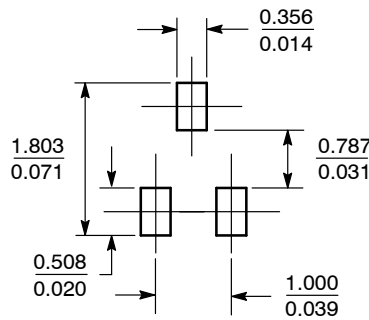
**GENERIC MARKING DIAGRAM\***



- XX = Specific Device Code
- M = Date Code
- = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present.

**SOLDERING FOOTPRINT\***




SCALE 10:1 (mm/inches)

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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