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April 1st, 2010 Renesas Electronics Corporation

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RENESAS

HD74LV2GT157A

2-channle Multiplexer / CMOS Logic Level Shifter

REJ03D0150-0200Z (Previous ADE-205-686A (Z)) Rev.2.00 Oct.23.2003

Description

The HD74LV2GT157A has 2–channel multiplexer in 8 pin package. The input protection circuitry on this device allows over voltage tolerance on the input, allowing the device to be used as a logic–level translator from 3.0 V CMOS Logic to 5.0 V CMOS Logic or from 1.8 V CMOS logic to 3.0 V CMOS Logic while operating at the high-voltage power supply. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

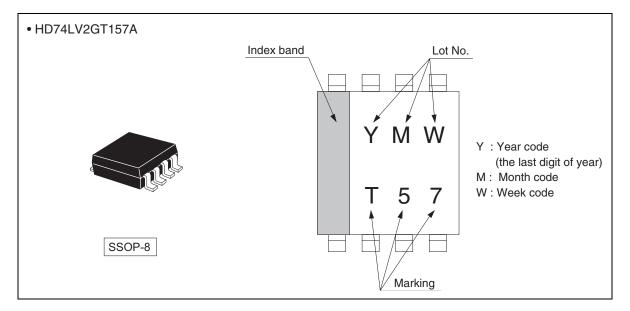
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- TTL compatible input level.
 Supply voltage range : 3.0 to 5.5 V
 Operating temperature range : -40 to +85°C
- Logic-level translate function
 3.0 V CMOS logic → 5.0 V CMOS logic (@V_{CC} = 5.0 V)
 1.8 V or 2.5 V CMOS logic → 3.3 V CMOS logic (@V_{CC} = 3.3 V)
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_O (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current $\pm 6 \text{ mA}$ (@V_{CC} = 3.0 V to 3.6 V), $\pm 12 \text{ mA}$ (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV2GT157AUSE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs/reel)



Outline and Article Indication



Function Table

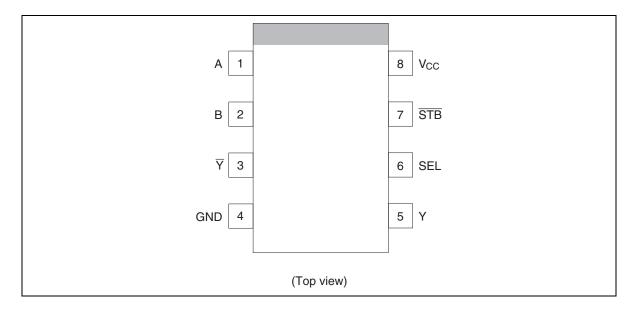
Inputs				Outputs		
STB	SEL	Α	В	Y	Ϋ́	
Н	Х	Х	Х	L	Н	
L	L	L	Х	L	Н	
L	L	Н	Х	Н	L	
L	Н	Х	L	L	Н	
L	Н	x	Н	Н	L	

H : High level

L : Low level

X : Immaterial

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	–0.5 to V _{CC} + 0.5	V	Output : H or L
		-0.5 to 7.0		V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	l _{ок}	±50	mA	$V_{\rm O}$ < 0 or $V_{\rm O}$ > $V_{\rm CC}$
Continuous output current	lo	±25	mA	$V_{O} = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

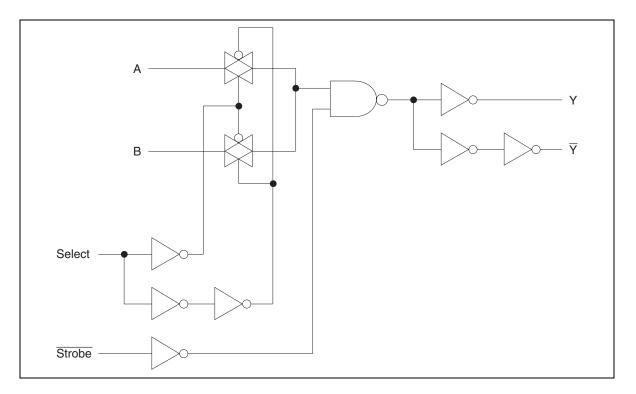


Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	3.0	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
Output current	I _{OL}	_	6	mA	V_{CC} = 3.0 to 3.6 V
		_	12		V_{CC} = 4.5 to 5.5 V
	I _{OH}	_	-6		V_{CC} = 3.0 to 3.6 V
		_	-12		V_{CC} = 4.5 to 5.5 V
Input transition rise or fall rate	Δt / Δv	0	100	ns / V	V_{CC} = 3.0 to 3.6 V
		0	20		V_{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Recommended Operating Conditions

Note: Unused or floating inputs must be held high or low.

Logic Diagram



Electrical Characteristic

• Ta = -40 to $85^{\circ}C$

Item	Symb	ol V _{cc} (V) *	Min	Тур	Мах	Unit	Test condition
Input voltage	V _{IH}	3.0 to 3.6	1.5	_	_	V	
		4.5 to 5.5	2.0	—	_		
	V _{IL}	3.0 to 3.6	_	_	0.6		
		4.5 to 5.5	_	_	0.8		
Hysteresis voltage	V _H	3.3	_	0.10	_	V	$V_T^+ - V_T^-$
		5.0	_	0.15	_		
Output voltage	V _{OH}	Min to Max	V _{CC} -0.1	_	_	V	I _{OH} = -50 μA
		3.0	2.48	_	_		I _{OH} = -6 mA
		4.5	3.8	_	_		I _{OH} = -12 mA
	V _{OL}	Min to Max	_	_	0.1		I _{OL} = 50 μA
		3.0	_	_	0.44		I _{OL} = 6 mA
		4.5	_	_	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	Icc	5.5	_	—	10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
	ΔI_{CC}	5.5	_	—	1.5	mA	One input $V_{IN} = 3.4 V$, other input V_{CC} or GND
Output leakage current	I _{OFF}	0	_	_	5	μA	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	CIN	5.0	—	3.0	_	pF	$V_{IN} = V_{CC} \text{ or } GND$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



Switching Characteristics

• $V_{CC} = 3.3 \pm 0.3 V$

		Ta = 2	25°C		Ta = −40 to 85°C		o 85°C Test		FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	7.5	11.5	1.0	13.0	ns	$C_L = 15 \text{ pF}$	A or B	Y
delay time	t _{PHL}	_	10.0	15.0	1.0	17.0	_	$C_L = 50 \text{ pF}$	-	
		—	10.0	15.0	1.0	17.5		$C_L = 15 \text{ pF}$	SEL	Y
		_	12.5	18.5	1.0	21.0		$C_L = 50 \text{ pF}$	_	
		_	8.5	14.0	1.0	16.0	_	$C_L = 15 \text{ pF}$	STB	Y
		_	11.0	17.5	1.0	19.5		$C_L = 50 \text{ pF}$	-	

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

		Ta = 2	25°C		Ta = -40 to 85°C			Test	FROM	то
ltem	Symbol	Min	Тур	Мах	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.1	7.4	1.0	8.5	ns	$C_L = 15 \text{ pF}$	A or B	Y
delay time	t _{PHL}	_	6.6	9.4	1.0	10.5	_	$C_L = 50 \text{ pF}$	-	
		_	6.3	9.1	1.0	10.5		$C_L = 15 \text{ pF}$	SEL	Y
		_	7.8	11.1	1.0	12.5		$C_L = 50 \text{ pF}$	_	
		_	5.6	8.6	1.0	10.0		$C_L = 15 \text{ pF}$	STB	Y
		_	7.1	10.6	1.0	12.0		$C_L = 50 \text{ pF}$	-	

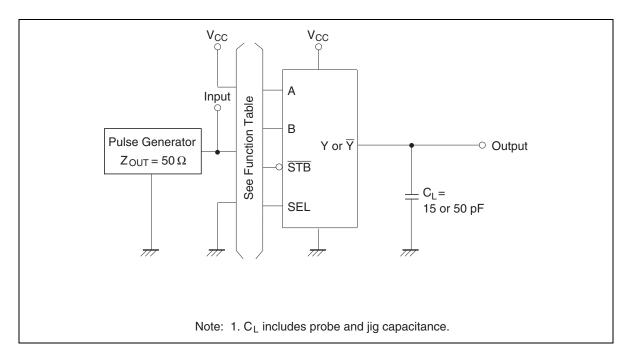
Operating Characteristics

• $C_L = 50 \text{ pF}$

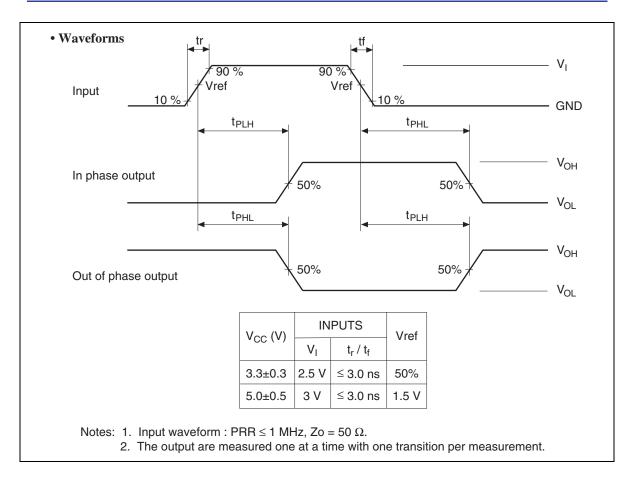
			Ta = 2	5°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	C _{PD}	5.0	_	35.0	—	pF	f = 10 MHz



Test Circuit

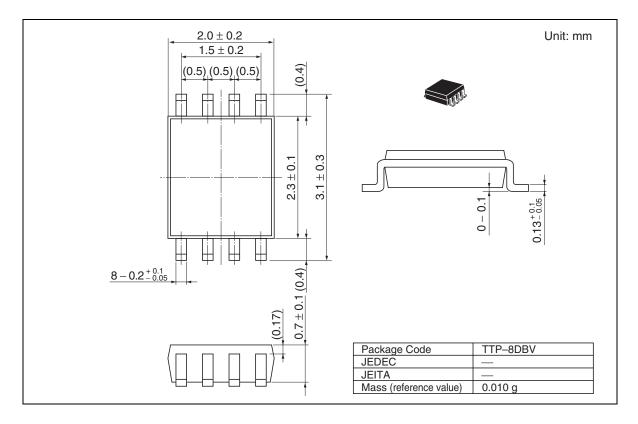








Package Dimensions





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