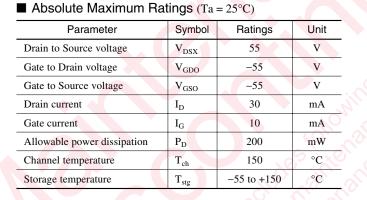
2SK0374 (2SK374)

Silicon N-Channel Junction FET

For low-frequency amplification For switching

Features

- Low noise-figure (NF)
- High gate to drain voltage V_{GDO}
- Mini-type package, allowing downsizing of the sets and automatic insertion through the tape/magazine packing.



unit: mm 0.40+0.10 0.16+0.10 $1.50^{+0.25}_{-0.05}$ 2.8+0 2 +¹ (0.95) (0.95) (0.65) 1.9 ± 0.1 2.90+0.20 1.1502 1.150 0 to 0.1 1: Source JEDEC: TO-236 2: Drain EIAJ: SC-59 3: Gate Mini3-G1 Package

Marking Symbol (Example): 2B

Electrical Characteristics (Ta = 25°C)

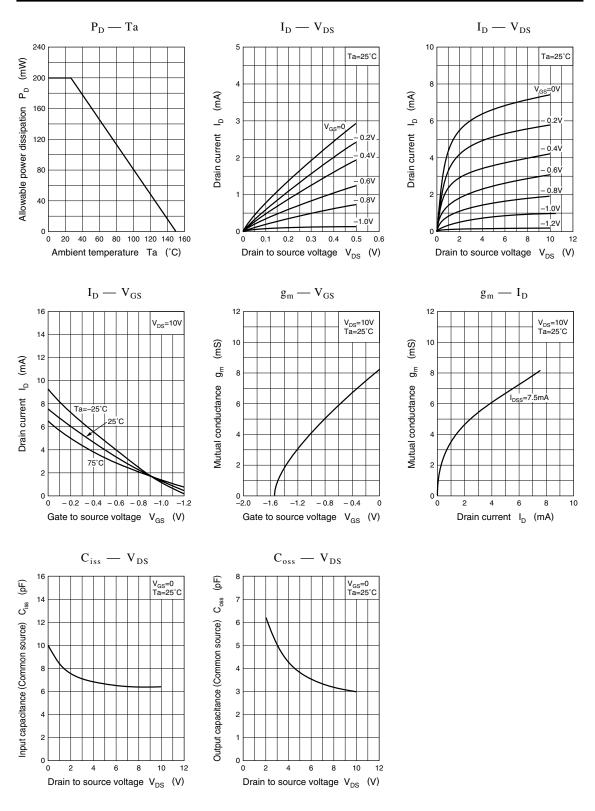
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS} *	$V_{DS} = 10V, V_{GS} = 0$		0	20	mA
Gate to Source leakage current	I _{GSS}	$V_{GS} = -30V, V_{DS} = 0$	s and		-10	nA
Gate to Drain voltage	V _{GDC}	$I_{\rm G} = -100 \mu A, V_{\rm DS} = 0$	-55	-80		V
Gate to Source cut-off voltage	V _{GSC}	$V_{DS} = 10V, I_D = 10\mu A$			-5	V
Mutual conductance	g _m	$V_{DS} = 10V, I_D = 5mA, f = 1kHz$	2.5	7.5		mS
Input capacitance (Common Source)	C _{iss}	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$		6.5		pF
Reverse transfer capacitance (Common Source)	C _{rss}	$v_{DS} = 10v, v_{GS} = 0, 1 = 100HZ$		1.9		pF
Noise figure	NF	$V_{DS} = 10V, V_{GS} = 0, R_g = 100k\Omega$ $f = 100Hz$		2.5		dB

* I_{DSS} rank classification

Runk	Р	Q	R	S
I _{DSS} (mA)	1 to 3	2 to 6.5	5 to 12	10 to 20
Marking Symbol	2BP	2BQ	2BR	2BS

Note) The part number in the parenthesis shows conventional part number.

Silicon Junction FETs (Small Signal)



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