TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOS VI)

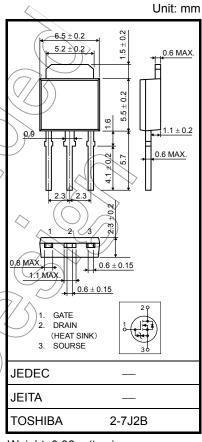
2SK4003

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON-resistance: $R_{DS (ON)} = 1.7 \Omega (typ.)$
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 600 V)
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
Drain-source voltage			V_{DSS}	600	$(\checkmark \checkmark))$
Drain-gate voltage (R _{GS} = 20 kΩ)			V_{DGR}	600	A
Gate-source voltage			V_{GSS}	±30	V
Drain current	DC	(Note 1)	ΙD	3	
	Pulse	(Note 1)	I _{DP}	12	Α
Drain power dissipation (Tc = 25°C)			P _D	20	W
Single-pulse avalanche energy (Note 2)			EAS	168	mJ
Avalanche current			IAR (3	A
Repetitive avalanche energy (Note 3)			EAR	2	mJ
Channel temperature			((T _{ch}))	150	//°C
Storage temperature range			T _{stg}	-55 to 150	J.C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristic Symbol	Max	Unit
Thermal resistance, channel to ambient Rth (ch-a)	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 8.2 mH, R_G = 25 Ω , I_{AR} = 6 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Electrical Characteristics (Ta = 25°C)

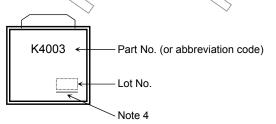
Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cutoff curr	rent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	/	_	100	μΑ
Drain-source b	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) >-	4.0	V
Drain-source O	N-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 1.5 A)	1.7	2.2	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1.5 A	0.5	2.0	_	S
Input capacitance		C _{iss}			600	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	7	_	pF
Output capacitance		Coss		_	60	_	
Switching time	Rise time	t _r	10 V p=1.5 A VOUT	- (16	>	
	Turn-on time	t _{on}	V _{GS} ₀ V R _L =		40)) _	
	Fall time	t _f	133Ω V _{DD} ≈ 200 V		18	_	ns
	Turn-off time	t _{off}	Duty ≤ 1%, t _w = 10 μs) –	80	_	
Total gate charged	ge (gate-source)	Qg		_	15	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 3 \text{ A}$	_	9	_	nC
Gate-drain ("Miller") charge		Qgd		_	6	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	-	_	3	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	-	_	12	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 3 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 3 A, V _{GS} = 0 V	-	800	_	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs	_	5	_	μС

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Note 4: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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