

Rail-to-Rail Input/Output Dual Operational Amplifier

■ FEATURES

- Operating Voltage 1.8 to 6.0V
- Rail-to-Rail Input $V_{ICM} = 0$ to 5.0V, (at $V^+ = 5V$)
- Rail-to-Rail Output $V_{OH} \geq 4.9V / V_{OL} \leq 0.1V$, (at $V^+ = 5V, R_L = 20k\Omega$)
- Load Drivability $V_{OH} \geq 4.75V / V_{OL} \leq 0.25V$, (at $V^+ = 5V, R_L = 2k\Omega$)
- Offset Voltage 5mV max. ($T_a = 25^\circ C$)
10mV max. ($T_a = -40^\circ C$ to $+105^\circ C$)
- Slew Rate 0.4V/ μs typ.
- Low Input Voltage Noise 10nV/ \sqrt{Hz} typ. (at $f = 1kHz$)
- Adequate phase margin $\Phi_M = 75deg.$ typ., (at $R_L = 2k\Omega$, voltage follower)
- Bipolar Technology
- Package Outline DMP8, MSOP8 (TVSP8) MEET JEDEC MO-187-DA/THIN TYPE
- AEC-Q100 This product meets the reliability level required by AEC-Q100.

■ PACKAGE OUTLINE



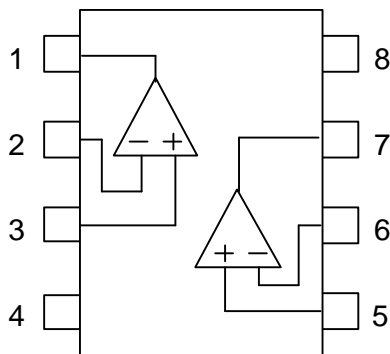
NJM2732M-T
(DMP8)



NJM2732RB1-T
(MSOP8 (TVSP8))

■ PIN CONFIGURATION

(Top View)



PIN FUNCTION

- 1. A OUTPUT
- 2. A -INPUT
- 3. A +INPUT
- 4. GND
- 5. B +INPUT
- 6. B -INPUT
- 7. B OUTPUT
- 8. V^+

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NJM2732RB1-T

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■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	7.0	V
Differential Input Voltage Range	V _{ID}	±1.0	V
Common Mode Input Voltage Range	V _{IC}	0 to 7.0 (Note1)	V
Power Dissipation	P _D	DMP8	380 (Note2)
		MSOP8 (TVSP8)	410 (Note2)
Operating Temperature Range	T _{opr}	-40 to +105	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C

(Note1) For supply voltage less than 7V, the absolute maximum input voltage is equal to the supply voltage.

(Note2) On the PCB "EIA/JEDEC (114.3x76.2x1.6mm, 2 layers, FR-4)".

■ RECOMMENDED OPERATING CONDITION

(Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V ⁺	1.8 to 6.0	V

■ ELECTRICAL CHARACTERISTICS (V⁺=5V, Ta=25°C)

● DC CHARACTERISTICS

(V⁺=5V, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I _{CC}	No signal applied	-	580	900	μA
		No signal applied, Ta=-40 °C to +105 °C	-	-	1000	
Input Offset Voltage	V _{IO}		-	1	5	mV
		Ta=-40 °C to +105 °C	-	-	10	
Input Bias Current	I _B		-	50	250	nA
		Ta=-40 °C to +105 °C	-	-	275	
Input Offset Current	I _{IO}		-	5	100	nA
		Ta=-40 °C to +105 °C	-	-	100	
Large Signal Voltage Gain	A _V	R _L =2kΩ	60	85	-	dB
		R _L =2kΩ, Ta=-40 °C to +105 °C	58	-	-	
Common Mode Rejection Ratio	CMR	CMR+: 2.5V ≤ V _{CM} ≤ 5V CMR-: 0V ≤ V _{CM} ≤ 2.5V (Note3)	55	70	-	dB
		CMR+: 2.5V ≤ V _{CM} ≤ 5V CMR-: 0V ≤ V _{CM} ≤ 2.5V (Note3), Ta=-40 °C to +105 °C	55	-	-	
Supply Voltage Rejection Ratio	SVR	V ⁺ /V ⁻ = ±2.0V to ±3.0V	70	85	-	dB
		V ⁺ /V ⁻ = ±2.0V to ±3.0V, Ta=-40 °C to +105 °C	70	-	-	
Maximum Output Voltage 1	V _{OH1}	R _L =20kΩ	4.9	4.95	-	V
		R _L =20kΩ, Ta=-40 °C to +105 °C	4.85	-	-	
	V _{OL1}	R _L =20kΩ	-	0.05	0.10	V
		R _L =20kΩ, Ta=-40 °C to +105 °C	-	-	0.15	
Maximum Output Voltage 2	V _{OH2}	R _L =2kΩ	4.75	4.85	-	V
		R _L =2kΩ, Ta=-40 °C to +105 °C	4.70	-	-	
	V _{OL2}	R _L =2kΩ	-	0.15	0.25	V
		R _L =2kΩ, Ta=-40 °C to +105 °C	-	-	0.30	
Input Common Mode Voltage Range	V _{ICM}	CMR ≥ 55dB	0	-	5	V
		CMR ≥ 55dB, Ta=-40 °C to +105 °C	0	-	5	

(Note3) CMR is represented by either CMR+ or CMR- has lower value.

CMR+ is measured with 2.5V ≤ V_{CM} ≤ 5.0 and CMR- is measured with 0V ≤ V_{CM} ≤ 2.5V.

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●AC CHARACTERISTICS

($V^+ = 5V$, $T_a = 25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain Bandwidth	GB	$R_L = 2k\Omega$	-	1	-	MHz
Phase Margin	Φ_M	$R_L = 2k\Omega$	-	75	-	Deg
Equivalent Input Noise Voltage	V_{NI}	$f = 1kHz$	-	10	-	nV/ \sqrt{Hz}

●TRANSIENT CHARACTERISTICS

($V^+ = 5V$, $T_a = 25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	$R_L = 2k\Omega$	-	0.4	-	V/ μs

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■ ELECTRICAL CHARACTERISTICS ($V^+=3V$, $T_a=25^\circ C$)

●DC CHARACTERISTICS

($V^+=3V$, $T_a=25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I_{CC}	No signal applied	-	510	880	μA
		No signal applied, $T_a=-40^\circ C$ to $+105^\circ C$	-	-	980	
Input Offset Voltage	V_{IO}		-	1	5	mV
		$T_a=-40^\circ C$ to $+105^\circ C$	-	-	10	
Input Bias Current	I_B		-	50	250	nA
		$T_a=-40^\circ C$ to $+105^\circ C$	-	-	275	
Input Offset Current	I_{IO}		-	5	100	nA
		$T_a=-40^\circ C$ to $+105^\circ C$	-	-	100	
Large Signal Voltage Gain	A_V	$R_L=2k\Omega$	60	84	-	dB
		$R_L=2k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	58	-	-	
Common Mode Rejection Ratio	CMR	CMR+: $1.5V \leq V_{CM} \leq 3V$ CMR-: $0V \leq V_{CM} \leq 1.5V$ (Note4)	48	63	-	dB
		CMR+: $1.5V \leq V_{CM} \leq 3V$ CMR-: $0V \leq V_{CM} \leq 1.5V$ (Note4) $T_a=-40^\circ C$ to $+105^\circ C$	48	-	-	
Supply Voltage Rejection Ratio	SVR	$V^+V^- = \pm 1.2V$ to $\pm 2.0V$	68	83	-	dB
		$V^+V^- = \pm 1.2V$ to $\pm 2.0V$, $T_a=-40^\circ C$ to $+105^\circ C$	68	-	-	
Maximum Output Voltage 1	V_{OH1}	$R_L=20k\Omega$	2.90	2.95	-	V
		$R_L=20k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	2.85	-	-	
	V_{OL1}	$R_L=20k\Omega$	-	0.05	0.10	V
		$R_L=20k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	-	-	0.15	
Maximum Output Voltage 2	V_{OH2}	$R_L=2k\Omega$	2.75	2.85	-	V
		$R_L=2k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	2.70	-	-	
	V_{OL2}	$R_L=2k\Omega$	-	0.15	0.25	V
		$R_L=2k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	-	-	0.30	
Input Common Mode Voltage Range	V_{ICM}	CMR \geq 48dB	0	-	3	V
		CMR \geq 48dB, $T_a=-40^\circ C$ to $+105^\circ C$	0	-	3	

(Note4) CMR is represented by either CMR+ or CMR-has lower value.

CMR+ is measured with $1.5V \leq V_{CM} \leq 3.0$ and CMR- is measured with $0V \leq V_{CM} \leq 1.5V$.

●AC CHARACTERISTICS

($V^+=3V$, $T_a=25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain Bandwidth	GB	$R_L=2k\Omega$	-	1	-	MHz
Phase Margin	Φ_M	$R_L=2k\Omega$	-	75	-	Deg
Equivalent Input Noise Voltage	V_{NI}	$f=1kHz$	-	10	-	nV/ \sqrt{Hz}

●TRANSIENT CHARACTERISTICS

($V^+=3V$, $T_a=25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	$R_L=2k\Omega$	-	0.35	-	V/ μs

■ ELECTRICAL CHARACTERISTICS ($V^+=1.8V$, $T_a=25^\circ C$)

●DC CHARACTERISTICS

($V^+=1.8V$, $T_a=25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I_{CC}	No signal applied	-	460	800	μA
		No signal applied, $T_a=-40^\circ C$ to $+105^\circ C$	-	-	900	
Input Offset Voltage	V_{IO}		-	1	5	mV
		$T_a=-40^\circ C$ to $+105^\circ C$	-	-	10	
Input Bias Current	I_B		-	50	250	nA
		$T_a=-40^\circ C$ to $+105^\circ C$	-	-	275	
Input Offset Current	I_{IO}		-	5	100	nA
		$T_a=-40^\circ C$ to $+105^\circ C$	-	-	100	
Large Signal Voltage Gain	A_V	$R_L=2k\Omega$	60	83	-	dB
		$R_L=2k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	50	-	-	
Common Mode Rejection Ratio	CMR	CMR+: $0.9V \leq V_{CM} \leq 1.8V$ CMR-: $0V \leq V_{CM} \leq 0.9V$ (Note5)	40	55	-	dB
		CMR+: $0.9V \leq V_{CM} \leq 1.8V$ CMR-: $0V \leq V_{CM} \leq 0.9V$ (Note5), $T_a=-40^\circ C$ to $+105^\circ C$	40	-	-	
Supply Voltage Rejection Ratio	SVR	$V^+/V^- = \pm 1.2V$ to $\pm 2.0V$	65	80	-	dB
		$V^+/V^- = \pm 1.2V$ to $\pm 2.0V$ $T_a=-40^\circ C$ to $+105^\circ C$	55	-	-	
Maximum Output Voltage 1	V_{OH1}	$R_L=20k\Omega$	1.70	1.75	-	V
		$R_L=20k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	1.65	-	-	
	V_{OL1}	$R_L=20k\Omega$	-	0.05	0.10	V
		$R_L=20k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	-	-	0.15	
Maximum Output Voltage 2	V_{OH2}	$R_L=2k\Omega$	1.55	1.65	-	V
		$R_L=2k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	1.50	-	-	
	V_{OL2}	$R_L=2k\Omega$	-	0.15	0.25	V
		$R_L=2k\Omega$, $T_a=-40^\circ C$ to $+105^\circ C$	-	-	0.30	
Input Common Mode Voltage Range	V_{ICM}	CMR $\geq 40dB$	0	-	1.8	V
		CMR $\geq 40dB$, $T_a=-40^\circ C$ to $+105^\circ C$	0	-	1.8	

(Note5) CMR is represented by either CMR+ or CMR- has lower value.

CMR+ is measured with $0.9V \leq V_{CM} \leq 1.8$ and CMR- is measured with $0V \leq V_{CM} \leq 0.9V$.

●AC CHARACTERISTICS

($V^+=1.8V$, $T_a=25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain Bandwidth	GB	$R_L=2k\Omega$	-	1	-	MHz
Phase Margin	Φ_M	$R_L=2k\Omega$	-	75	-	Deg
Equivalent Input Noise Voltage	V_{NI}	$f=1kHz$	-	10	-	nV/ \sqrt{Hz}

●TRANSIENT CHARACTERISTICS

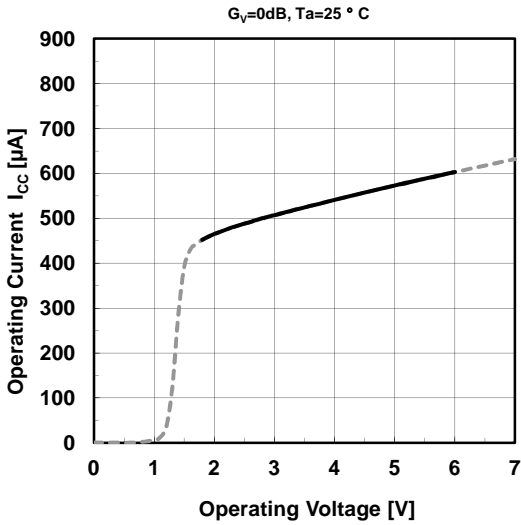
($V^+=1.8V$, $T_a=25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	$R_L=2k\Omega$	-	0.3	-	V/ μs

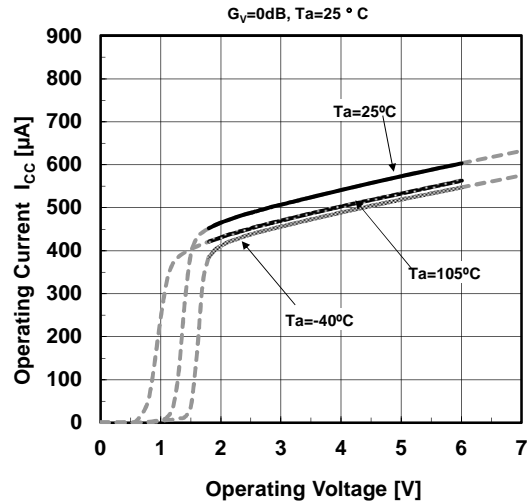
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■ TYPICAL CHARACTERISTICS

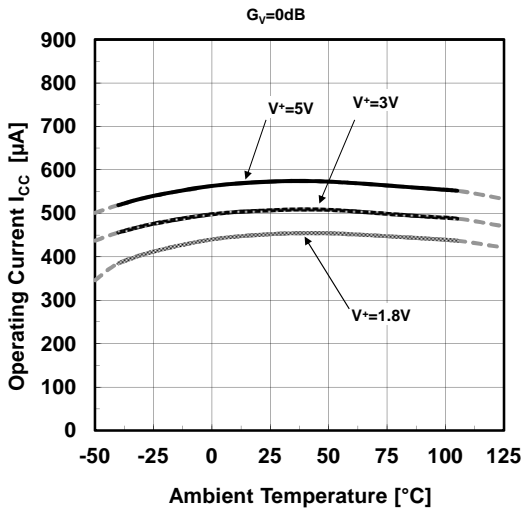
Operating Current vs. Operating Voltage



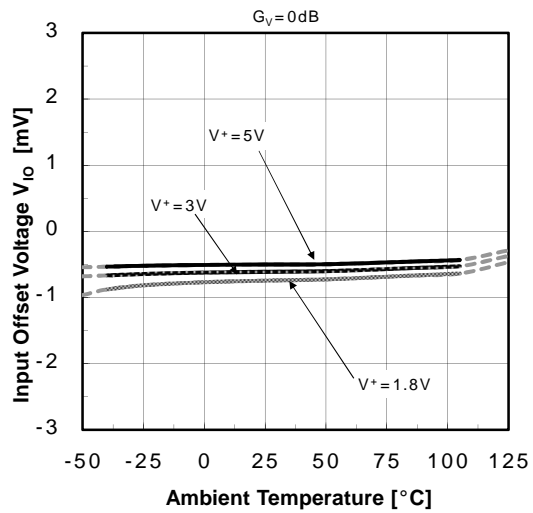
Operating Current vs. Operating Voltage (correlation with T_a)



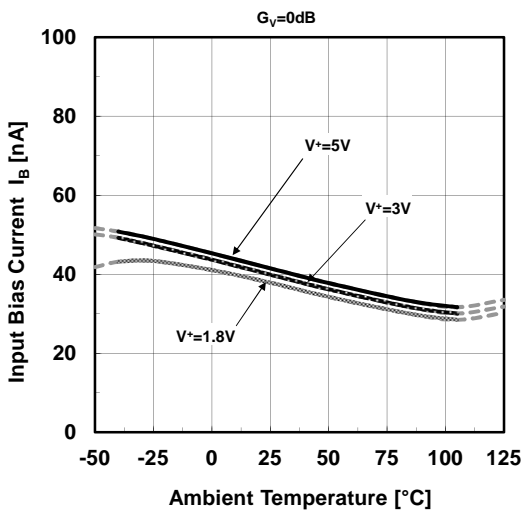
Operating Current vs. Ambient Temperature



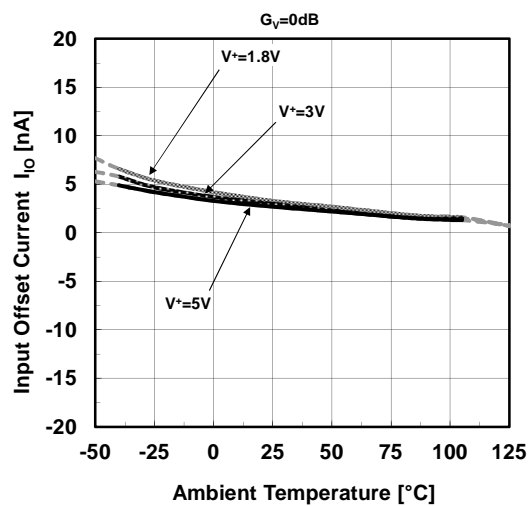
Input Offset Voltage vs. Ambient Temperature



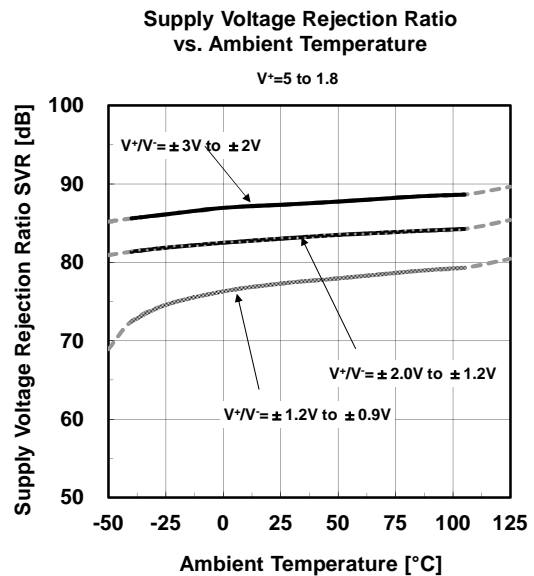
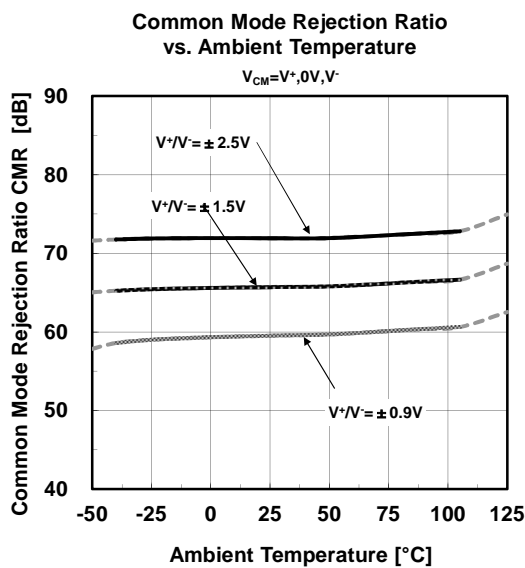
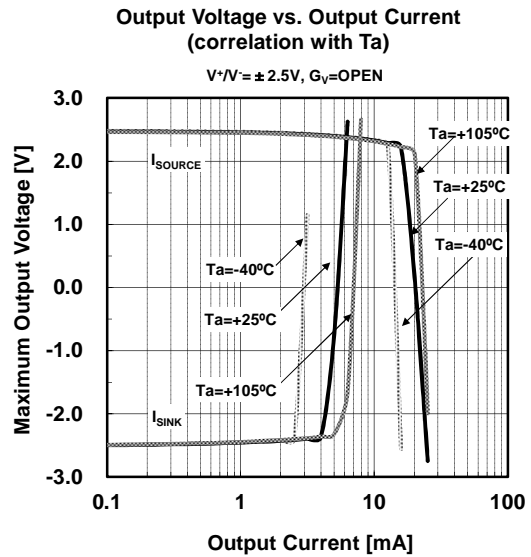
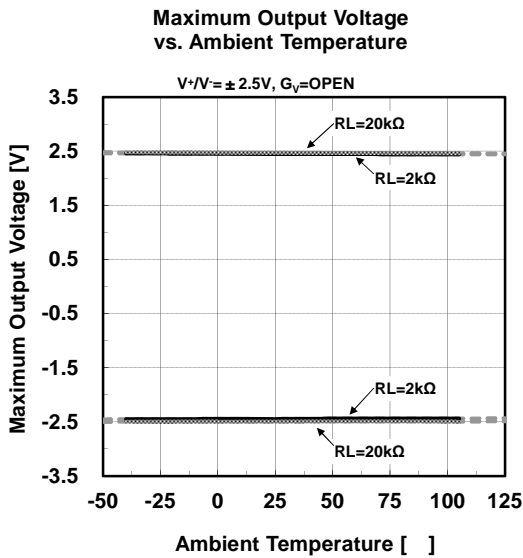
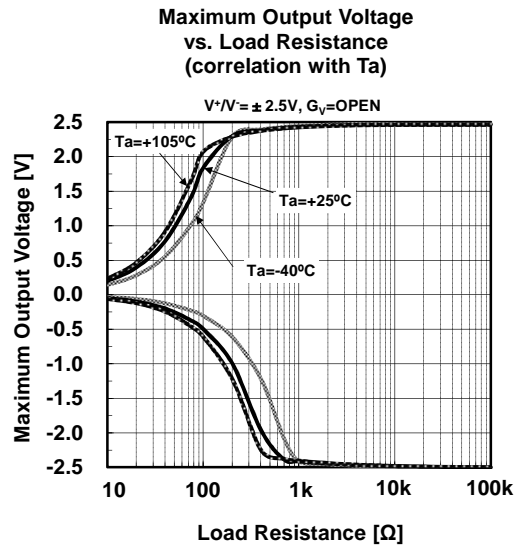
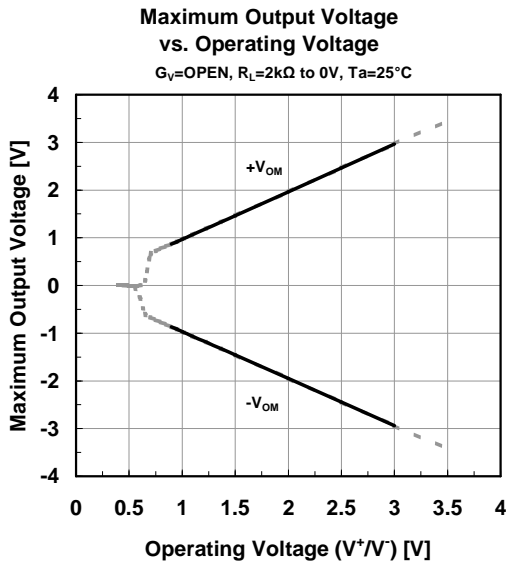
Input Bias Current vs. Ambient Temperature



Input Offset Current vs. Ambient Temperature



■ TYPICAL CHARACTERISTICS

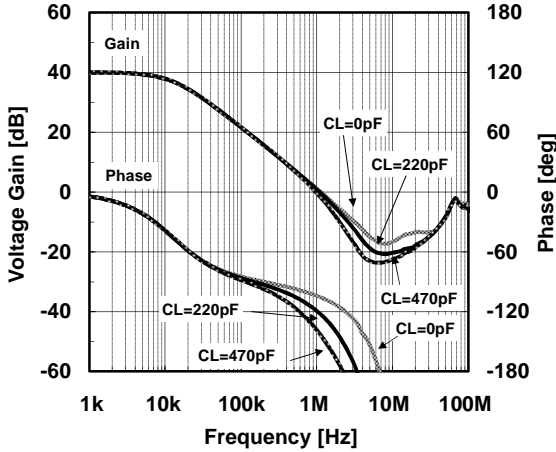


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■ TYPICAL CHARACTERISTICS

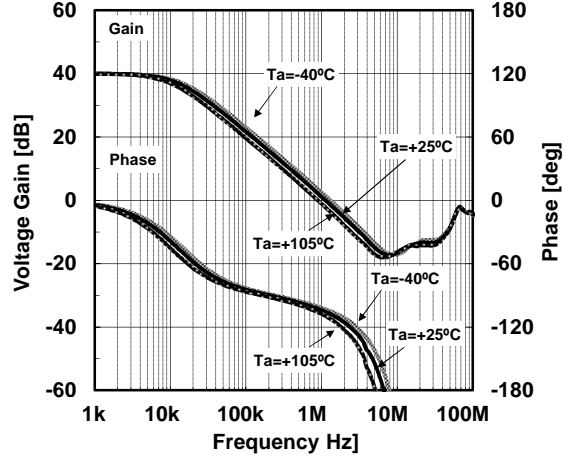
Voltage Gain/Phase vs. Frequency
(with Capacitive load)

$V^+/V^- = \pm 2.5V$, $G_v = 40dB$, $R_F = 2k\Omega$,
 $R_G = 20\Omega$, $R_L = 2k\Omega$, $T_a = +25^\circ C$



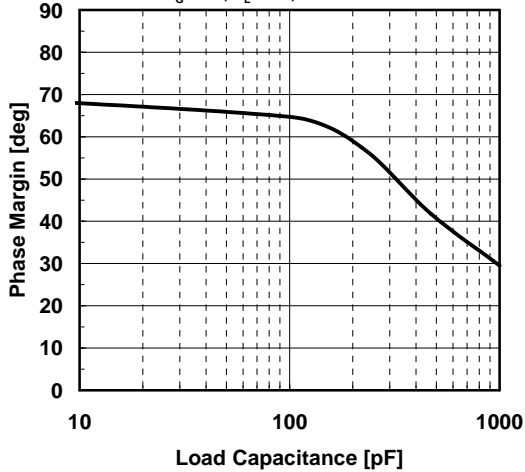
Voltage Gain/Phase vs. Frequency
(correlation with T_a)

$V^+/V^- = \pm 2.5V$, $G_v = 40dB$,
 $R_F = 2k\Omega$, $R_G = 20\Omega$, $R_L = 2k\Omega$, $C_L = 0pF$



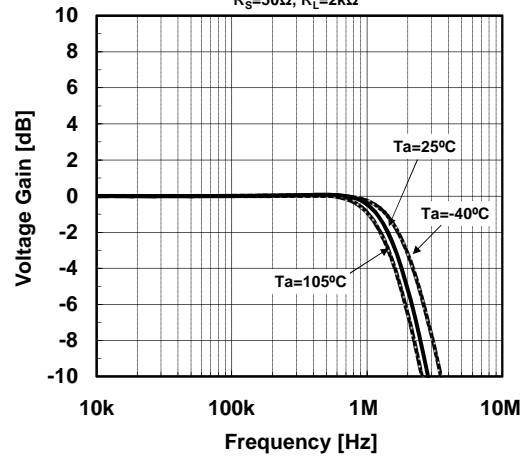
Phase Margin vs. Load Capacitance

$V^+/V^- = \pm 1.5V$, $G_v = 40dB$, $R_F = 2k\Omega$,
 $R_G = 20\Omega$, $R_L = 2k\Omega$, $T_a = +25^\circ C$



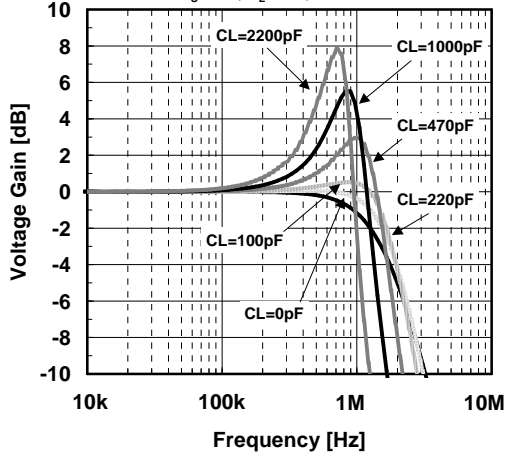
Voltage Gain vs. Frequency
(correlation with T_a)

$V^+/V^- = \pm 2.5V$, $G_v = 0dB$, $C_L = 100pF$,
 $R_G = 50\Omega$, $R_L = 2k\Omega$



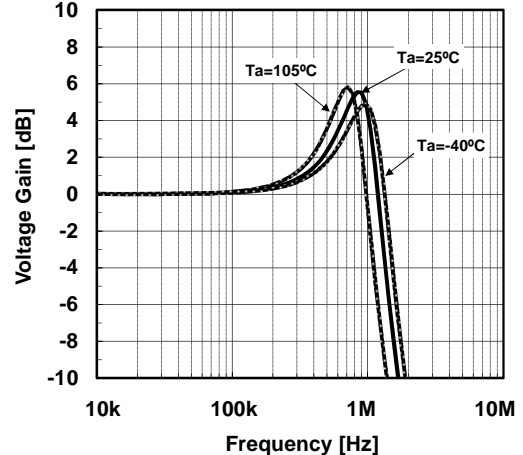
Voltage Gain vs. Frequency

$V^+/V^- = \pm 2.5V$, $G_v = 0dB$,
 $R_G = 50\Omega$, $R_L = 2k\Omega$, $T_a = +25^\circ C$

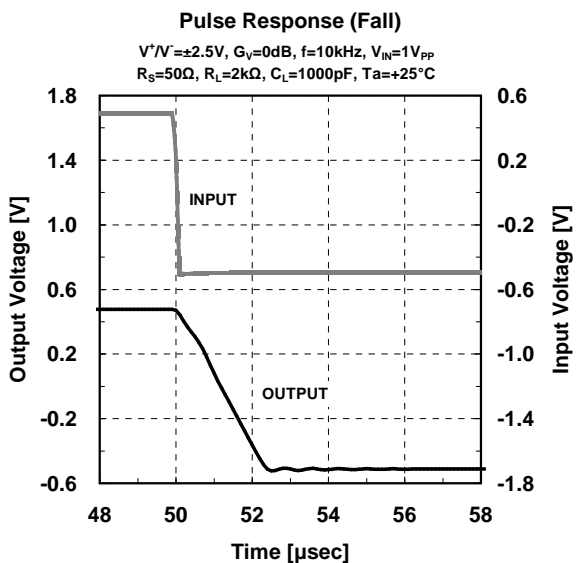
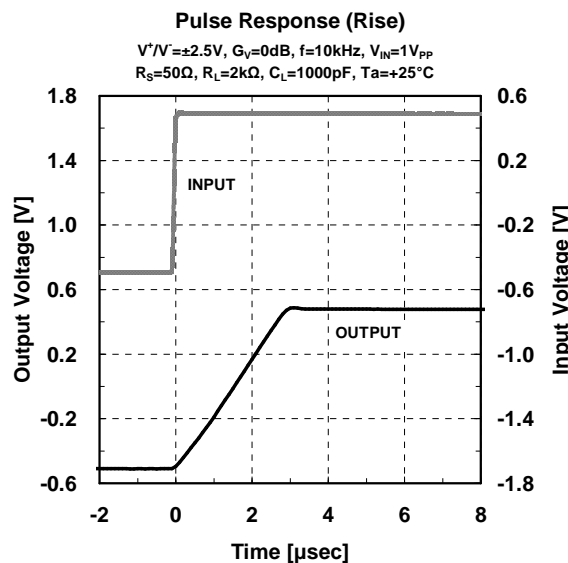
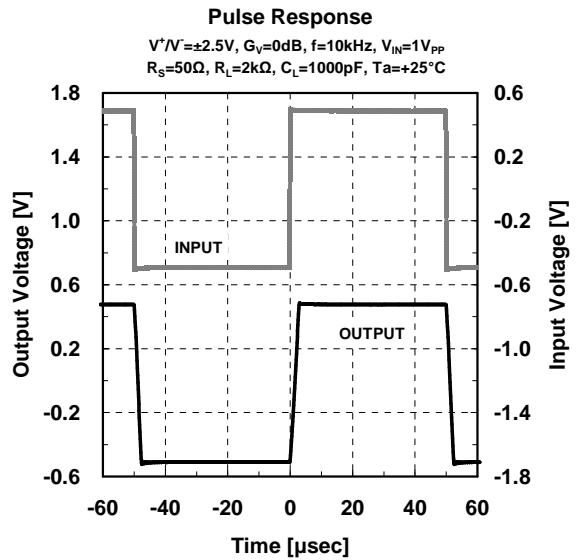
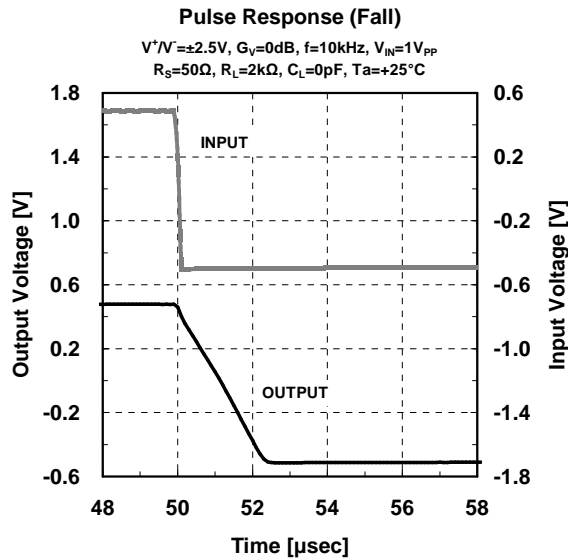
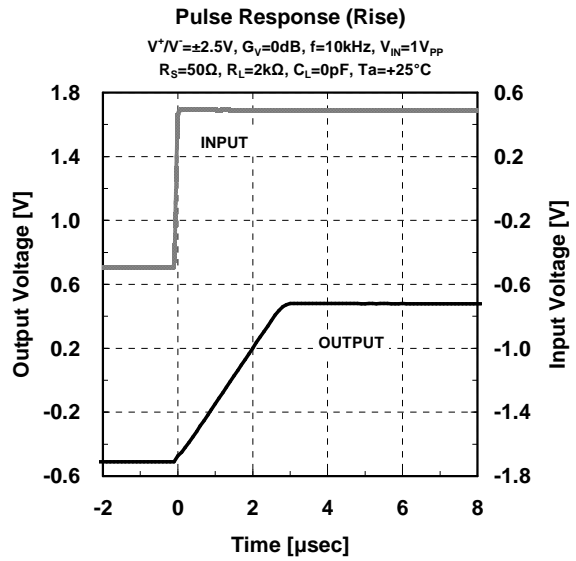
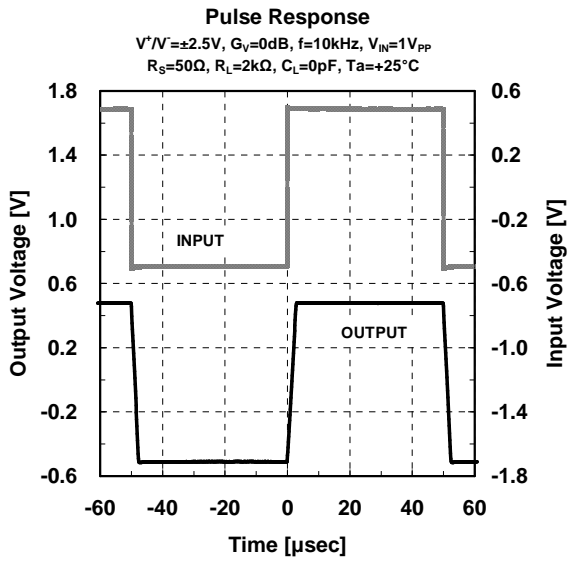


Voltage Gain vs. Frequency
(correlation with T_a)

$V^+/V^- = \pm 2.5V$, $G_v = 0dB$, $C_L = 1000pF$,
 $R_G = 50\Omega$, $R_L = 2k\Omega$

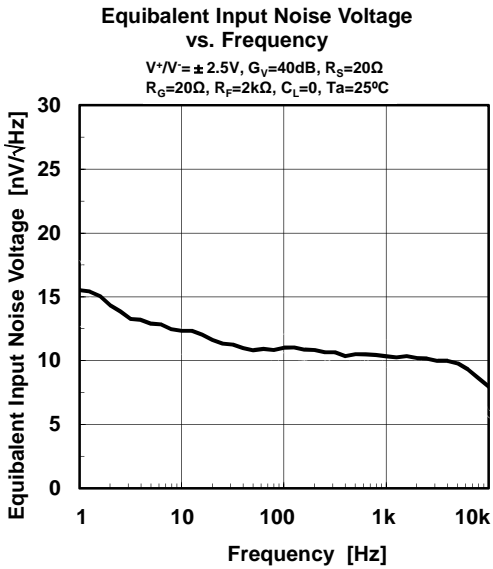
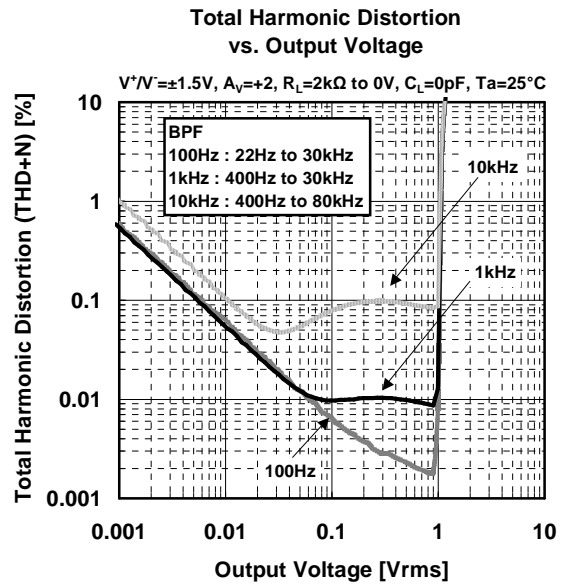
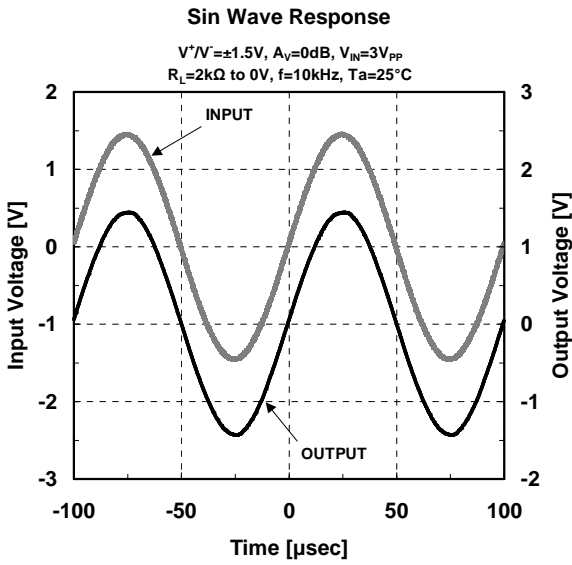


■ TYPICAL CHARACTERISTICS



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■ TYPICAL CHARACTERISTICS



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