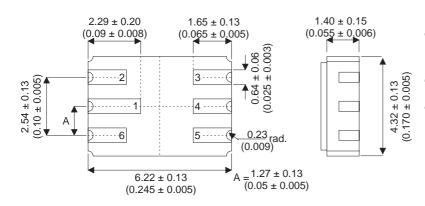




## **MECHANICAL DATA** Dimensions in mm (inches)

# **DUAL HIGH GAIN** PNP TRANSISTORS IN A HERMETICALLY SEALED CERAMIC SURFACE MOUNT PACKAGE FOR HIGH RELIABILITY APPLICATIONS



### **FEATURES**

- HERMETIC CERAMIC SURFACE MOUNT PACKAGE
- CECC SCREENING OPTIONS
- SPACE QUALITY LEVELS OPTIONS

## LCC2 PACKAGE **Underside View**

PAD 1 - Collector 1 PAD 4 - Collector 2 PAD 2 - Base 1 PAD 5 - Emitter 2 PAD 3 - Base 2 PAD 6 - Emitter 1

### **APPLICATIONS:**

Suitable for use in high gain, low noise differential amplifier applications.

### ABSOLUTE MAXIMUM RATINGS

	(T <sub>amb</sub> = 25°C unless otherwise stated)	EACH SIDE	TOTAL DEVICE	
$V_{CBO}$	Collector – Base Voltage	–60V		
$V_{CEO}$	Collector – Emitter Voltage	–60V		
$V_{EBO}$	Emitter – Base Voltage	–5V		
I <sub>C</sub>	Collector Current	–50mA		
$P_{D}$	Total Device Dissipation	500mW	600mW	
	Derate above 25°C	2.9mW / °C	3.4mW / °C	
T <sub>STG</sub>	Storage Temperature Range	−65 to 200°C		

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Issue 1



# 2N3811DCSM

# **ELECTRICAL CHARACTERISTICS** (T<sub>amb</sub> = 25°C unless otherwise stated)

Parameter		Test Conditions		Min.	Тур.	Max.	Unit		
INDIVIDUAL TRANSISTOR CHARACTERISTICS									
V <sub>(BR)CBO</sub>	Collector – Base Breakdown Voltage	$I_C = -10\mu A$	I <sub>E</sub> = 0	-60					
V <sub>(BR)CEO*</sub>	Collector – Emitter Breakdown Voltage	$I_C = -10 \text{mA}$	I <sub>B</sub> = 0	-60			V		
V <sub>(BR)EBO</sub>	Emitter – Base Breakdown Voltage	$I_{E} = -10 \mu A$	I <sub>C</sub> = 0	<b>-</b> 5					
1	Collector Cut-off Current	$V_{CB} = -50V$	I <sub>E</sub> = 0			-10	nA		
I <sub>CBO</sub>			T <sub>A</sub> = 150°C			-10	μΑ		
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -4V$	I <sub>C</sub> = 0			-20	nA		
		$I_{C} = -10 \mu A$	$V_{CE} = -5V$	225					
		$I_{C} = -100 \mu A$	$V_{CE} = -5V$	300		900			
h	DC Current Gain		$T_A = -55$ °C	150			_		
h <sub>FE</sub>	DC Current Gain	$I_{C} = -500 \mu A$	$V_{CE} = -5V$	300		900			
		$I_C = -1 \text{mA}$	$V_{CE} = -5V$	300		900			
		$I_C = -10 \text{mA}$	V <sub>CE</sub> = -5V *	250					
	Base – Emitter Voltage	$I_{C} = -100 \mu A$	$V_{CE} = -5V$			-0.7			
$V_{BE}$		$I_B = -10 \mu A$	$I_{C} = -100 \mu A$			-0.7	V		
		$I_B = -100 \mu A$	$I_C = -1mA$			-0.8			
V <sub>CE(sat)</sub>	Collector – Emitter Saturation Voltage	$I_B = -10\mu A$	$I_{C} = -100 \mu A$			-0.2	V		
		$I_B = -100 \mu A$	$I_C = -1mA$			-0.25	\ \ \		
h <sub>ie</sub>	Small Signal Common – Emitter	10		40	kΩ				
	Input Impedance	V <sub>CE</sub> = -10V		10		40	IN32		
h <sub>fe</sub>	Small Signal Common – Emitter	1 vCE10 v	300		900				
	Current Gain	 		300			900		
h <sub>re</sub>	Small Signal Common – Emitter	$I_C = -1mA$				25 x 10 <sup>-4</sup>			
	Reverse Voltage Gain	f = 1kHz							
h <sub>oe</sub>	Small Signal Common – Emitter	T = TRITZ		5		60	μmho		
	Output Admittance			5		00	μιιιο		
		$V_{CE} = -5V$	$I_{C} = -500 \mu A$	1					
h <sub>fe</sub>	Small Signal Common – Emitter	f = 30MHz		'			_		
	Current Gain	$V_{CE} = -5V$ $I_{C} = -1mA$	$I_C = -1mA$	1	5	5			
		f = 100MHz		'		3			
C <sub>obo</sub>	Common – Base Open Circuit	$V_{CB} = -5V$	I <sub>E</sub> = 0			4			
	Output Capacitance	f = 100kHz				7	pF		
C <sub>ibo</sub>	Common – Base Open Circuit	$V_{EB} = -0.5V$	I <sub>C</sub> = 0			8	"		
	Input Capacitance	f = 100kHz							

#### **NOTES**

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Document Number 5749 Issue 1

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<sup>\*</sup> Pulse Test:  $t_p$  = 300 $\mu$ s,  $\delta$  ≤ 2%. 1) Terminals not under test are open circuited under all test conditions.





# **ELECTRICAL CHARACTERISTICS** (T<sub>amb</sub> = 25°C unless otherwise stated)

Parameter		Test Conditions		Min.	Тур.	Max.	Unit	
TRANSISTOR MATCHING CHARACTERISTICS								
h <sub>FE1</sub>	Static Forward Current Gain	$V_{CE} = -5V$	$I_{C} = -100 \mu A$	0.9		1		
h <sub>FE2</sub>	Balance Ratio	See Note 1.		0.9		ı		
V <sub>BE1</sub> – V <sub>BE2</sub>	Base – Emitter Voltage Differential	$V_{CE} = -5V$				5	mV	
		$I_C = -10\mu A to$	o −10mA					
		V <sub>CE</sub> = -5V	$I_{C} = -100 \mu A$					
IA()/	\AT		$I_{C} = -100 \mu A$			0.8	- mV	
$ \Delta(V_{BE1} - V_{BE2}) $	• • •	T <sub>A1</sub> = 25°C	$T_{A2} = -55^{\circ}C$					
	Base – Emitter Voltage Differential		$I_{C} = -100 \mu A$			1		
		T <sub>A1</sub> = 25°C	T <sub>A2</sub> = 125°C		L			

# **OPERATING CHARACTERISTICS** (T<sub>amb</sub> = 25°C unless otherwise stated)

Parameter		Test Conditions	Min.	Тур.	Max.	Unit	
INDIVIDUAL TRANSISTOR CHARACTERISTICS							
F	Spot Noise Figure	$V_{CE} = -10V$ $I_{C} = -100\mu A$ $R_{G} = 3k\Omega$ $f = 100Hz$ Noise Bandwidth = 20Hz			1.5	dB	
		$V_{CE} = -10V$ $I_{C} = -100\mu A$ $R_{G} = 3k\Omega$ $f = 1kHz$ Noise Bandwidth = 200Hz					
		$V_{CE} = -10V$ $I_{C} = -100\mu A$ $R_{G} = 3k\Omega$ $f = 10kHz$ Noise Bandwidth = 2kHz					
_ F	Average Noise Figure	$V_{CE}=-10V$ $I_{C}=-100\mu A$ $R_{G}=3k\Omega$ Noise Bandwidth = 15.7kHz See Note 2.			2.5	dB	

#### **NOTES**

- 1) The lower of the two readings is taken as h<sub>FF1</sub>
- 2) Average noise figure is measured in an amplifier with response down 3dB at 10Hz and 10 kHz and a high frequency rolloff of 6dB / octave.

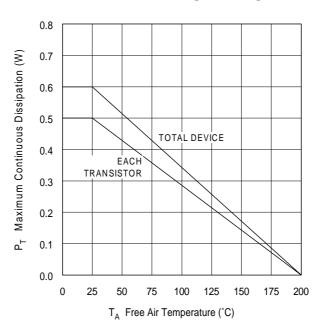
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