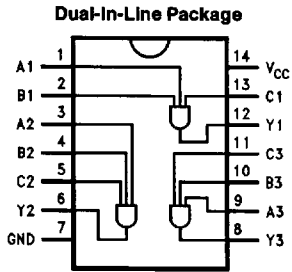


# DM54LS15/DM74LS15 Triple 3-Input AND Gate with Open-Collector Outputs

## General Description

This device contains three independent gates, each of which perform the logic AND function. The outputs are open-collector.

## Connection Diagram



TL/F/10167-1

Order Number DM54LS15J, DM54LS15W,  
DM74LS15M or DM74LS15N  
See NS Package Number J14A, M14A, N14A or W14B

## Function Table

(Each Gate)

Inputs			Output Y
A	B	C	
H	H	H	H
L	X	X	L
X	L	X	L
X	X	L	L

## Logic Diagram

(Each Gate)



TL/F/10167-1

Positive Logic

$$Y = A \cdot B \cdot C \text{ or } Y = \overline{\overline{A} + \overline{B} + \overline{C}}$$

DM54LS15/DM74LS15 Triple 3-Input AND Gate with Open-Collector Outputs

### Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	
DM54LS	-55°C to +125°C
DM74LS	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### Recommended Operating Conditions at $V_{CC} = +5.0V$ , $T_A = +25^\circ C$

Symbol	Parameter	DM54LS15			DM74LS15			Units
		Min	Nom	Max	Min	Nom	Max	
$V_{CC}$	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
$V_{IH}$	High Level Input Voltage	2			2			V
$V_{IL}$	Low Level Input Voltage			0.7			0.8	V
$V_{OH}$	High Level Output Voltage			5.5			5.5	V
$I_{OL}$	Low Level Output Current			4			8	mA
$T_A$	Free Air Operating Temperature	-55		125	0		70	°C

### Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

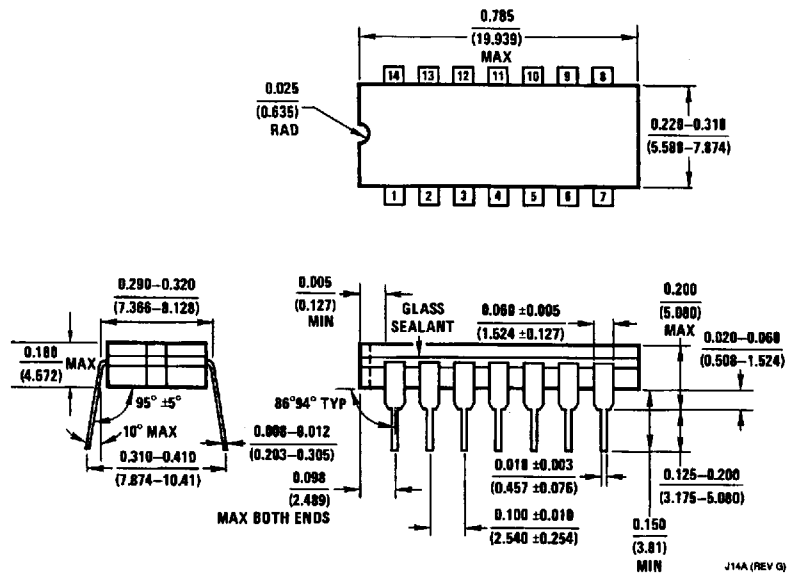
Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
$V_I$	Input Clamp Voltage	$V_{CC} = \text{Min}$ , $I_I = -18 \text{ mA}$			-1.5	V
$I_{CEX}$	High Level Output Current	$V_{CC} = \text{Min}$ , $V_O = 5.5V$ $V_{IH} = \text{Min}$			100	$\mu A$
$V_{OL}$	Low Level Output Voltage	$V_{CC} = \text{Min}$ , $I_{OL} = \text{Max}$ , $V_{IH} = \text{Min}$	DM54		0.4	V
			DM74		0.5	
		$I_{OL} = 4 \text{ mA}$ , $V_{CC} = \text{Min}$	DM74		0.4	
$I_I$	Input Current @ Max Input Voltage	$V_{CC} = \text{Max}$ , $V_I = 7V$ $V_I = 10V$ (for DM54)			0.1	mA
$I_{IH}$	High Level Input Current	$V_{CC} = \text{Max}$ , $V_I = 2.7V$			20	$\mu A$
$I_{IL}$	Low Level Input Current	$V_{CC} = \text{Max}$ , $V_I = 0.4V$			-0.4	mA
$I_{CCH}$	Supply Current with Outputs High	$V_{CC} = \text{Max}$ , $V_{IN} = \text{OPEN}$			3.6	mA
$I_{CCL}$	Supply Current with Outputs Low	$V_{IN} = \text{GND}$			6.6	mA

Note 1: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$ .

**Switching Characteristics**  $V_{CC} = +5.0V, T_A = +25^{\circ}C$

Symbol	Parameter	$R_L = 2\text{ k}\Omega$ $C_L = 15\text{ pF}$		Units
		Max		
		DM54	DM74	
$t_{PLH}$	Propagation Delay Time Low to High Level Output	24	20	ns
$t_{PHL}$	Propagation Delay Time High to Low Level Output	18	15	ns

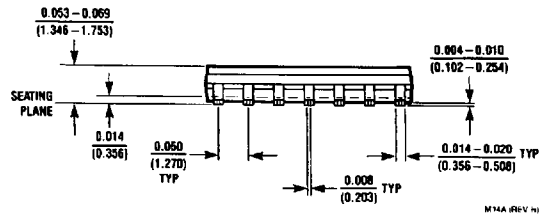
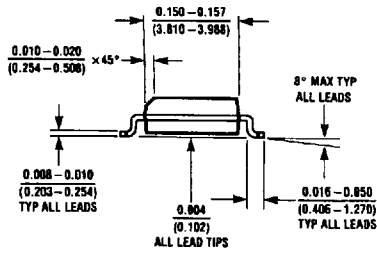
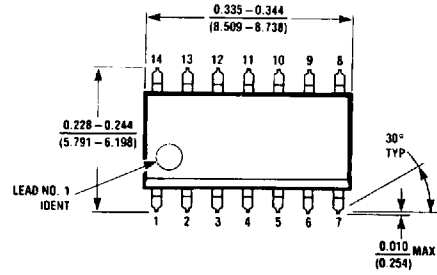
**Physical Dimensions** inches (millimeters)



**14-Lead Ceramic Dual-In-Line Package (J)**  
**Order Number DM54LS15J**  
**NS Package Number J14A**

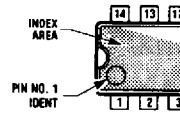
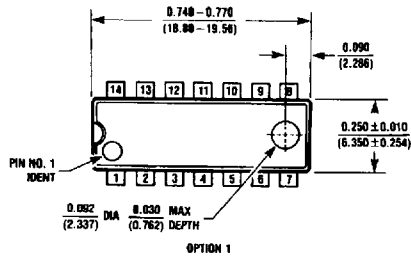
J14A (REV G)

**Physical Dimensions** inches (millimeters) (Continued)

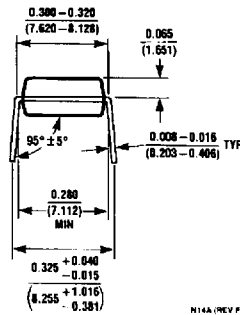
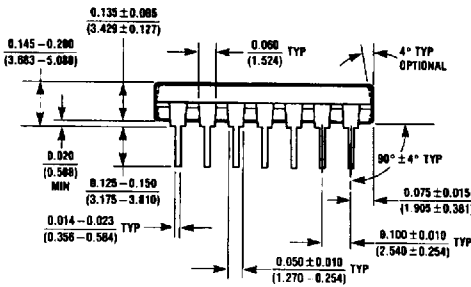


MMA (REV. H)

**14-Lead Small Outline Molded Package (M)**  
**Order Number DM74LS15M**  
**NS Package Number M14A**



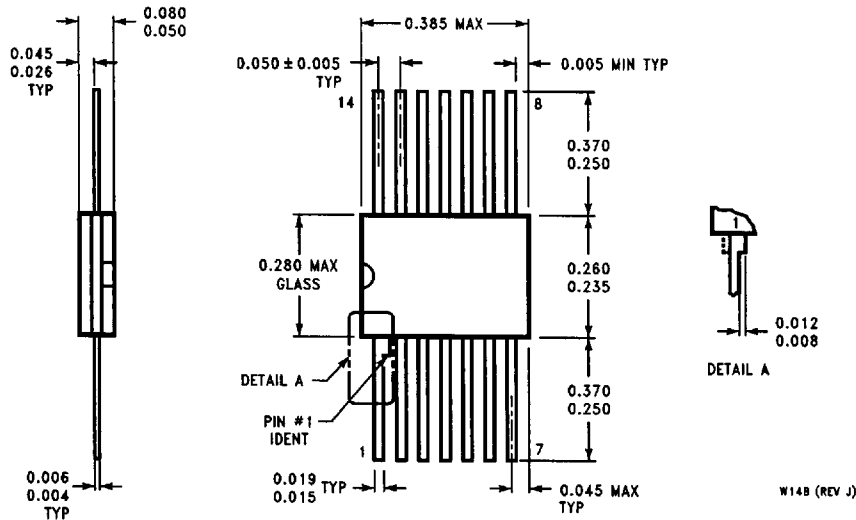
OPTION 02



N14A (REV. P)

**14-Lead Molded Dual-In-Line Package (N)**  
**Order Number DM74LS15N**  
**NS Package Number N14A**

**Physical Dimensions** inches (millimeters) (Continued)



**14-Lead Ceramic Flat Package (W)**  
**Order Number DM54LS15W**  
**NS Package Number W14B**

W14B (REV J)

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