

#### Vishay High Power Products

# HEXFRED® Ultrafast Soft Recovery Diode, 8 A



PRODUCT SUMMARY					
$V_{R}$	600 V				
V <sub>F</sub> at 8 A at 25 °C	1.7 V				
I <sub>F(AV)</sub>	8 A				
t <sub>rr</sub> (typical)	18 ns				
T <sub>J</sub> (maximum)	150 °C				

#### **FEATURES**

- · Ultrafast recovery time
- · Ultrasoft recovery
- Very low I<sub>RRM</sub>
- Very low Q<sub>rr</sub>
- · Guaranteed avalanche
- · Specified at operating conditions
- Lead (Pb)-free
- Designed and qualified for Q101 level

#### **BENEFITS**

- · Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- · Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

#### **DESCRIPTION**

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for freewheeling, flyback, power converters, motor drives, and other applications where high speed and reduced switching losses are design requirements.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Cathode to anode voltage	V <sub>RRM</sub>		600	V		
Maximum continuous forward current	I <sub>F</sub>	T <sub>C</sub> = 100 °C	8			
Single pulse forward current	I <sub>FSM</sub>		60	Α		
Peak repetitive forward current	I <sub>FRM</sub>		24			
Maximum power dissipation	$P_D$	T <sub>C</sub> = 100 °C	14	W		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to + 150	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA		600	-	-	
		I <sub>F</sub> = 8 A		-	1.4	1.7	V
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 16 A	See fig. 1	-	1.7	2.1		
		I <sub>F</sub> = 8 A, T <sub>J</sub> = 125 °C		-	1.4	1.7	
Maximum reverse		V <sub>R</sub> = V <sub>R</sub> rated		-	0.3	5.0	
leakage current	I <sub>R</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 0.8 x V <sub>R</sub> rated		-	100	500	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	See fig. 3	-	10	25	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body -		-	8.0	-	nH

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

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### HFA08SD60SPbF

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	18	-	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 8 A - dI <sub>F</sub> /dt = 200 A/μs V <sub>B</sub> = 200 V	-	37	55	ns
	-	T <sub>J</sub> = 125 °C		-	55	90	
Pook roomer ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	3.5	5.0	Α
Peak recovery current		T <sub>J</sub> = 125 °C		-	4.5	8.0	
Doverse receivery charge	verse recovery charge Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	65	138	nC
neverse recovery charge		Q <sub>rr</sub>	T <sub>J</sub> = 125 °C	H	-	124	360
Rate of fall of recovery current dI <sub>(rec)M</sub> /dt	-11 /-14	T <sub>J</sub> = 25 °C		-	240	-	Δ /
	T <sub>J</sub> = 125 °C		-	210	-	- A/μs	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55	-	150	°C
Lead temperature	T <sub>lead</sub>		-	-	300	
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	3.5	°C/W
Thermal resistance, junction to ambient	$R_{thJA}$	Typical socket mount	-	-	80	C/VV
Woight			-	2.0	-	g
Weight			-	0.07	=	OZ.
Marking device		Case style D-PAK		HFA08	SD60S	

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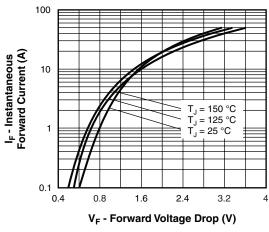


Fig. 1 - Typical Forward Voltage Drop Characteristics

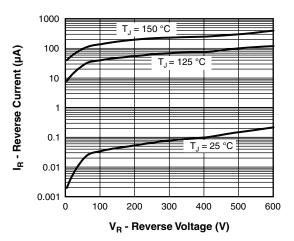


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

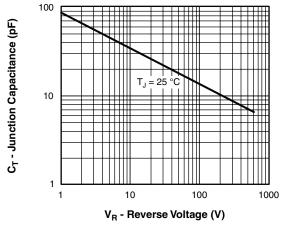


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

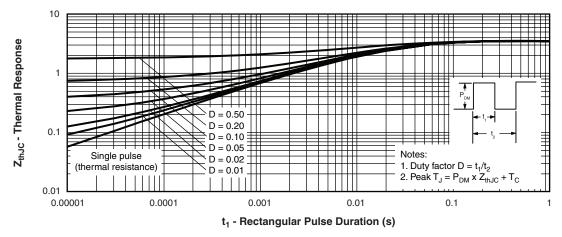


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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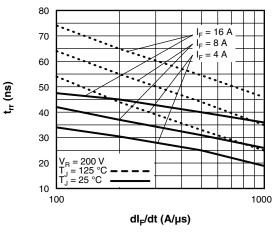


Fig. 5 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

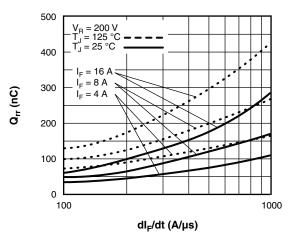


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

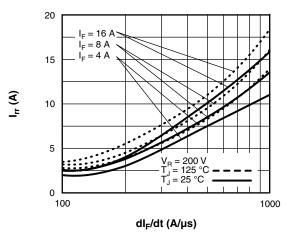


Fig. 6 - Typical Recovery Current vs. dI<sub>F</sub>/dt

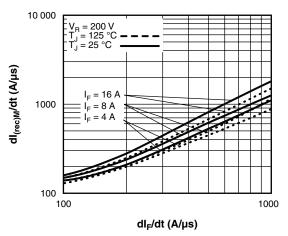


Fig. 8 - Typical  $dI_{(rec)M}/dt$  vs.  $dI_F/dt$ 



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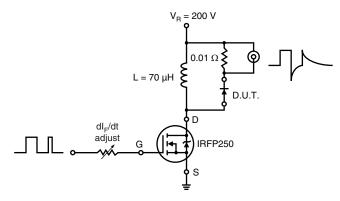
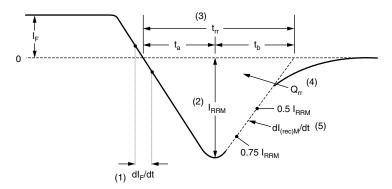


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $t_{\rm rr}$  reverse recovery time measured from zero crossing point of negative going  $I_{\rm F}$  to point where a line passing through 0.75  $I_{\rm RRM}$  and 0.50  $I_{\rm RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dI<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 10 - Reverse Recovery Waveform and Definitions

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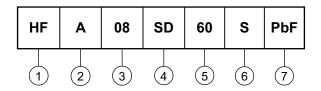
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#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 HEXFRED® family
- 2 Electron irradiated
- 3 Current rating (08 = 8 A)
- 4 D-PAK
- 5 Voltage rating (60 = 600 V)
  - Suffix —
- • None = Standard production
  - PbF = Lead (Pb)-free

$S = D^2PAK/D-PAK$			
TR = Tape and reel			

TRL = Tape and reel left

TRR = Tape and reel right

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95016				
Part marking information	http://www.vishay.com/doc?95059			
Packaging information	http://www.vishay.com/doc?95033			

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