

# General purpose (dual digital transistors)

## IMD23

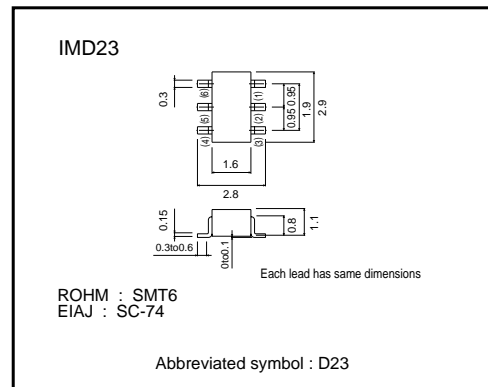
●Features

- 1) Both the DTB113Z chip and DTC114E chip in a SMT package.
- 2) Mounting possible with SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

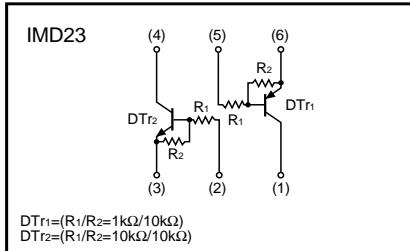
●Structure

Epitaxial planar type  
NPN / PNP silicon transistor (Built-in resistor type)

●External dimensions (Unit : mm)



●Equivalent circuits



●Absolute maximum ratings (Ta=25°C)

DT1

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-10 to +5	V
Output current	I <sub>c</sub>	-500	mA
Power dissipation	P <sub>d</sub>	300(Total)	mW *
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* 200mW per element must not be exceeded.

Transistors

DT<sub>r2</sub>

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	50	V
Input voltage	V <sub>IN</sub>	-10 to +40	V
Output current	I <sub>o</sub>	50	mA
	I <sub>c (Max.)</sub>	100	
Power dissipation	P <sub>d</sub>	300 (TOTAL)	mW *
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* 200mW per element must not be exceeded.

●Electrical characteristics (T<sub>a</sub>=25°C)

DT<sub>r1</sub>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	-	-	-0.3	V	V <sub>CC</sub> =-5V, I <sub>o</sub> =-100μA
	V <sub>I(on)</sub>	-3	-	-		V <sub>o</sub> =-0.3V, I <sub>o</sub> =-20mA
Output voltage	V <sub>O(on)</sub>	-	-	-0.3	V	I <sub>o</sub> /I <sub>i</sub> =-50mA/-2.5mA
Input current	I <sub>i</sub>	-	-	-7.2	mA	V <sub>i</sub> =-5V
Output current	I <sub>O(off)</sub>	-	-	-0.5	μA	V <sub>CC</sub> =-50V, V <sub>i</sub> =0V
DC current gain	G <sub>i</sub>	56	-	-	-	V <sub>o</sub> =-5V, I <sub>o</sub> =-50mA
Input resistance	R <sub>1</sub>	0.7	1	1.3	kΩ	-
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	8	10	12	-	-
Transition frequency	f <sub>r</sub>	-	200	-	MHz	V <sub>CE</sub> =-10V, I <sub>E</sub> =50mA, f=100MHz *

\* Transition frequency of the device

DT<sub>r2</sub>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	-	-	0.5	V	V <sub>CC</sub> =5V, I <sub>o</sub> =100μA
	V <sub>I(on)</sub>	3	-	-		V <sub>o</sub> =0.3V, I <sub>o</sub> =10mA
Output voltage	V <sub>O(on)</sub>	-	0.1	0.3	V	I <sub>o</sub> =10mA, I <sub>i</sub> =0.5mA
Input current	I <sub>i</sub>	-	-	0.88	mA	V <sub>i</sub> =5V
Output current	I <sub>O(off)</sub>	-	-	0.5	μA	V <sub>CC</sub> =50V, V <sub>i</sub> =0V
DC current gain	G <sub>i</sub>	30	-	-	-	V <sub>o</sub> =5V, I <sub>o</sub> =5mA
Transition frequency	f <sub>r</sub>	-	250	-	MHz	V <sub>CE</sub> =10V, I <sub>E</sub> =-5mA, f=100MHz *
Input resistance	R <sub>1</sub>	7	10	13	kΩ	-
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	-	-

\* Transition frequency of the device

Transistors

●Packaging specifications

Type	Package	Taping
	Code	T110
	Basic ordering unit (pieces)	3000
IMD23		○

●Electrical characteristic curves

DTr1 (PNP)

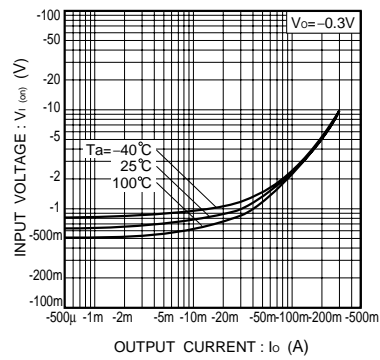


Fig.1 Input voltage vs. output current (ON characteristics)

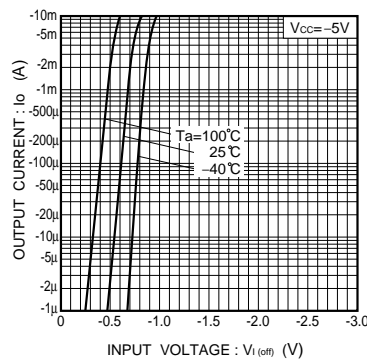


Fig.2 Output current vs. input voltage (OFF characteristics)

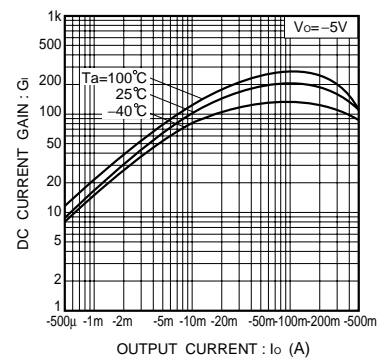


Fig.3 DC current gain vs. output current

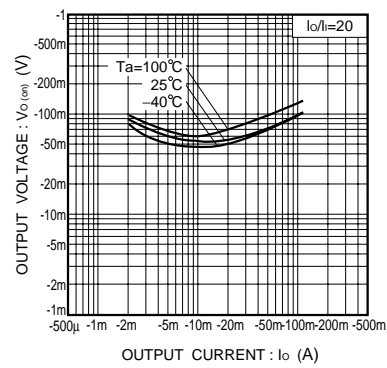


Fig.4 Output voltage vs. output current

Transistors

DTr2 (NPN)

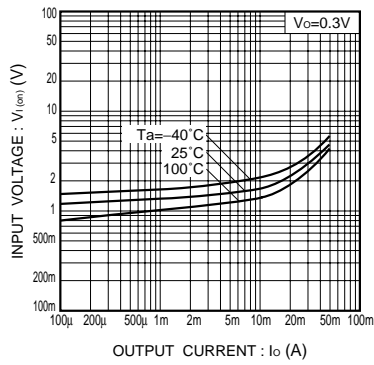


Fig.5 Input voltage vs. output current (ON characteristics)

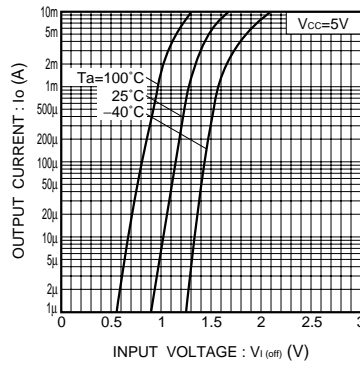


Fig.6 Output current vs. input voltage (OFF characteristics)

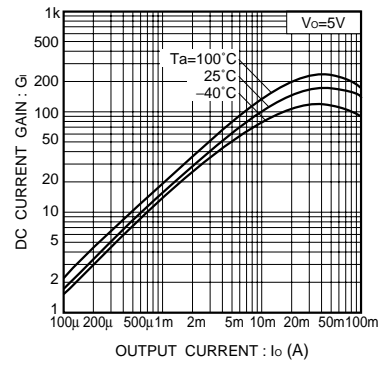


Fig.7 DC current gain vs. output current

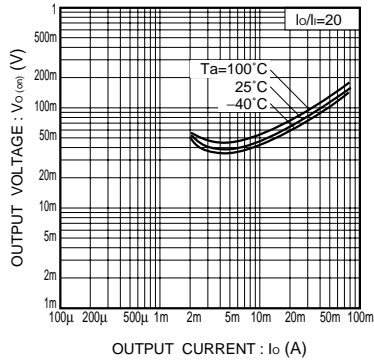


Fig.8 Output voltage vs. output current

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