

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, BIPOLAR, LOW-POWER SCHOTTKY TTL,  
 NOR GATES, MONOLITHIC SILICON

This specification is approved for use by all Departments and Agencies of the Department of Defense.

1. SCOPE

1.1 Scope. This specification covers the detail requirements for monolithic silicon, low-power Schottky TTL, positive NOR logic gating microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided and are reflected in the complete part number.

1.2 Part number. The part number shall be in accordance with MIL-M-38510.

1.2.1 Device types. The device types shall be as follows:

<u>Device type</u>	<u>Circuit</u>
01	Quadruple, 2-input positive NOR gate
02	Triple, 3-input positive NOR gate
03	Quadruple, 2-input exclusive NOR gate (open collector output)

1.2.2 Device class. The device class shall be the product assurance level as defined in MIL-M-38510.

1.2.3 Case outlines. The case outlines shall be designated as follows:

<u>Outline letter</u>	<u>Case outline (see MIL-M-38510), appendix C)</u>
A	F-1 (14-lead, 1/4" x 1/4"), flat package
B	F-3 (14-lead, 3/16" x 1/4"), flat package
C	D-1 (14-lead, 1/4" x 3/4"), dual-in-line package
D	F-2 (14-lead, 1/4" x 3/8"), flat package
X	C-2A (20-terminal, .350" x .350"), square chip carrier package
2	C-2 (20-terminal, .350" x .350"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range - - - - -	-0.5 V dc to +7.0 V dc
Input voltage range- - - - -	-1.5 V dc at -18 mA to +5.5 V dc
Storage temperature range- - - - -	-65°C to +150°C
Maximum power dissipation (P <sub>D</sub> ) 1/:	
Device type 01 - - - - -	30 mW
Device type 02 - - - - -	38 mW
Device type 03 - - - - -	72 mW
Lead temperature (soldering, 10 seconds) - - -	+300°C
Thermal resistance, junction-to-case (θ <sub>JC</sub> ):	
Cases A, B, C, D, X, and 2 - - - - -	(See MIL-M-38510, appendix C)
Junction temperature (T <sub>J</sub> ) 2/ - - - - -	+175°C

1/ Must withstand the added P<sub>D</sub> due to short-circuit test (e.g., I<sub>OS</sub>).

2/ Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening condition per method 5004 of MIL-STD-883.

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Rome Air Development Center, (RBE-2), Griffiss AFB, NY 13441-5700, by using the self-addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

**1.4 Recommended operating conditions.**

Supply voltage ( $V_{CC}$ )	- - - - -	+4.5 V dc minimum to +5.5 V dc maximum
Minimum high-level input voltage ( $V_{IH}$ )	- - - -	+2.0 V dc
Maximum low-level input voltage ( $V_{IL}$ )	- - - -	+0.7 V dc
Case operating temperature range ( $T_C$ )	- - - -	-55°C to +125°C

**2. APPLICABLE DOCUMENTS****2.1 Government documents.**

2.1.1 Specification and standard. The following specification and standard form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

**SPECIFICATION****MILITARY**

MIL-M-38510 - Microcircuits, General Specification for.

**STANDARD****MILITARY**

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein (except for associated detail specifications, specification sheets or MS standards), the text of this specification shall take precedence. Nothing in this specification, however, shall supersede applicable laws and regulations unless a specific exemption has been obtained.

**3. REQUIREMENTS**

3.1 Detail specification. The individual item requirements shall be in accordance with MIL-M-38510, and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510, and herein.

3.2.1 Case outlines. The case outlines shall be as specified in 1.2.3.

3.2.2 Logic diagrams and terminal connections. The logic diagrams and terminal connections shall be as specified on figure 1.

3.2.3 Truth tables and logic equations. The truth tables and logic equations shall be as specified on figure 2.

3.2.4 Schematic circuits. The schematic circuits shall be submitted to the preparing activity prior to inclusion of a manufacturer's device in this specification and shall be submitted to the qualifying activity and agent activity (DESC-ECS) as a prerequisite for qualification. All qualified manufacturers' schematics shall be maintained by the agent activity and will be available upon request.

3.3 Lead material and finish. The lead material and finish shall be in accordance with MIL-M-38510 (see 6.4).

3.4 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are specified in table I, and apply over the full recommended case operating temperature range.

3.5 Electrical test requirements. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions 1/ $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Device type	Limits		Unit
				Min	Max	
High-level output voltage	$V_{OH}$	$V_{CC} = +4.5\text{ V}$ , $V_{IN} = +0.7\text{ V}$ $I_{OH} = -400\ \mu\text{A}$	01, 02	+2.5		V
Low-level output voltage	$V_{OL}$	$V_{CC} = +4.5\text{ V}$ , $I_{OL} = +4\text{ mA}$ $V_{IN} = +2.0\text{ V}$	A11		+0.4	V
High-level input current	$I_{IH1}$	$V_{CC} = +5.5\text{ V}$ , $V_{IN} = +2.7\text{ V}$	01, 02		+20	$\mu\text{A}$
		$V_{CC} = +5.5\text{ V}$ , $V_{IN} = +2.7\text{ V}$	03		+40	$\mu\text{A}$
High-level input current	$I_{IH2}$	$V_{CC} = +5.5\text{ V}$ , $V_{IN} = +5.5\text{ V}$	01, 02		+100	$\mu\text{A}$
		$V_{CC} = +5.5\text{ V}$ , $V_{IN} = +5.5\text{ V}$	03		+200	$\mu\text{A}$
Collector cut-off current	$I_{CEX}$	$V_{CC} = +4.5\text{ V}$ , $V_{IN} = +0.7\text{ V}$ $V_{OH} = +5.5\text{ V}$	03		+100	$\mu\text{A}$
Low-level input current	$I_{IL}$	$V_{CC} = +5.5\text{ V}$ , $V_{IN} = +0.4\text{ V}$	01, 02	-30	-400	$\mu\text{A}$
		$V_{CC} = +5.5\text{ V}$ , $V_{IN} = +0.4\text{ V}$	03	-200	-760	$\mu\text{A}$
Short-circuit output current	$I_{OS}$	$V_{CC} = +5.5\text{ V}$ , $\frac{2/}{V_{IN} = 0\text{ V}}$	01, 02	-15	-130	mA
Input-clamp voltage	$V_{IC}$	$V_{CC} = +4.5\text{ V}$ , $I_{IN} = -18\text{ mA}$ $T_C = +25^{\circ}\text{C}$	A11		-1.5	V
High-level supply current	$I_{CCH}$	$V_{CC} = +5.5\text{ V}$ , $V_{IN} = 0\text{ V}$	01		+3.2	mA
			02		+4.0	
Low-level supply current	$I_{CCL}$	$V_{CC} = +5.5\text{ V}$ , $V_{IN} = +4.5\text{ V}$	01		+5.4	mA
			02		+6.8	
Supply current	$I_{CC}$	$V_{CC} = +5.5\text{ V}$ $\frac{3/}{}$	03		+13	mA
Propagation delay time, high-to-low-level	$t_{PHL}$	$C_L = 50\text{ pF} \pm 10\%$ $R_L = 2\text{ k}\Omega \pm 5\%$ $V_{CC} = +5.0\text{ V}$	01	+2.0	+30	ns
			02	+2.0	+26	
	$t_{PHL1}$	From A or B	Other input low	03	+2.0	+45
$t_{PHL2}$	From A or B	Other input high	+2.0		+45	ns

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions 1/ $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Device type	Limits		Unit
				Min	Max	
Propagation delay time, low-to-high-level	$t_{PLH}$	$C_L = 50\text{ pF} \pm 10\%$ $R_L = 2\text{ k}\Omega \pm 5\%$ $V_{CC} = +5.0\text{ V}$	01,02	+2.0	+30	ns
	$t_{PLH1}$	From A Other or B input low	03	+2.0	+56	ns
	$t_{PLH2}$	From A Other or B input high		+2.0	+56	ns

1/ Complete terminal conditions shall be as specified in table III.

2/ Not more than one output should be shorted at a time.

3/  $I_{CC}$  is measured with one input of each gate at +4.5 V, the other inputs grounded, and the outputs open.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (see table III)	
	Class S devices	Class B devices
Interim electrical parameters (method 5004)	1	1
Final electrical test parameters (method 5004)	1*, 2, 3, 9, 10, 11	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 9, 10, 11	1, 2, 3, 9, 10, 11
Group B test requirements (method 5005), subgroup 5	1, 2, 3, 9, 10, 11	N/A
Group C end-point electrical parameters (method 5005)	N/A	1, 2, 3
Group D end-point electrical parameters (method 5005)	1, 2, 3	1, 2, 3

\*PDA applies to subgroup 1 (see 4.2c.).

		Pin name		Pin name		Pin name	
Pin number		Device type 01		Device type 02		Device type 03	
Cases							
2 and X	A, B, C and D	2 and X	A, B, C and D	2 and X	A, B, C and D	2 and X	A, B, C and D
1	1	NC	1Y	NC	1A	NC	
2	2	1Y	1A	1A	1B	1A	
3	3	1A	1B	1B	2A	1B	
4	4	1B	2Y	2A	2B	1Y	
5	5	NC	2A	NC	2C	NC	
6	6	2Y	2B	2B	2Y	2Y	
7	7	NC	GND	NC	GND	NC	
8	8	2A	3A	2C	3Y	2A	
9	9	2B	3B	2Y	3A	2B	
10	10	GND	3Y	GND	3B	GND	
11	11	NC	4A	NC	3C	NC	
12	12	3A	4B	3Y	1Y	3A	
13	13	3B	4Y	3A	1C	3B	
14	14	3Y	V <sub>CC</sub>	3B	V <sub>CC</sub>	3Y	
15		NC		NC		NC	
16		4A		3C		4Y	
17		NC		NC		NC	
18		4B		1Y		4A	
19		4Y		1C		4B	
20		V <sub>CC</sub>		V <sub>CC</sub>		V <sub>CC</sub>	

FIGURE 1. Terminal connections.

Device type 01

Truth table each gate		
Input		Output
A	B	Y
H	X	L
X	H	L
L	L	H

X = Irrelevant

Positive logic:  $Y = \overline{A + B}$

Device type 02

Truth table each gate			
Input			Output
A	B	C	Y
H	X	X	L
X	H	X	L
X	X	H	L
L	L	L	H

X = Irrelevant

Positive logic:  $Y = \overline{A+B+C}$

Device type 03

Truth table each gate		
Input		Output
A	B	Y
L	L	H
L	H	L
H	L	L
H	H	H

Positive logic:  $Y = \overline{A \oplus B} = AB + \overline{A} \overline{B}$

FIGURE 2. Truth tables and logic equations.

3.6 Marking. Marking shall be in accordance with MIL-M-38510.

3.7 Microcircuit group assignment. The devices covered by this specification shall be microcircuit group number 8 (see MIL-M-38510, appendix E).

#### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-M-38510 and methods 5005 and 5007, as applicable, of MIL-STD-883, except as modified herein.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test (method 1015 of MIL-STD-883).

(1) Test condition D, E, or F, using the circuit shown on figure 3 or equivalent.

(2)  $T_A = +125^\circ\text{C}$  minimum.

b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.

c. The percent defective allowable (PDA) shall be as specified in MIL-M-38510.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-M-38510. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).

4.4 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-M-38510 and as specified herein. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection. Group A inspection shall be in accordance with table I of method 5005 of MIL-STD-883 and as follows:

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, 7, and 8 shall be omitted.

4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of method 5005 of MIL-STD-883. Electrical parameters shall be as specified in table II herein.

4.4.3 Group C inspection. Group C inspection shall be in accordance with table III of method 5005 of MIL-STD-883 and as follows:

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test (method 1005 of MIL-STD-883) conditions, or equivalent.

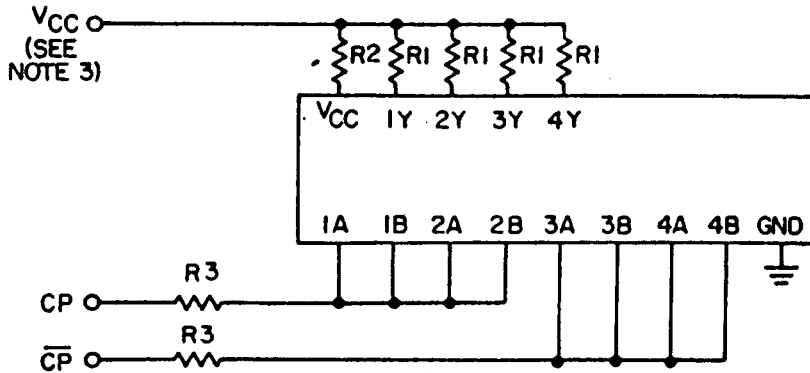
(1) Test condition D, E, or F, using the circuit shown on figure 3, or equivalent.

(2)  $T_A = +125^\circ\text{C}$ , minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.4.4 Group D inspection. Group D inspection shall be in accordance with table IV of method 5005 of MIL-STD-883. End-point electrical parameters shall be as specified in table II herein.

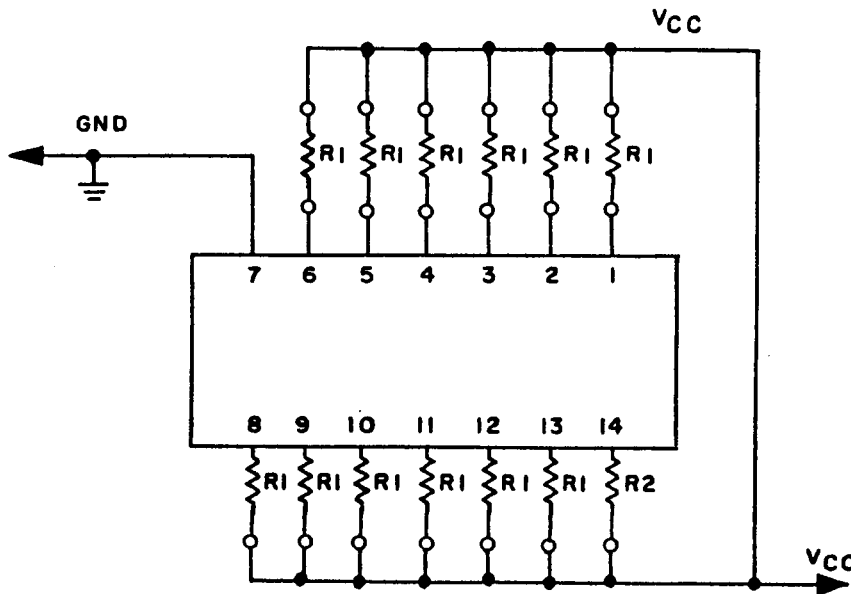
Device type 01



NOTES:

1. CP and CP - 100 kHz  $\pm$ 50 percent square wave; duty cycle = 50  $\pm$ 15 percent;  $V_{IH}$  = 2.0 V minimum to 5.5 V maximum;  $V_{IL}$  = -0.5 V minimum to +0.7.
2.  $R_1$  = 1 k $\Omega$   $\pm$ 5 percent;  $R_2$  = 10 $\Omega$   $\pm$ 5 percent;  $R_3$  = 27 $\Omega$   $\pm$ 5 percent.
3.  $V_{CC}$  shall be high enough to insure that 5.0 V minimum is present at  $V_{CC}$  device terminal.

High temperature burn-in

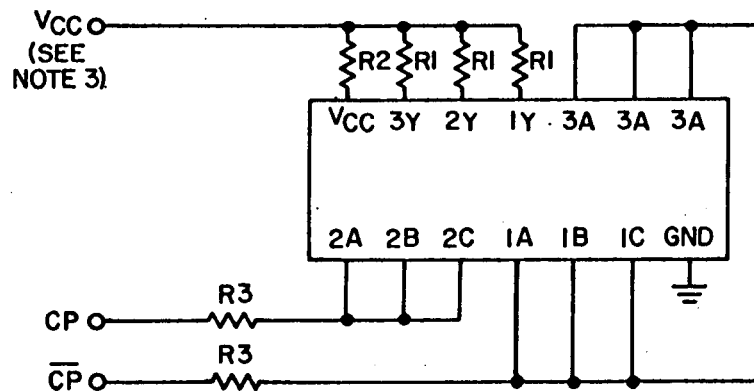


NOTES:

1. High temperature conditions F(175°C) condition F burn-in only.
2.  $R_1$  = 2 k $\Omega$ .
3.  $V_{CC}$  shall be chosen such that 5.0 V minimum is present at device terminal.

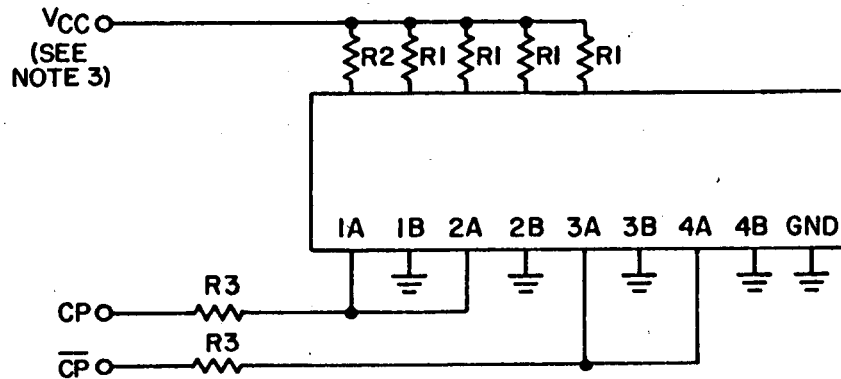
FIGURE 3. Burn-in and life test circuits.



Device type 02

## NOTES:

1. CP and  $\overline{\text{CP}}$  = 100 kHz  $\pm 50\%$  square wave; duty cycle = 50  $\pm 15\%$ ;  $V_{IH}$  = 2.0 V minimum to 5.5 V maximum;  $V_{IL}$  = -0.5 V minimum to +0.7 maximum.
2.  $R_1$  = 1 k $\Omega$   $\pm 5\%$ ;  $R_2$  = 10 $\Omega$   $\pm 5\%$ ;  $R_3$  = 27 $\Omega$   $\pm 5\%$ .
3. VCC shall be high enough to insure 5.0 V at device VCC terminal.

Device type 03

## NOTES:

1. CP and  $\overline{\text{CP}}$  = 100 kHz  $\pm 50\%$  square wave; duty cycle = 50  $\pm 15\%$ ;  $V_{IH}$  = 2.0 V minimum to 5.5 V maximum;  $V_{IL}$  = -0.5 V minimum to +0.7 V maximum.
2.  $R_1$  = 1 k $\Omega$   $\pm 5\%$ ;  $R_2$  = 10 $\Omega$   $\pm 5\%$ ;  $R_3$  = 27 $\Omega$   $\pm 5\%$ .
3. VCC shall be high enough to insure 5.0 V at device VCC terminal.

FIGURE 3. Burn-in and life test circuits - Continued.

TABLE III. Group A. Inspection for device type 01. Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D, X, Y, Z / Test no.	Cases A, B, C, D, X, Y, Z / Test no.										Limits		Measured Terminal	Unit		
				1	2	3	4	5	6	7	8	9	10	11	12			13	14
1 $T_C = +25^\circ\text{C}$	V <sub>OH</sub>	3006	1	-400 $\mu\text{A}$	0.7 V	0.7 V	2Y	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1Y	2.5 V	
			2	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	2Y	"	
			3	"	"	"	"	"	"	"	"	"	"	"	"	"	"	3Y	"
			4	"	"	"	"	"	"	"	"	"	"	"	"	"	"	4Y	"
	V <sub>OL</sub>	3007	5	4 mA	2.0 V	GND	GND	GND	5.5 V	GND	GND	GND	GND	GND	GND	GND	1Y	0.4 V	
			6	4 mA	2.0 V	GND	GND	GND	5.5 V	GND	GND	GND	GND	GND	GND	GND	1Y	"	
			7	"	"	"	"	"	"	"	"	"	"	"	"	"	2Y	"	
			8	"	"	"	"	"	"	"	"	"	"	"	"	"	3Y	"	
			9	"	"	"	"	"	"	"	"	"	"	"	"	"	3Y	"	
			10	"	"	"	"	"	"	"	"	"	"	"	"	"	3Y	"	
			11	"	"	"	"	"	"	"	"	"	"	"	"	"	4Y	"	
			12	"	"	"	"	"	"	"	"	"	"	"	"	"	4Y	"	
V <sub>IC</sub>		13	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5 V		
		14	"	"	"	"	"	"	"	"	"	"	"	"	"	1A	"		
		15	"	"	"	"	"	"	"	"	"	"	"	"	"	1B	"		
		16	"	"	"	"	"	"	"	"	"	"	"	"	"	2A	"		
		17	"	"	"	"	"	"	"	"	"	"	"	"	"	2B	"		
		18	"	"	"	"	"	"	"	"	"	"	"	"	"	3A	"		
		19	"	"	"	"	"	"	"	"	"	"	"	"	"	3B	"		
		20	"	"	"	"	"	"	"	"	"	"	"	"	"	4A	"		
I <sub>IH1</sub>	3010	21	2.7 V	2.7 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20 $\mu\text{A}$		
		22	GND	GND	2.7 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	"		
		23	"	"	"	"	"	"	"	"	"	"	"	"	"	1B	"		
		24	"	"	"	"	"	"	"	"	"	"	"	"	"	2A	"		
		25	"	"	"	"	"	"	"	"	"	"	"	"	"	2B	"		
		26	"	"	"	"	"	"	"	"	"	"	"	"	"	3A	"		
		27	"	"	"	"	"	"	"	"	"	"	"	"	"	3B	"		
		28	"	"	"	"	"	"	"	"	"	"	"	"	"	4A	"		
I <sub>IH2</sub>		29	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1A	100 $\mu\text{A}$		
		30	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	"		
		31	"	"	"	"	"	"	"	"	"	"	"	"	"	1B	"		
		32	"	"	"	"	"	"	"	"	"	"	"	"	"	2A	"		
		33	"	"	"	"	"	"	"	"	"	"	"	"	"	2B	"		
		34	"	"	"	"	"	"	"	"	"	"	"	"	"	3A	"		
		35	"	"	"	"	"	"	"	"	"	"	"	"	"	3B	"		
		36	"	"	"	"	"	"	"	"	"	"	"	"	"	4A	"		
I <sub>IL 2/</sub>	3009	37	0.4 V	0.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1A	2/ V		
		38	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1A	"		
		39	"	"	"	"	"	"	"	"	"	"	"	"	"	1B	"		
		40	"	"	"	"	"	"	"	"	"	"	"	"	"	2A	"		
		41	"	"	"	"	"	"	"	"	"	"	"	"	"	2B	"		
		42	"	"	"	"	"	"	"	"	"	"	"	"	"	3A	"		
		43	"	"	"	"	"	"	"	"	"	"	"	"	"	3B	"		
		44	"	"	"	"	"	"	"	"	"	"	"	"	"	4A	"		
I <sub>OS 3/</sub>	3011	45	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1Y	3/ mA		
		46	"	"	"	"	"	"	"	"	"	"	"	"	"	2Y	"		
		47	"	"	"	"	"	"	"	"	"	"	"	"	"	3Y	"		
		48	"	"	"	"	"	"	"	"	"	"	"	"	"	4Y	"		
I <sub>CCH</sub>	3005	49	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VCC	3.2 V			
			"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
I <sub>CC1</sub>	3005	50	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	VCC	5.4 V			
			"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		

See footnotes at end of device type 01.

TABLE III. Group A Inspection for device type 01 - Continued.  
Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

Subgroup	Symbol	MIL-STD-883 Y, 2 J/ method	Cases A, B, C, D,										Measured terminal		Limits		Unit			
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Min	Max	
2		Test no.	1Y	1A	1B	2Y	2A	2B	GND	3A	3B	3Y	4A	4B	4Y	VCC				
			2	3	4	6	8	9	10	12	13	14	16	18	19	20				
9	t <sub>PHL</sub>	3003 (Fig. 4)	51	OUT	GND	OUT	IN	GND	GND	IN	GND	OUT	IN	GND	OUT	5.0 V	1A to 1Y	2.0	16	
			52	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	IN	1B to 1Y		
10	t <sub>PLH</sub>		53	OUT	GND	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	2A to 2Y			
			54	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	2B to 2Y			
			55	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	3A to 3Y			
			56	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	3B to 3Y			
			57	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	4A to 4Y			
			58	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	4B to 4Y			
			59	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	1A to 1Y		22	
			60	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	1B to 1Y			
11	t <sub>PHL</sub>		67	OUT	GND	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	1A to 1Y		26	
			68	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	1B to 1Y			
			69	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	2A to 2Y			
			70	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	2B to 2Y			
			71	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	3A to 3Y			
			72	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	3B to 3Y			
			73	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	4A to 4Y			
			74	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	4B to 4Y			
	t <sub>PLH</sub>		75	OUT	GND	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	1A to 1Y		30	
			76	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	1B to 1Y			
			77	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	2A to 2Y			
			78	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	2B to 2Y			
			79	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	3A to 3Y			
			80	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	3B to 3Y			
			81	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	4A to 4Y			
			82	OUT	IN	OUT	OUT	IN	GND	IN	GND	OUT	IN	GND	OUT	IN	4B to 4Y			
Same tests, terminal conditions, and limits as for subgroup 10, except T <sub>C</sub> = -55°C.																				
Same tests, terminal conditions, and limits as for subgroup 1, except T <sub>C</sub> = +125°C and V <sub>IC</sub> tests are omitted.																				
Same tests, terminal conditions, and limits as for subgroup 1, except T <sub>C</sub> = -55°C and V <sub>IC</sub> tests are omitted.																				

1/ Cases X and 2 pins not referenced are N/C.

2/ I<sub>IL</sub> limits in  $\mu$ A shall be as follows:

Parameters	Circuits						
	A	B	C	D	E	F	G
I <sub>IL</sub>	-120/-360	-30/-300	-160/-400	-120/-360	-150/-380	-100/-340	-160/-400

3/ I<sub>OS</sub> limits for circuits A, B, C, D, E, and F are -15/-100 mA; for circuit G, -30/-130 mA.

TABLE III. Group A Inspection for device type 02.  
Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D, X, 2, 1/																					Limits	Unit
			1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured terminal								
	Test no.		1A	1B	2A	2B	2C	2Y	3Y	3A	3B	3C	1Y	1C	V <sub>CC</sub>	Mfn	Max								
I T <sub>C</sub> = +25°C	V <sub>OH</sub>	3006	1	0.7 V	0.7 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1Y	2.5	V						
			2	5.5 V	5.5 V	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	2Y	2.5	V						
			3	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	3Y	2.5	V						
V <sub>OL</sub>	3007	4	2.0 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1Y	0.4	V						
		5	GND	2.0 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1Y	0.4	V						
		6	GND	GND	2.0 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1Y	0.4	V						
		7	GND	GND	GND	2.0 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	2Y	0.4	V						
		8	GND	GND	GND	GND	2.0 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	2Y	0.4	V						
		9	GND	GND	GND	GND	GND	2.0 V	GND	GND	GND	GND	GND	GND	GND	GND	2Y	0.4	V						
		10	GND	GND	GND	GND	GND	GND	2.0 V	GND	GND	GND	GND	GND	GND	GND	2Y	0.4	V						
		11	GND	GND	GND	GND	GND	GND	GND	GND	2.0 V	GND	GND	GND	GND	GND	2Y	0.4	V						
		12	GND	GND	GND	GND	GND	GND	GND	GND	GND	2.0 V	GND	GND	GND	GND	2Y	0.4	V						
		13	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	2.0 V	GND	GND	GND	2Y	0.4	V						
		14	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	2.0 V	GND	GND	2Y	0.4	V						
		V <sub>IC</sub>	3010	13	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA					
14	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
15	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
16	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
17	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
18	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
19	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
20	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
21	-18 mA			-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	-18 mA	1A	-1.5	mA						
22	2.7 V			GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA						
23	GND			2.7 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA						
I <sub>IH1</sub>	3011	24	GND	GND	2.7 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		25	GND	GND	GND	2.7 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		26	GND	GND	GND	GND	2.7 V	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		27	GND	GND	GND	GND	GND	2.7 V	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		28	GND	GND	GND	GND	GND	GND	2.7 V	GND	GND	GND	GND	GND	GND	1A	20	μA							
		29	GND	GND	GND	GND	GND	GND	GND	2.7 V	GND	GND	GND	GND	GND	1A	20	μA							
		30	GND	GND	GND	GND	GND	GND	GND	GND	2.7 V	GND	GND	GND	GND	1A	20	μA							
		31	5.5 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA						
		32	GND	5.5 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA						
		33	GND	GND	5.5 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA						
I <sub>IH2</sub>	3009	34	GND	GND	GND	5.5 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		35	GND	GND	GND	GND	5.5 V	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		36	GND	GND	GND	GND	GND	5.5 V	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		37	GND	GND	GND	GND	GND	GND	5.5 V	GND	GND	GND	GND	GND	GND	1A	20	μA							
		38	GND	GND	GND	GND	GND	GND	GND	5.5 V	GND	GND	GND	GND	GND	1A	20	μA							
		39	GND	GND	GND	GND	GND	GND	GND	GND	5.5 V	GND	GND	GND	GND	1A	20	μA							
		40	0.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1A	20	μA						
		41	5.5 V	0.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1A	20	μA						
		42	5.5 V	5.5 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	0.4 V	1A	20	μA						
		43	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	1A	20	μA						
I <sub>IL 2/</sub>	3009	44	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		45	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		46	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		47	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		48	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		49	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		50	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		51	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1A	20	μA							
		I <sub>OS 3/</sub>	3011	49	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1Y	3/	mA					
				50	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1Y	3/	mA					

See footnotes at end of device type 02.

TABLE III. Group A inspection for device type 02 - Continued.  
Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D		Cases X, Y, Z		Cases 1, 2		Cases 3, 4		Cases 5, 6		Cases 7, 8		Cases 9, 10		Cases 11, 12		Cases 13, 14		Cases 15, 16		Limits	Measured terminals	Unit				
			1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20				21	22	Min	Max
9 $T_C = +25^\circ\text{C}$	$t_{PH}$	3003 (Fig. 4)	54	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns		
			55	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns
10 $T_C = +125^\circ\text{C}$	$t_{PH}$		63	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns		
			64	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns
			65	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns
			66	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns
			67	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns
			68	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns
11	$t_{PH}$		81	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns		
			82	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	GND	IN	5.0 V	1A to 1V 18 to 1V 1C to 1V 2A to 2V 2B to 2V 2C to 2V 3A to 3V 3B to 3V 3C to 3V	ns		

1/ Cases X and Z pins not referenced are N/C.  
2/ I<sub>TL</sub> limits in  $\mu\text{A}$  shall be as follows:

Parameters	Circuits					
	A	B	C	D	E	F
I <sub>TL</sub>	-120/-360	-30/-300	-160/-400	-120/-360	-100/-340	-160/-400
						-150/-380

3/ I<sub>OS</sub> limits for circuits A, B, C, D, E, F are -15/-100 mA; for circuit G, -30/-130 mA.

Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D										Measured terminal	Limits		Unit				
			1	2	3	4	5	6	7	8	9	10		11	12		13	14	Min	Max
1 $T_C = +25^\circ\text{C}$	$V_{OL}$	3007	1A	18	1V	2V	2A	2B	GND	3A	3B	3V	4A	4B	VCC					
			1	2.0 V	4 mA	5.5 V	5.5 V	5.5 V	GND	5.5 V	5.5 V			5.5 V	5.5 V	4.5 V				
			2	0.7 V	4 mA	5.5 V	5.5 V	5.5 V												
			3	2.0 V	4 mA	5.5 V	5.5 V	5.5 V												
			4	0.7 V	4 mA	5.5 V	5.5 V	5.5 V												
			5	2.0 V	4 mA	5.5 V	5.5 V	5.5 V												
			6	0.7 V	4 mA	5.5 V	5.5 V	5.5 V												
			7	2.0 V	4 mA	5.5 V	5.5 V	5.5 V												
2	$I_{CEX}$		9	0.7 V	5.5 V															
			10	2.0 V	5.5 V															
			11	0.7 V	5.5 V															
			12	2.0 V	5.5 V															
			13	0.7 V	5.5 V															
			14	2.0 V	5.5 V															
			15	0.7 V	5.5 V															
			16	2.0 V	5.5 V															
3	$V_{IC}$		17	-18 mA																
			18	-18 mA																
			19	-18 mA																
			20	-18 mA																
			21	-18 mA																
			22	-18 mA																
			23	-18 mA																
			24	-18 mA																
4	$I_{IH1}$	3010	25	2.7 V	GND															
			26	2.7 V	GND															
			27	GND																
			28	2.7 V	GND															
			29	GND																
			30	2.7 V	GND															
			31	GND																
			32	2.7 V	GND															
5	$I_{IH2}$		33	5.5 V																
			34	GND																
			35	5.5 V																
			36	GND																
			37	5.5 V																
			38	GND																
			39	5.5 V																
			40	GND																
6	$I_{IL} \frac{2}{}$	3009	41	0.4 V	5.5 V															
			42	5.5 V	0.4 V															
			43	0.4 V	5.5 V															
			44	5.5 V	0.4 V															
			45	0.4 V	5.5 V															
			46	5.5 V	0.4 V															
			47	0.4 V	5.5 V															
			48	5.5 V	0.4 V															
7	$I_{CC}$	3005	49	GND	4.5 V															
2	Same tests, terminal conditions, and limits as for subgroup 1, except $T_C = +125^\circ\text{C}$ and $V_{IC}$ tests are omitted.																			
3	Same tests, terminal conditions, and limits as for subgroup 1, except $T_C = -55^\circ\text{C}$ and $V_{IC}$ tests are omitted.																			

See footnotes at end of device type 03.



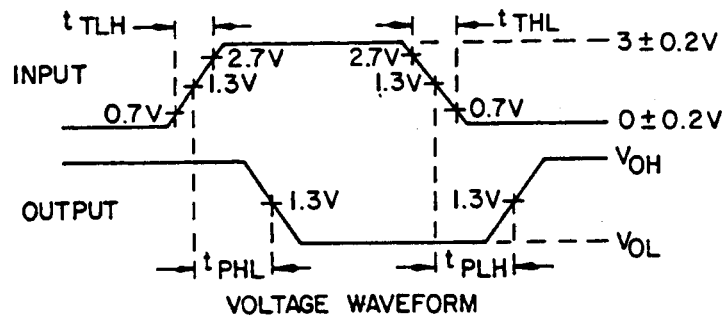
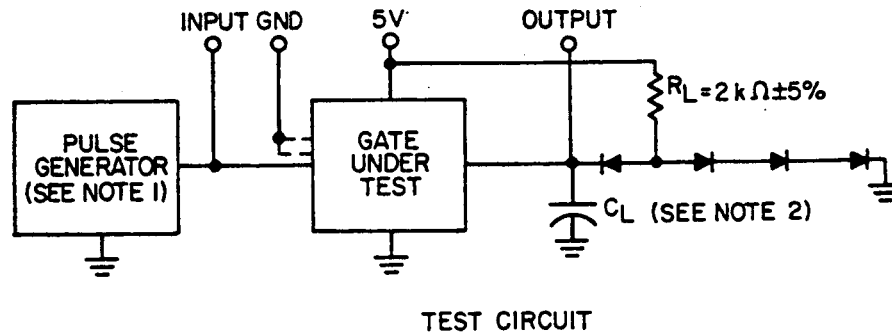
TABLE III. Group A inspection for device type 03 - Continued.  
Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

Subgroup	Symbol	MIL-STD-883 X.2 I/ method	Cases A, B, C, D, X.2 I/	Cases														Limits	Unit
				1	2	3	4	5	6	7	8	9	10	11	12	13	14		
			Test no.	1A	1B	1Y	2Y	2A	2B	2C	3A	3B	3Y	4Y	4A	4B	4C		
10 $T_C = -125^\circ\text{C}$	ΦLH2	(Fig. 4)	106	IN	2.7 V	OUT	OUT	IN	2.7 V	GND	IN	2.7 V	OUT	OUT	IN	2.7 V	IN		
			107	IN	2.7 V	IN	OUT	OUT	IN	2.7 V	GND	IN	2.7 V	OUT	OUT	IN	2.7 V	IN	
			108	IN	2.7 V	IN	OUT	OUT	IN	2.7 V	GND	IN	2.7 V	OUT	OUT	IN	2.7 V	IN	
			109	IN	2.7 V	IN	OUT	OUT	IN	2.7 V	GND	IN	2.7 V	OUT	OUT	IN	2.7 V	IN	
			110	IN	2.7 V	IN	OUT	OUT	IN	2.7 V	GND	IN	2.7 V	OUT	OUT	IN	2.7 V	IN	
			111	IN	2.7 V	IN	OUT	OUT	IN	2.7 V	GND	IN	2.7 V	OUT	OUT	IN	2.7 V	IN	
			112	IN	2.7 V	IN	OUT	OUT	IN	2.7 V	OUT	OUT	IN	2.7 V	IN	2.7 V	IN		
			113	IN	2.7 V	IN	OUT	OUT	IN	2.7 V	OUT	OUT	IN	2.7 V	IN	2.7 V	IN		
11	Same tests, terminal conditions, and limits as for subgroup 10, except $T_C = -55^\circ\text{C}$ .																		

1/ Cases X and 2 pins not referenced are N/C.  
2/ IIL limits in  $\mu\text{A}$  shall be as follows:

Parameters	Circuits						
	A	B	C	D	E	F	G
IIL	-200/-680	-230/-600	-275/-600	-230/-600	-300/-760	-200/-720	-300/-760



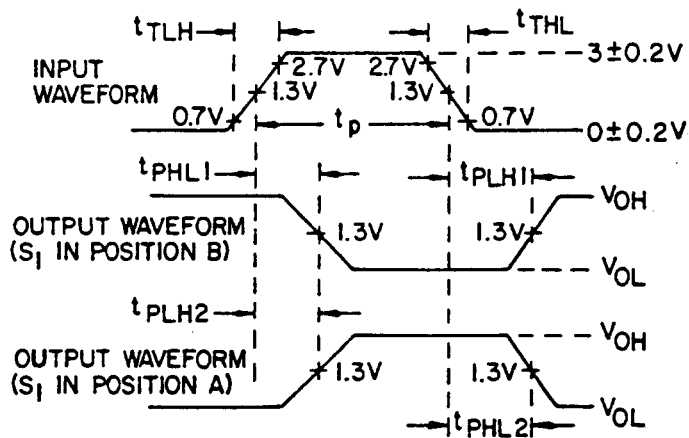
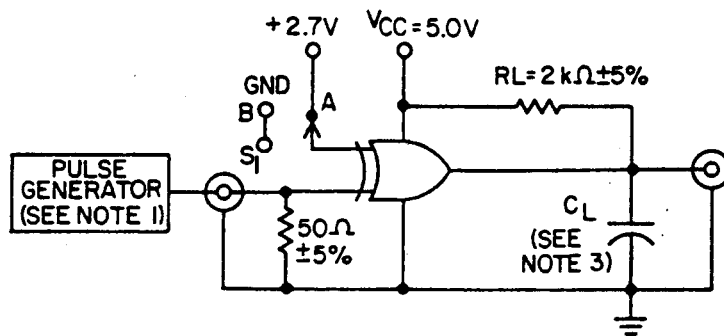
Device types 01 and 02

## NOTES:

1. Pulse generator has the following characteristics:  $t_{THL} \leq 6\text{ ns}$ ,  $t_{TLH} < 15\text{ ns}$ ,  $PRR < 1\text{ MHz}$ ,  $t_p = .5\text{ }\mu\text{s}$ ,  $Z_{out} = 50\Omega$ .
2.  $C_L = 50\text{ pF} \pm 10\text{ percent}$ , including scope probe, wiring, and stray capacitance, without package in test fixture.
3. Voltage measurements are to be made with respect to network ground terminal.
4. Diodes are 1N3064 or equivalent.

FIGURE 4. Switching time test circuit and waveforms for device types 01 through 03.

## Device type 03



VOLTAGE WAVEFORM

## NOTES:

1. The generator has the following characteristics:  $t_{THL} \leq 6$  ns,  $t_{TLH} \leq 15$  ns,  $t_p = .5$   $\mu$ s,  $PRR \leq 1$  MHz,  $Z_{out} = 50\Omega$ .
2. All diodes are 1N3064, or equivalent.
3.  $C_L = 50$  pF  $\pm 10\%$ , including scope probe, wiring, and stray capacitance, without package in test fixture.
4. Voltage measurements are to be made with respect to network ground terminal.

FIGURE 4. Switching time test circuit and waveforms for device types 01 through 03 - Continued.

4.5 Methods of inspection. Methods of inspection shall be as follows.

4.5.1 Voltage and current. All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.2 Ordering data. The acquisition document should specify the following:

- a. Complete part number (see 1.2).
- b. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
- c. Requirements for certificate of compliance, if applicable.
- d. Requirements for notification of change of product or process to the contracting activity in addition to notification to qualifying activity, if applicable.
- e. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
- f. Requirements for product assurance options.
- g. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements shall not affect the part numbers. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
- h. Requirements for "JAN" marking.

6.3 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-M-38510, MIL-STD-1331, and as follows:

GND	- - - - -	Ground zero voltage potential.
I <sub>IN</sub>	- - - - -	Current flowing into an input terminal.
V <sub>IN</sub>	- - - - -	Voltage level at an input terminal.

6.4 Logistic support. Lead materials and finishes (see 3.3) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2) and lead material for finish C (see 3.3). Longer length leads and lead forming shall not affect the part number.

6.5 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information shall not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-M-38510.

<u>Military device type</u>	<u>Generic-industry type</u>
01	54LS02
02	54LS27
03	54LS266

6.6 Manufacturers' designation. Manufacturers' circuits which form a part of this specification are designated as shown in table IV herein.

TABLE IV. Manufacturer's designation.

Military device types	Manufacturer					
	Texas Instruments	Signetics Corp.	National Company	Raytheon Company	Motorola Inc.	Fairchild Semiconductor
01	A	B	C	G	E	F
02	A	B	C	F	G	E
03	A	B	C	D	E	F

6.7 Changes from previous issue. Asterisks are not used in this revision to identify changes with respect to the previous issue, due to the extensiveness of the changes.

**Custodians:**

Army - ER  
Navy - EC  
Air Force - 17

**Preparing activity:**

Air Force - 17

**Agent:**

DLA - ES

(Project 5962-1081)

**Review activities:**

Army - AR, MI  
Navy - SH, OS  
Air Force - 11, 19, 85, 99  
DLA - ES

**User activities:**

Army - SM  
Navy - AS, CG, MC