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April 1st, 2010 Renesas Electronics Corporation

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HD74LV1GW17A

Dual Buffer with Schmitt Trigger Inputs

REJ03D0079-0200 Rev.2.00 May 19, 2006

Description

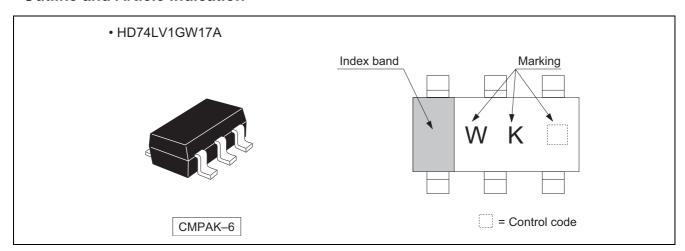
The HD74LV1GW17A has dual buffer with schmitt–trigger input in a 6 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range: 1.65 to 5.5 V
 Operating temperature range: -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1GW17ACME	CMPAK-6 pin	PTSP0006JA-A (CMPAK-6V)	СМ	E (3,000 pcs / Reel)

Outline and Article Indication



Function Table

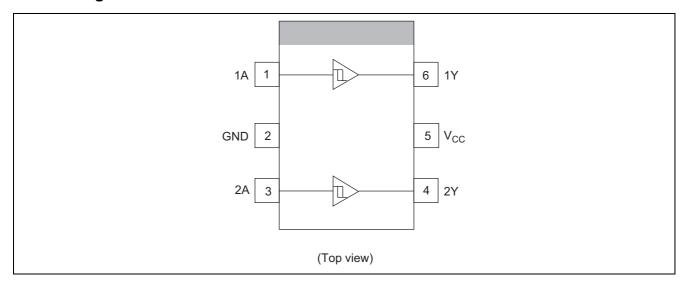
Input A	Output Y
Н	Н
L	L

H : High level L : Low level





Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2	V	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
Output voltage range	Vo	-0.5 to 7.0	V	V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V _I < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I _O	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through Vcc or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	Vı	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
		_	1		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
	I _{OL}	_	2	- mA	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
Output current		_	12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Output current		_	-1		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
	1	_	-2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
	I _{OH}	_	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

Ta = -40 to $85^{\circ}C$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	_	_	V _{CC} ×0.75		
	V _T ⁺	2.5	_	_	1.75		
	VT	3.3	_	_	2.31		
		5.0	_	_	3.50		
		1.65 to 1.95	V _{CC} ×0.25	_	_		
Threshold	V _T -	2.5	0.75	_	_	V	
voltage	VT	3.3	0.99	_	_	V	
		5.0	1.5	_	_		
		1.65 to 1.95	0.1	_	V _{CC} ×0.4		
	ΔV_{T}	2.5	0.25	-	1.0		
	ΔV_{T}	3.3	0.33	-	1.32		
		5.0	0.5	_	2.0		
		Min to Max	V _{CC} -0.1	-	_		I _{OH} = -50 μA
		1.65	1.4	-	_		$I_{OH} = -1 \text{ mA}$
	V _{OH}	2.3	2.0	-	_		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	-	_		I _{OH} = -6 mA
Output valtage		4.5	3.8		_	V	I _{OH} = -12 mA
Output voltage		Min to Max	_	-	0.1	V	I _{OL} = 50 μA
		1.65	_	-	0.3		I _{OL} = 1 mA
	V_{OL}	2.3	_	-	0.4		I _{OL} = 2 mA
		3.0	_	-	0.44		I _{OL} = 6 mA
		4.5	_	-	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	-	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent	1	5.5			10	^	$V_{IN} = V_{CC}$ or GND,
supply current	I _{CC}	5.5		1	10	μΑ	$I_O = 0$
Output leakage current	I _{OFF}	0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

 $V_{CC}=1.8\pm0.15\ V$

Item	Symbol	-	Га = 25°C	;	Ta = -40	to 85°C	Unit	Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Onit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	16.8	32.0	1.0	34.0	20	$C_L = 15 pF$	۸	V
delay time	t _{PHL}		23.8	43.0	1.0	46.0	ns	$C_L = 50 pF$	A	ī

 $V_{CC}=2.5\pm0.2~V$

Item	Symbol	-	Га = 25°C	;	Ta = -40) to 85°C	Unit	Test	FROM	ТО
iteiii	Symbol	Min	Тур	Max	Min	Max	Onic	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	10.5	19.7	1.0	22.0	nc	$C_L = 15 pF$	۸	V
delay time	t _{PHL}	_	14.0	24.0	1.0	27.0	ns	$C_L = 50 pF$	Α	ı

 $V_{CC}=3.3\pm0.3\ V$

Item	Symbol	Ta = 25°C Ta = -4		Ta = -40	to 85°C	Unit	Test	FROM	ТО	
itein	Syllibol	Min	Тур	Max	Min	Max	Onit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	8.3	12.8	1.0	15.0	nc	$C_L = 15 pF$	۸	V
delay time	t _{PHL}	_	10.8	16.3	1.0	18.5	ns	$C_L = 50 pF$	A	ı

 $V_{CC}=5.0\pm0.5~V$

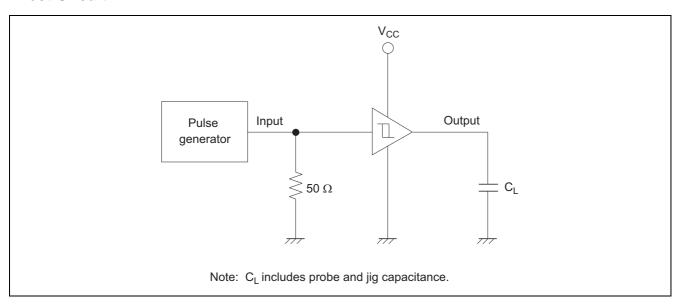
Item	Symbol	Ta = 25°C Ta = -40 to 85°C		Unit	Test	FROM	ТО			
iteiii	Syllibol	Min	Тур	Max	Min	Max	Onit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.5	8.6	1.0	10.0	nc	C _L = 15 pF	۸	V
delay time	t _{PHL}	_	7.0	10.6	1.0	12.0	ns	C _L = 50 pF	A	ı

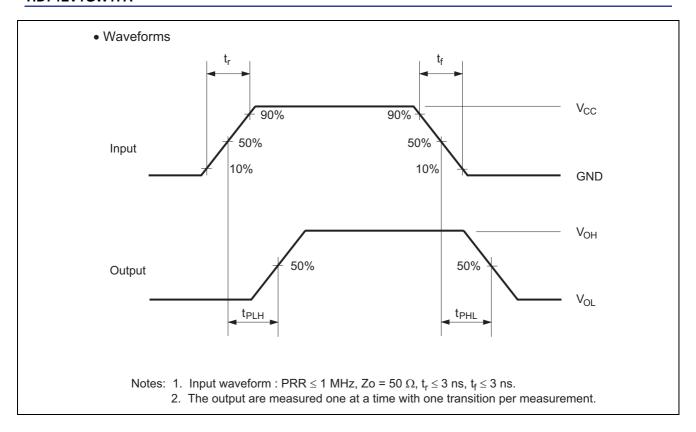
Operating Characteristics

 $C_L = 50 \text{ pF}$

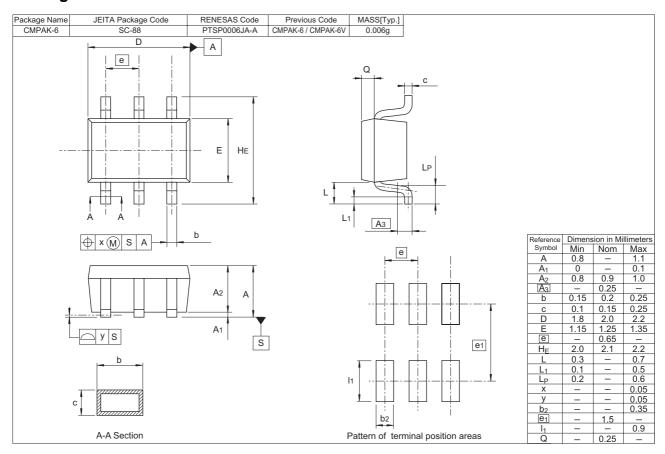
Item	Symbol	V (\(\)		Ta = 25°C	;	Unit	Test Conditions	
item	Syllibol	Symbol V _{CC} (V)		Тур	Max	Onit	rest Conditions	
Power dissipation	C	3.3	_	8.5	_	pF	f = 10 MHz	
capacitance	C _{PD}	5.0	_	10.0	_	ρг		

Test Circuit





Package Dimensions



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