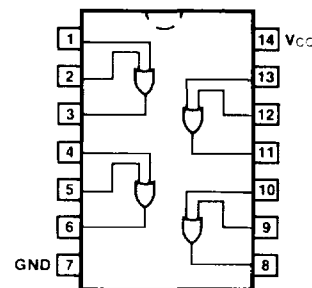
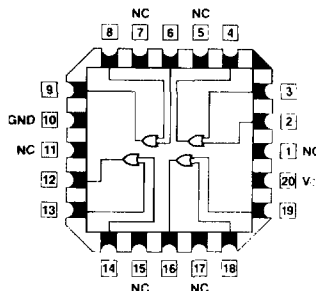


54F/74F32

Quad 2-Input OR Gate

Connection Diagrams



Ordering Code: See Section 5

Pin Assignment
for LCC and PCCPin Assignment
for DIP and SOIC

Input Loading/Fan-Out: See Section 3 for U.L. definitions

Pin Names	Description	54F/74F(U.L.) HIGH/LOW
	Inputs	0.5/0.375
	Outputs	25/12.5

DC Characteristics over Operating Temperature Range (unless otherwise specified)

Symbol	Parameter	54F/74F			Units	Conditions	
		Min	Typ	Max		$V_{IN} = \text{Open}$	$V_{CC} = \text{Max}$
I_{CCH}	Power Supply Current		6.1	9.2	mA	$V_{IN} = \text{Open}$	$V_{CC} = \text{Max}$
I_{CCL}			10.3	15.5		$V_{IN} = \text{Gnd}$	

AC Characteristics: See Section 3 for waveforms and load configurations

Symbol	Parameter	54F/74F			54F		74F		Units	Fig. No.
		$T_A = +25^\circ\text{C}$ $V_{CC} = +5.0\text{ V}$ $C_L = 50\text{ pF}$			$T_A, V_{CC} = \text{Mil}$ $C_L = 50\text{ pF}$		$T_A, V_{CC} = \text{Com}$ $C_L = 50\text{ pF}$			
		Min	Typ	Max	Min	Max	Min	Max		
t_{PLH}	Propagation Delay	3.0	4.2	5.6	3.0	7.5	3.0	6.6	ns	3-1
t_{PHL}		3.0	4.0	5.3	2.5	7.5	3.0	6.3		3-4