TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK2599

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON-resistance :  $R_{DS\ (ON)}$  = 2.9  $\Omega$  (typ.)

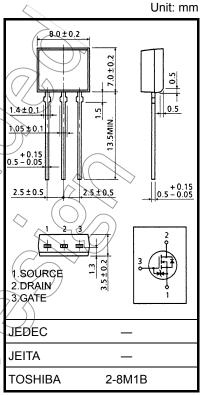
• High forward transfer admittance :  $|Y_{fs}| = 1.7 \text{ S (typ.)}$ 

Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 500 V)

Enhancement mode : V<sub>th</sub> = 2.0 to 4.0 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 1 mA)

## Absolute Maximum Ratings (Ta = 25°C)

Charac	teristics	Symbol	Rating	Unit	
Drain-source volta	ge	$V_{DSS}$	500	A	
Drain-gate voltage	(R <sub>GS</sub> = 20 kΩ)	$V_{DGR}$	500	y	
Gate-source voltage	ge	$V_{GSS}$	±30	> v	
Drain current	DC (Note 1)	ΙD	2	Α	
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	5	A	
	Pulse (t = 100 µs) (Note 1)	I <sub>DP</sub>	12	A	
Drain power dissipa	ation	PD	)) 1.3	w	
Single pulse avalar	nche energy (Note 2)	EAS	112	mJ	
Avalanche current		JAR	2	A	
Repetitive avalanch	ne energy (Note 3)	//ÈAR	0.13	mJ	
Channel temperatu	ire	→t <sub>ch</sub>	150	°C	
Storage temperatu	re range	→ T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.54 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	96.1	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 48.4 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 2 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

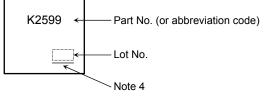
## **Electrical Characteristics (Ta = 25°C)**

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source br	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	/	_	100	μΑ
Drain-source br	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	) >-	4.0	V
Drain-source O	N-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1 A	<u> </u>	2.9	3.2	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 A	0.8	1.7	_	S
Input capacitance		C <sub>iss</sub>		_	380	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	40	_	pF
Output capacitance		Coss		_	120	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 10V$ $V_{OUT}$ $V_{OUT}$ $R_{L} = 200\Omega$	- (	15	>	
	Turn-on time	t <sub>on</sub>		M	25	) –	
	Fall time	t <sub>f</sub>	y = 200y	7	20	_	ns
	Turn-off time	t <sub>off</sub>	$V_{DD} = 200V$ Duty $\leq 1\%$ , $t_{w} = 10 \mu s$	) –	80	_	
Total gate charg	ge (Gate-source )	Qg		_	9	_	
Gate-source ch	arge	Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, V_{D} = 2 \text{ A}$	_	5	_	nC
Gate-drain ("mi	ller") charge	Q <sub>gd</sub>		_	4	_	

# Source-Drain Ratings and Characteristics (Ta ≠25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	1 <sub>DR</sub>		_	-	2	Α
Pulse drain reverse current (Note 1)	) I <sub>DRP</sub>	t=1 ms	_		5	Α
	I <sub>DRP</sub>	t = 100 µs			12	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V	_	-	-1.5	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V dI <sub>DR</sub> / dt = 100 A / μs	_	1000	_	ns
Reverse recovered charge	Qrr		_	3.5	_	μC

## Marking

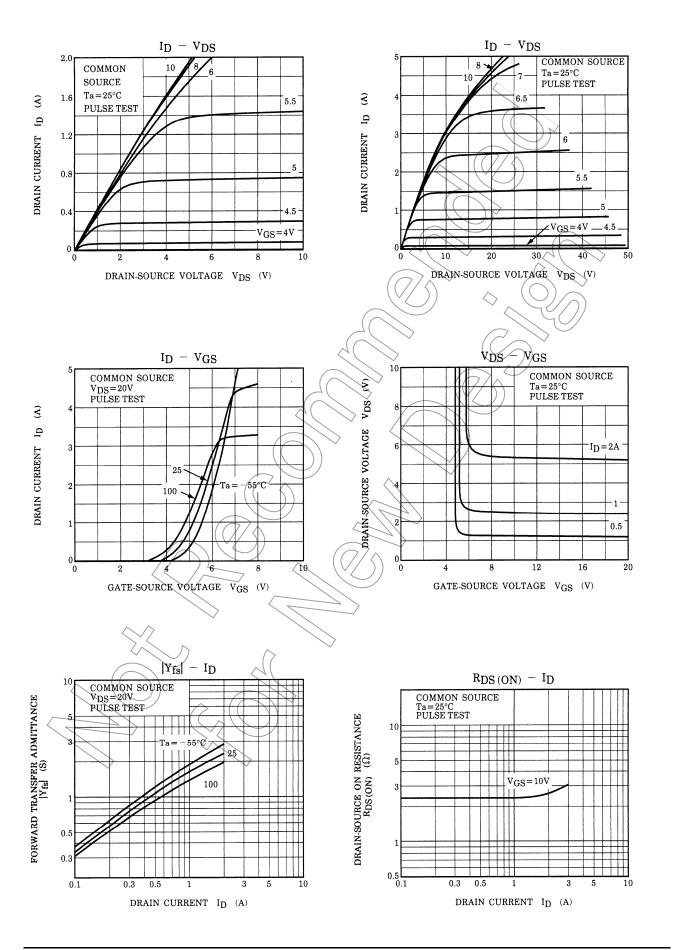


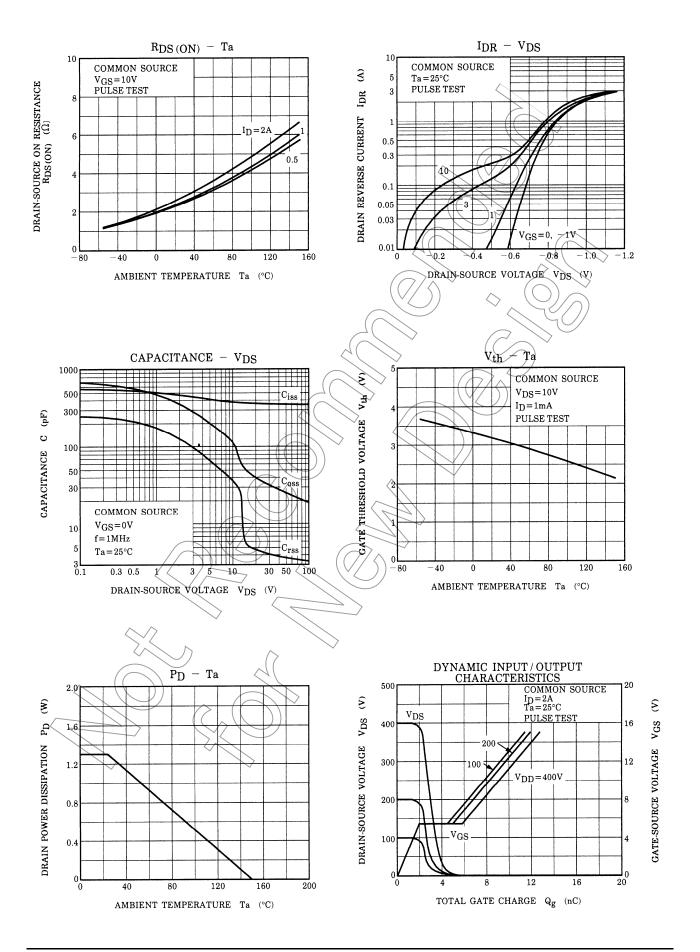
Note 4: A line under a Lot No. identifies the indication of product Labels.

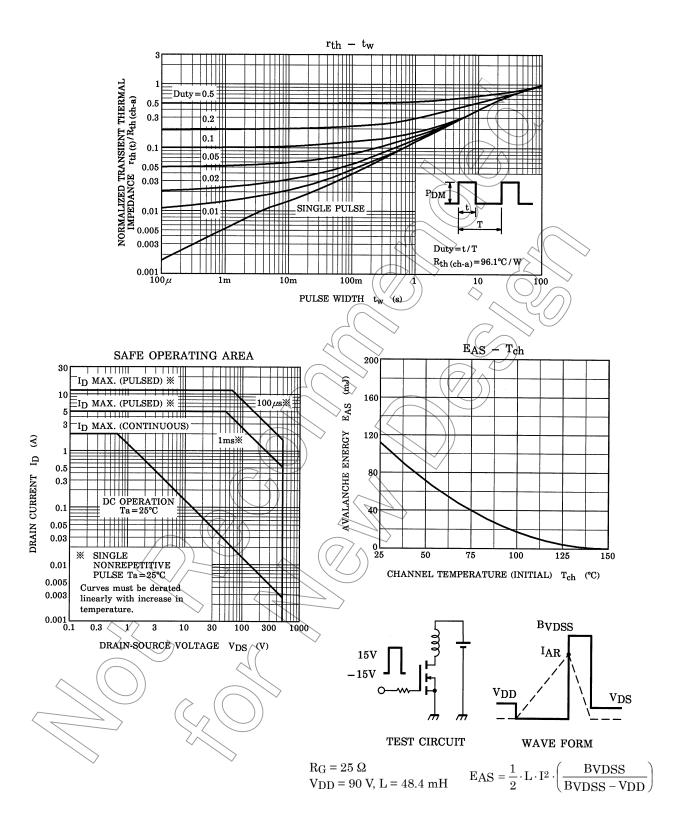
Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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