

# **Rochester Electronics Manufactured Components**

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

# **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
  - Class Q Military
  - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)

• Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

September 1986	
Revised March 2000	

DM74AS00 Quad 2-Input NAND Gate

# FAIRCHILD

SEMICONDUCTOR

# DM74AS00 Quad 2-Input NAND Gate

#### **General Description**

This device contains four independent gates, each of which performs the logic NAND function.

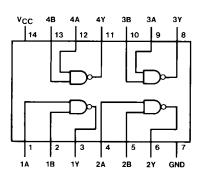
# Features

- Switching specifications at 50 pF
- Switching specifications guaranteed over full temperature and V<sub>CC</sub> range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky, low power Schottky, and advanced low power Schottky TTL counterpart
- Improved AC performance over Schottky, low power Schottky, and advanced low power Schottky counterparts

## **Ordering Code:**

Order Number	Package Number	Package Description				
DM74AS00M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow				
DM74AS00N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide				
Devices also available	Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.					

#### **Connection Diagram**



# **Function Table**

 $\mathbf{Y} = \overline{\mathbf{AB}}$ 

Inp	Inputs		
Α	В	Y	
L	L	Н	
L	н	Н	
н	L	Н	
н	н	L	

H = HIGH Logic Level L = LOW Logic Level

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# Absolute Maximum Ratings(Note 1)

7V
7V
$0^{\circ}C$ to $+70^{\circ}C$
$-65^{\circ}C$ to $+150^{\circ}C$
84.0°C/W
114.0°C/W

Note 1: The Absolute Maximum Ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The Recommended Operating Conditions table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
VIH	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
I <sub>OH</sub>	HIGH Level Output Current			-2	mA
I <sub>OL</sub>	LOW Level Output Current			20	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

#### **Electrical Characteristics**

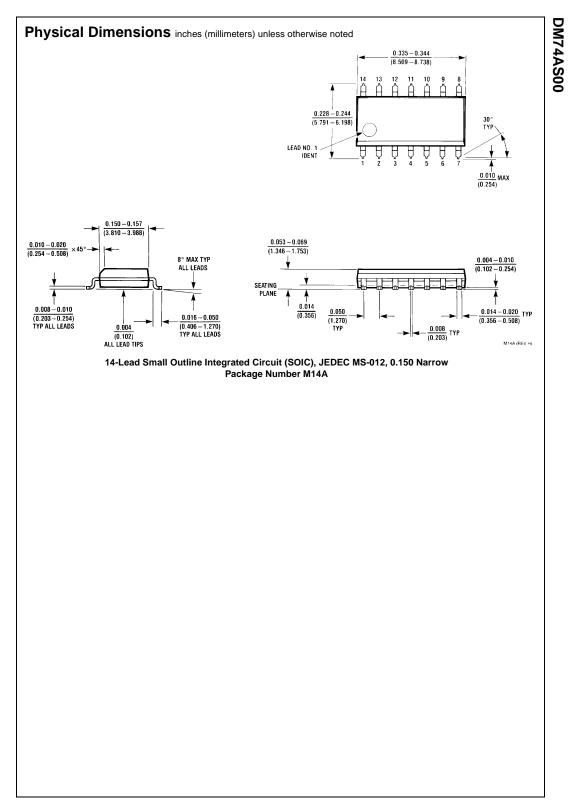
over recommended operating free air temperature range. All typical values are measured at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

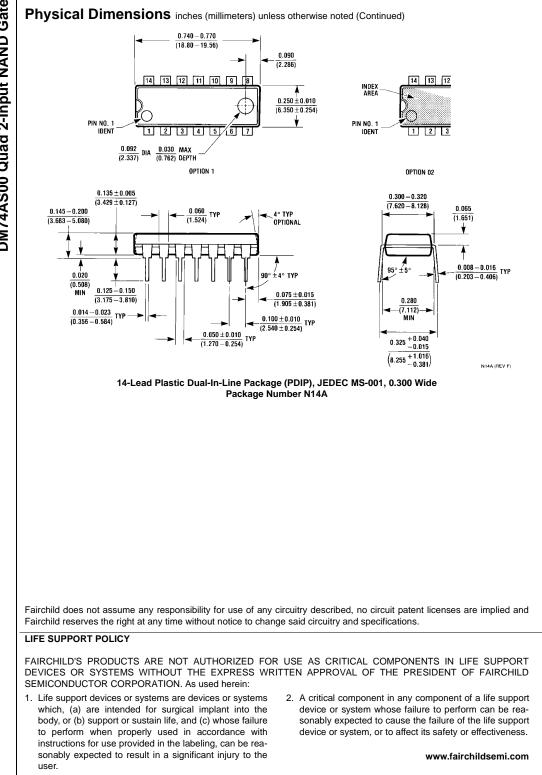
Symbol	Parameter	Condition	IS	Min	Тур	Max	Units
V <sub>IK</sub>	Input Clamp Voltage	$V_{CC} = 4.5V, I_I = -18 \text{ mA}$				-1.2	V
V <sub>OH</sub>	HIGH Level	I <sub>OH</sub> = -2 mA		V 2			V
	Output Voltage	$V_{CC} = 4.5V$ to 5.5V		V <sub>CC</sub> – 2			
V <sub>OL</sub>	LOW Level	$V_{CC} = 4.5V$			0.35	0.5	V
	Output Voltage	$I_{OL} = 20 \text{ mA}$			0.55		
l <sub>l</sub>	Input Current at Max Input Voltage	$V_{CC} = 5.5V, V_{IH} = 7V$				0.1	mA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μΑ
Ι <sub>ΙL</sub>	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.5	mA
I <sub>O</sub>	Output Drive Current	$V_{CC} = 5.5 V, V_{O} = 2.25 V$		-30		-112	mA
I <sub>CC</sub>	Supply Current	$V_{CC} = 5.5V$	Outputs HIGH		2.2	3.2	mA
			Outputs LOW		10.8	17.4	mA

# Switching Characteristics

Symbol	Parameter	Conditions	Min	Max	Units
1 211	Propagation Delay Time LOW-to-HIGH Level Output	$V_{CC} = 4.5V$ to 5.5V R <sub>L</sub> = 500 $\Omega$	1	4.5	ns
1116	Propagation Delay Time HIGH-to-LOW Level Output	C <sub>L</sub> = 50 pF	1	4	ns

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