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April 1st, 2010 Renesas Electronics Corporation

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RENESAS HD74LVU04A

Hex Inverters

REJ03D0228-0200Z (Previous ADE-205-248 (Z)) Rev.2.00 May 21, 2004

Description

The HD74LVU04A has six inverters with unbuffered outputs in a 14-pin package.

Low-voltage and high-speed operation is suitable for the battery-powered products (e.g., notebook computers), and the low-power consumption extends the battery life.

Features

- $V_{CC} = 2.0 \text{ V}$ to 5.5 V operation
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
- All outputs V_0 (Max.) = 5.5 V (@V_{CC} = 0 V)
- Typical V_{OL} ground bounce < 0.8 V (@V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.3 V (@V_{CC} = 3.3 V, Ta = 25° C)
- Output current $\pm 6 \text{ mA} (@V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}), \pm 12 \text{ mA} (@V_{CC} = 4.5 \text{ V to } 5.5 \text{ V})$
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LVU04AFPEL	SOP-14 pin(JEITA)	FP–14DAV	FP	EL (2,000 pcs/reel)
HD74LVU04ARPEL	SOP-14 pin(JEDEC)	FP-14DNV	RP	EL (2,500 pcs/reel)
HD74LVU04ATELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Function Table

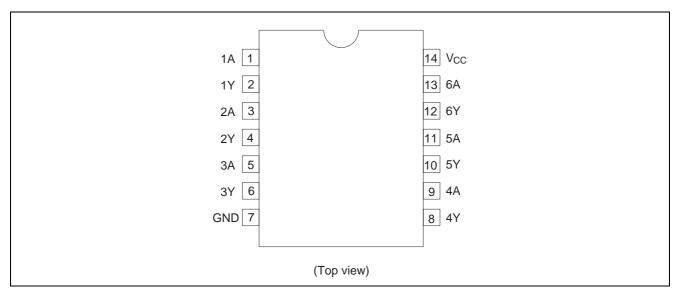
Input A	Output Y	
Н	L	
L	Н	

Note: H: High level

L: Low level



Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V _{CC}	–0.5 to 7.0	V	
Input voltage range*1	VI	–0.5 to 7.0	V	
Output voltage range*1, 2	Vo	-0.5 to V _{CC} + 0.5	V	Output: H or L
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	l _{ок}	±50	mA	$V_0 < 0 \text{ or } V_0 > V_{CC}$
Continuous output current	lo	±25	mA	$V_{O} = 0$ to V_{CC}
Continuous current through	I_{CC} or I_{GND}	±50	mA	
V _{CC} or GND				
Maximum power dissipation at	PT	785	mW	SOP
Ta = 25°C (in still air)* ³		500		TSSOP
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

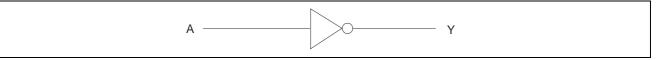
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	Vcc	2.0	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
Output current	I _{OH}		-50	μΑ	$V_{CC} = 2.0 V$
			-2	mA	V_{CC} = 2.3 to 2.7 V
			-6		$V_{CC} = 3.0$ to 3.6 V
		_	-12		$V_{CC} = 4.5$ to 5.5 V
	I _{OL}	_	50	μA	$V_{CC} = 2.0 V$
		_	2	mA	V_{CC} = 2.3 to 2.7 V
		_	6		$V_{CC} = 3.0$ to 3.6 V
			12		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	Та	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Logic Diagram



DC Electrical Characteristics

							Ta = -40 to 85
Item	Symbol	V _{cc} (V)*	Min	Тур	Max	Unit	Test Conditions
Input voltage	VIH	2.0	1.7		_	V	
		2.3 to 2.7	$V_{CC} \times 0.8$	_	_	_	
		3.0 to 3.6	$V_{CC} \times 0.8$	_	_		
		4.5 to 5.5	$V_{CC} \times 0.8$	_	_		
	VIL	2.0	_	_	0.3		
		2.3 to 2.7	_	_	$V_{CC} \times 0.2$		
		3.0 to 3.6	_	_	$V_{CC} \times 0.2$		
		4.5 to 5.5	_	_	$V_{CC} \times 0.2$		
Output voltage	V _{OH}	Min to Max	$V_{CC} - 0.1$	_	_	V	I _{OL} = -50 μA
		2.3	2.0	—	_		$I_{OL} = -2 \text{ mA}$
		3.0	2.48	—	_		$I_{OL} = -6 \text{ mA}$
		4.5	3.8	_	_		$I_{OL} = -12 \text{ mA}$
	V _{OL}	Min to Max	_	_	0.1	V	I _{OL} = 50 μA
		2.3	_	_	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	_	_	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	_		0.55	_	I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I _{CC}	5.5	_	_	20	μΑ	$V_{IN} = V_{CC}$ or GND, $I_0 = 0$
Input capacitance	CIN	3.3	_	4.0	_	pF	$V_I = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

									V	$T_{\rm CC} = 2.5 \pm 0.2$ V
		Ta =	25°C		Ta = –4	40 to 85°C		Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		3.2	10.9	1.0	14.0	ns	$C_L = 15 \text{ pF}$	А	Y
delay time	t _{PHL}	_	6.6	13.4	1.0	16.0		$C_L = 50 \text{ pF}$		

									V	$T_{\rm CC} = 3.3 \pm 0.3 \text{ V}$
		Ta =	25°C		Ta = -4	40 to 85°C		Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	2.5	8.9	1.0	10.5	ns	C _L = 15 pF	А	Y
delay time	t _{PHL}	_	4.7	11.4	1.0	13.0		$C_L = 50 \text{ pF}$		

									V	$_{\rm CC} = 5.0 \pm 0.5 \ V$
		Ta =	25°C		Ta = –4	40 to 85°C		Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		2.2	5.5	1.0	6.5	ns	$C_L = 15 \text{ pF}$	А	Y
delay time	t _{PHL}	_	3.9	7.0	1.0	8.0		$C_L = 50 \text{ pF}$		

 $V = 5.0 \pm 0.5 V$

Operating Characteristics

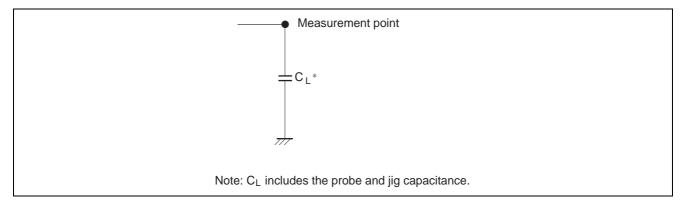
							$C_L = 50 \text{ pF}$
			Ta = 2	5°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	CPD	3.3	_	5.6	_	pF	f = 10 MHz
		5.0	_	6.7	—		

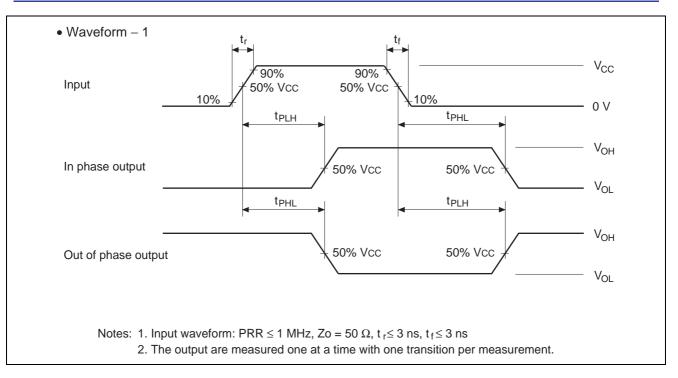
Noise Characteristics

 $C_L = 50 \text{ pF}$

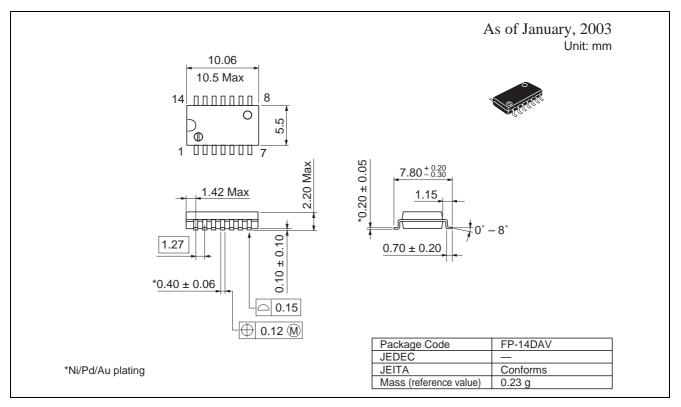
			Ta = 25	5°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test Conditions
Quiet output, maximum dynamic V _{OL}	V _{OL (P)}	3.3	_	0.5	0.8	V	
Quiet output, minimum dynamic V _{OL}	V _{OL (V)}	3.3	—	-0.1	-0.8	V	
Quiet output, minimum dynamic V _{OH}	V _{OH (V)}	3.3	—	3.0	_	V	
High-level dynamic put voltage	V _{IH (D)}	3.3	2.31	—	—	V	
Low-level dynamic put voltage	V _{IL (D)}	3.3	_		0.99	V	

Test Circuit

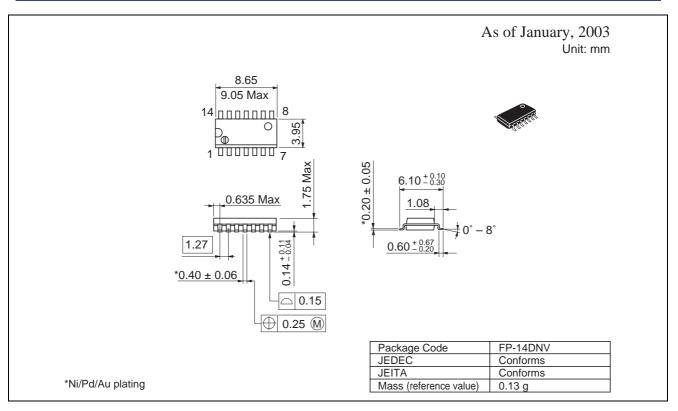


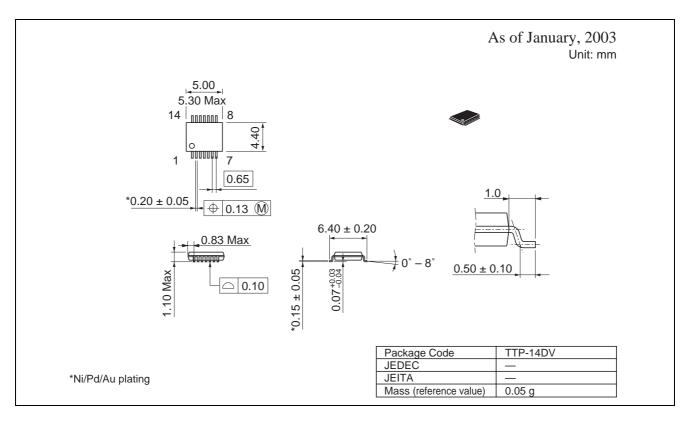


Package Dimensions



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RENESAS

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