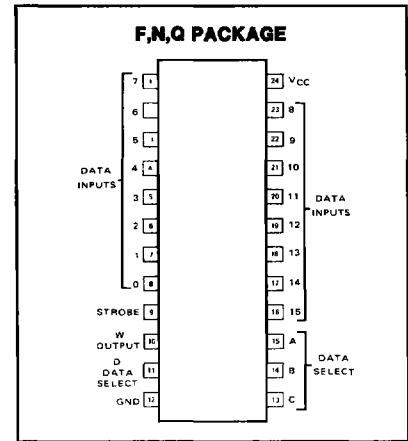


**SPEED/PACKAGE AVAILABILITY**

54 Q,F      74 N,F

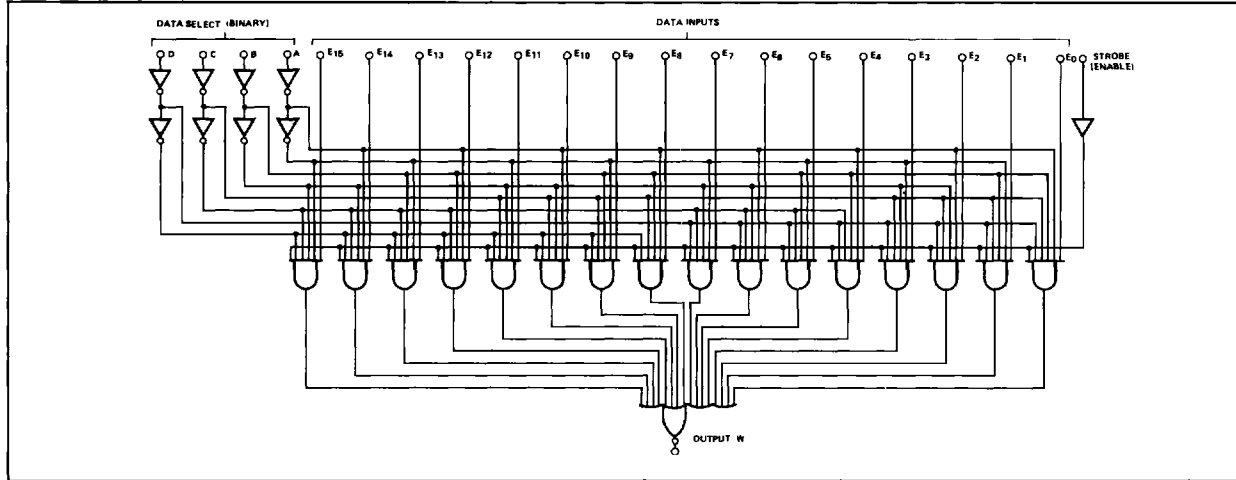
**PIN CONFIGURATION**



**TRUTH TABLE**

				INPUTS																	OUTPUT
D	C	B	A	STROBE	e <sub>0</sub>	E <sub>1</sub>	E <sub>2</sub>	E <sub>3</sub>	E <sub>4</sub>	E <sub>5</sub>	E <sub>6</sub>	E <sub>7</sub>	E <sub>8</sub>	E <sub>9</sub>	E <sub>10</sub>	E <sub>11</sub>	E <sub>12</sub>	E <sub>13</sub>	E <sub>14</sub>	E <sub>15</sub>	W
X	X	X	X	1	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1
0	0	0	0	0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1
0	0	0	0	0	0	1	X	X	X	X	X	X	X	X	X	X	X	X	X	X	0
0	0	0	1	0	0	X	0	X	X	X	X	X	X	X	X	X	X	X	X	X	1
0	0	0	1	0	0	X	1	X	X	X	X	X	X	X	X	X	X	X	X	X	0
0	0	1	0	0	0	X	X	0	X	X	X	X	X	X	X	X	X	X	X	X	1
0	0	1	0	0	0	X	X	1	X	X	X	X	X	X	X	X	X	X	X	X	0
0	0	1	1	0	0	X	X	X	0	X	X	X	X	X	X	X	X	X	X	X	1
0	0	1	1	0	0	X	X	X	1	X	X	X	X	X	X	X	X	X	X	X	0
0	1	0	0	0	0	X	X	X	X	0	X	X	X	X	X	X	X	X	X	X	1
0	1	0	0	0	0	X	X	X	X	1	X	X	X	X	X	X	X	X	X	X	0
0	1	0	1	0	0	X	X	X	X	X	0	X	X	X	X	X	X	X	X	X	1
0	1	0	1	0	0	X	X	X	X	1	X	X	X	X	X	X	X	X	X	X	0
0	1	1	0	0	0	X	X	X	X	X	0	X	X	X	X	X	X	X	X	X	1
0	1	1	0	0	0	X	X	X	X	X	1	X	X	X	X	X	X	X	X	X	0
0	1	1	1	0	0	X	X	X	X	X	X	0	X	X	X	X	X	X	X	X	1
0	1	1	1	0	0	X	X	X	X	X	X	1	X	X	X	X	X	X	X	X	0
1	0	0	0	0	0	X	X	X	X	X	X	X	0	X	X	X	X	X	X	X	1
1	0	0	0	0	0	X	X	X	X	X	X	X	1	X	X	X	X	X	X	X	0
1	0	0	1	0	0	X	X	X	X	X	X	X	X	0	X	X	X	X	X	X	1
1	0	0	1	0	0	X	X	X	X	X	X	X	X	1	X	X	X	X	X	X	0
1	0	1	0	0	0	X	X	X	X	X	X	X	X	X	0	X	X	X	X	X	1
1	0	1	0	0	0	X	X	X	X	X	X	X	X	X	1	X	X	X	X	X	0
1	0	1	1	0	0	X	X	X	X	X	X	X	X	X	X	0	X	X	X	X	1
1	0	1	1	0	0	X	X	X	X	X	X	X	X	X	X	1	X	X	X	X	0
1	1	0	0	0	0	X	X	X	X	X	X	X	X	X	X	X	0	X	X	X	1
1	1	0	0	0	0	X	X	X	X	X	X	X	X	X	X	X	1	X	X	X	0
1	1	0	1	0	0	X	X	X	X	X	X	X	X	X	X	X	X	0	X	X	1
1	1	0	1	0	0	X	X	X	X	X	X	X	X	X	X	X	X	1	X	X	0
1	1	1	0	0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	0	X	1
1	1	1	0	0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	1	X	0
1	1	1	1	0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	0	1
1	1	1	1	0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1	0
1	1	1	1	0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	0	1
1	1	1	1	1	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1	0
1	1	1	1	1	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	0	1

LOGIC DIAGRAM



SWITCHING CHARACTERISTICS  $V_{CC} = 5V, T_A = 25^\circ C$

TEST CONDITIONS			54/74			UNIT
			$C_L = 15pF$ $R_L = 400\Omega$			
PARAMETER	FROM INPUT	TO OUTPUT	MIN	TYP	MAX	
Propagation delay time						
t <sub>PLH</sub> Low-to-high	A,B,C (4 levels)	Y		35	52	ns
t <sub>PHL</sub> High-to-low				20	30	
t <sub>PLH</sub> Low-to-high	A,B,C,D (3 levels)	W		23	35	
t <sub>PHL</sub> High-to-low				22	33	
t <sub>PLH</sub> Low-to-high	Strobe	Y		35	52	
t <sub>PHL</sub> High-to-low				19	30	
t <sub>PLH</sub> Low-to-high	Strobe	W		15.5	24	
t <sub>PHL</sub> High-to-low				21	30	
t <sub>PLH</sub> Low-to-high	D <sub>0</sub> - D <sub>7</sub>	Y		19	29	
t <sub>PHL</sub> High-to-low				16	24	
t <sub>PLH</sub> Low-to-high	E <sub>0</sub> - E <sub>15</sub>	W		13	20	
t <sub>PHL</sub> High-to-low				8.5	14	

Load circuit and typical waveforms are shown at the front of section.

10101