# SILICON TRANSISTOR 2SC1623

# NPN SILICON EPITAXIAL TRANSISTOR MINI MOLD

### FEATURES

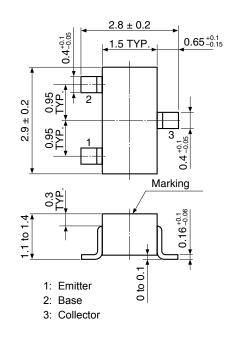
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- High DC Current Gain: hFE = 200 TYP. (VcE = 6.0 V, lc = 1.0 mA)
- High Voltage: VCEO = 50 V

#### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Collector to Base Voltage	Vсво	60	V
Collector to Emitter Voltage	VCEO	50	V
Emitter to Base Voltage	Vebo	5.0	V
Collector Current (DC)	lc	100	mA
Total Power Dissipation	Р⊤	200	mW
Junction Temperature	Tj	150	°C
Storage Temperature Range	Tstg	–55 to +150	°C

#### <R> PACKAGE DRAWING (Unit: mm)



## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cut-off Current	Ісво			0.1	μA	V <sub>CB</sub> = 60 V, I <sub>E</sub> = 0 A
Emitter Cut-off Current	Іево			0.1	μA	V <sub>EB</sub> = 5.0 V, I <sub>C</sub> = 0 A
DC Current Gain	hfe	90	200	600		$V_{CE}$ = 6.0 V, I <sub>C</sub> = 1.0 mA <sup>Note</sup>
Collector Saturation Voltage	V <sub>CE(sat)</sub>		0.15	0.3	V	$I_{\rm C}$ = 100 mA, $I_{\rm B}$ = 10 mA <sup>Note</sup>
Base to Saturation Voltage	V <sub>BE(sat)</sub>		0.86	1.0	V	Ic = 100 mA, I <sub>B</sub> = 10 mA <sup>Note</sup>
Base to Emitter voltage	VBE	0.55	0.62	0.65	V	$V_{CE}$ = 6.0 V, I <sub>C</sub> = 1.0 mA <sup>Note</sup>
Gain Bandwidth Product	f⊤		250		MHz	V <sub>CE</sub> = 6.0 V, I <sub>E</sub> = -10 mA
Output Capacitance	Cob		3.0		pF	$V_{CB}$ = 6.0 V, I <sub>E</sub> = 0 A, f = 1.0 MHz

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**Note** Pulsed: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2%

#### **hfe CLASSIFICATION**

Marking	L4	L5	L6	L7
hfe	90 to 180	135 to 270	200 to 400	300 to 600

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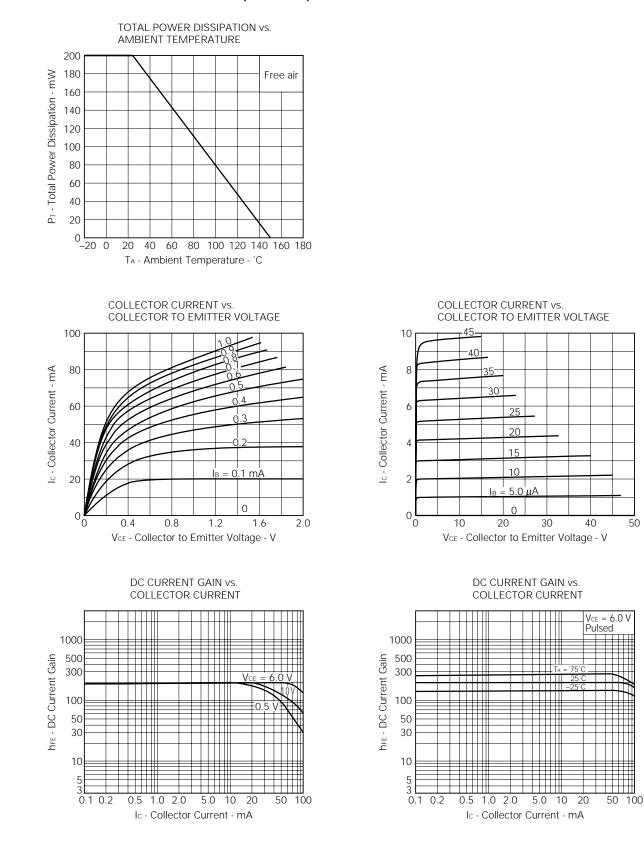
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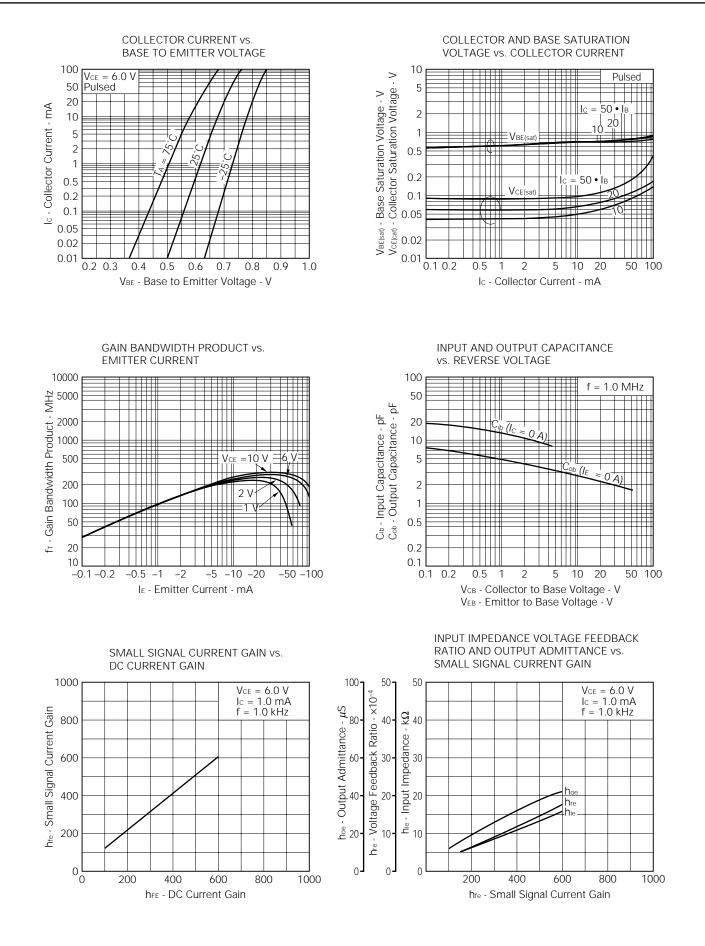
The mark <R> shows major revised points.

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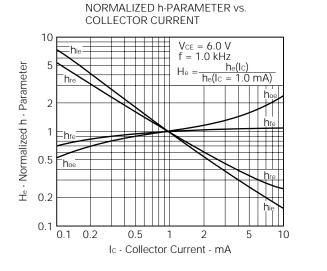


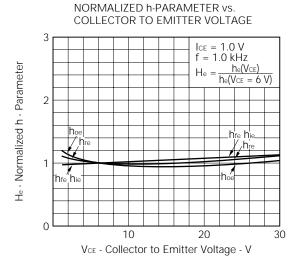


#### TYPICAL CHARACTERISTICS ( $T_A = 25^{\circ}C$ ) <R>



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