

# MBRD1020CT - MBRD10100CT

#### **10A SURFACE MOUNT DUAL SCHOTTKY BARRIER RECTIFIER**

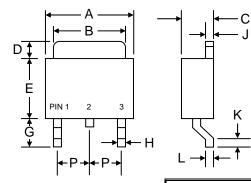


#### Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Surge Current Capability
- Low Power Loss, High Efficiency
- Ideally Suited for Automatic Assembly
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications

#### **Mechanical Data**

- Case: DPAK/TO-252, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.3 grams (approx.)
- Mounting Position: Any
- Marking: Device Code, See Page 3
- Lead Free: For RoHS / Lead Free Version, Add "-LF" Suffix to Part Number, See Page 4



PIN 10

DPAK/TO-252						
Dim	Min	Max				
Α	6.05	6.70				
В	5.05	5.55				
С	2.10	2.50				
D	1.05	1.25				
E	5.48	6.20				
G	2.55	3.40				
н	0.55	0.90				
J	0.40	0.60				
к	0.95	1.60				
L	0.45	0.55				
Р	2.30 Typical					
All Dimensions in mm						

#### Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

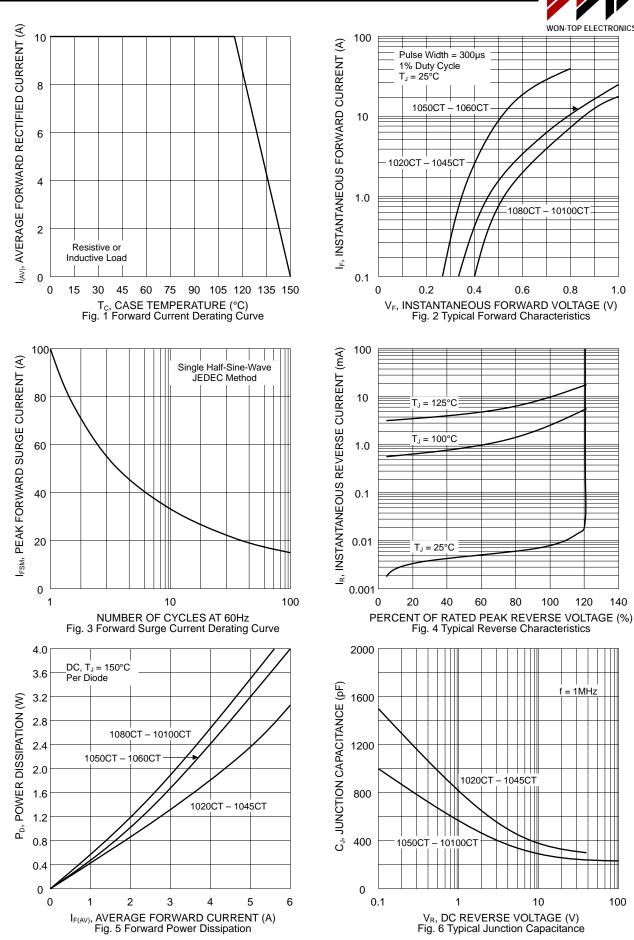
Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	MBRD 1020CT	MBRD 1030CT	MBRD 1040CT	MBRD 1045CT	MBRD 1050CT	MBRD 1060CT	MBRD 1080CT	MBRD 10100CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	20	30	40	45	50	60	80	100	V
RMS Reverse Voltage	VR(RMS)	14	21	28	28	35	42	56	70	V
Average Rectified Output CurrentTotal Device $@T_c = 115^{\circ}C$ Per Diode	lo	10 5.0						А		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	100					A			
Forward Voltage per diode $@I_F = 5.0A$	Vfm	0.55 0.75 0.85			85	V				
Peak Reverse Current $@T_J = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_J = 100^{\circ}C$	Irm	0.2 15					mA			
Typical Junction Capacitance (Note 1)	CJ	500 380				pF				
Thermal Resistance, Junction to Ambient (Note 2) Thermal Resistance, Junction to Case (Note 2)	R JA R JC	70 2.5				°C/W				
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150				°C				

Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

2. Mounted on FR-4 PC board with minimum recommended pad layout per diode.

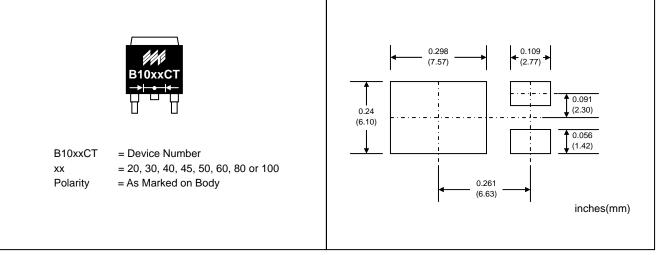
## MBRD1020CT – MBRD10100CT



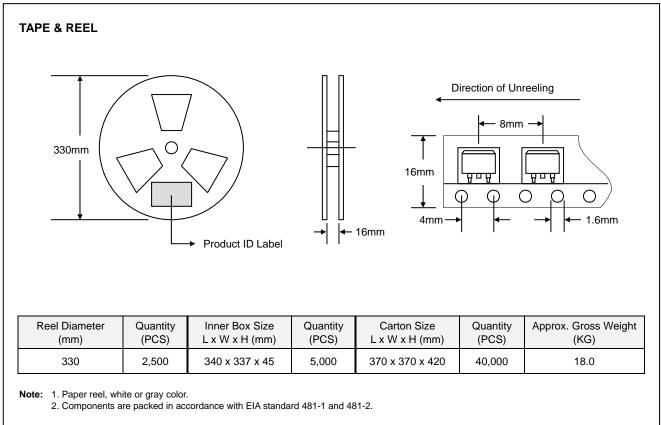
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### MARKING INFORMATION RECOMMENDED FOOTPRINT



#### PACKAGING INFORMATION





Product No.	Package Type	Shipping Quantity				
MBRD1020CT-T3	DPAK	2500/Tape & Reel				
MBRD1030CT-T3	DPAK	2500/Tape & Reel				
MBRD1040CT-T3	DPAK	2500/Tape & Reel				
MBRD1045CT-T3	DPAK	2500/Tape & Reel				
MBRD1050CT-T3	DPAK	2500/Tape & Reel				
MBRD1060CT-T3	DPAK	2500/Tape & Reel				
MBRD1080CT-T3	DPAK	2500/Tape & Reel				
MBRD10100CT-T3	DPAK	2500/Tape & Reel				

#### **ORDERING INFORMATION**

1. Shipping quantity given is for minimum packing quantity only. For minimum order quantity, please consult the Sales Department.

2. To order RoHS / Lead Free version (with Lead Free finish), add "-LF" suffix to part number above. For example, MBRD1020CT-T3-LF.

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WARNING: DO NOT USE IN LIFE SUPPORT EQUIPMENT. WTE power semiconductor products are not authorized for use as critical components in life support devices or systems without the express written approval.

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