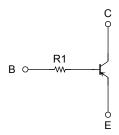
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

# **RN2912FS**, **RN2913FS**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine pitch small mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
  Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.
- Complementary to RN1912FS and RN1913FS

### **Equivalent Circuit and Bias Resistor Values**



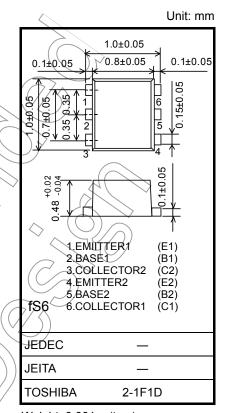
Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

		\ '	\
Characteristics	Symbol	Rating	Unit
Collector-base voltage	Усво	-20	> v
Collector-emitter voltage	V <sub>CEO</sub>	(-20	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	C	-50	mA
Collector power dissipation	P <sub>C</sub> (Note 1)	50	mW
Junction temperature	Tj	150	°C
Storage temperature range	Tstg	<b>−55~150</b>	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

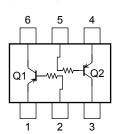
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating



Weight: 0.001 g (typ.)

## Equivalent Circuit (top view)



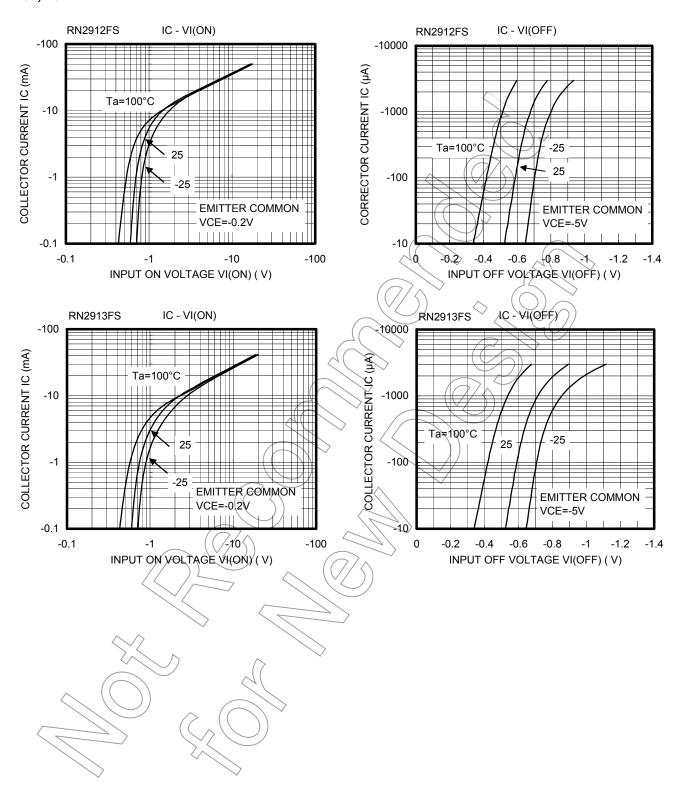


## Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

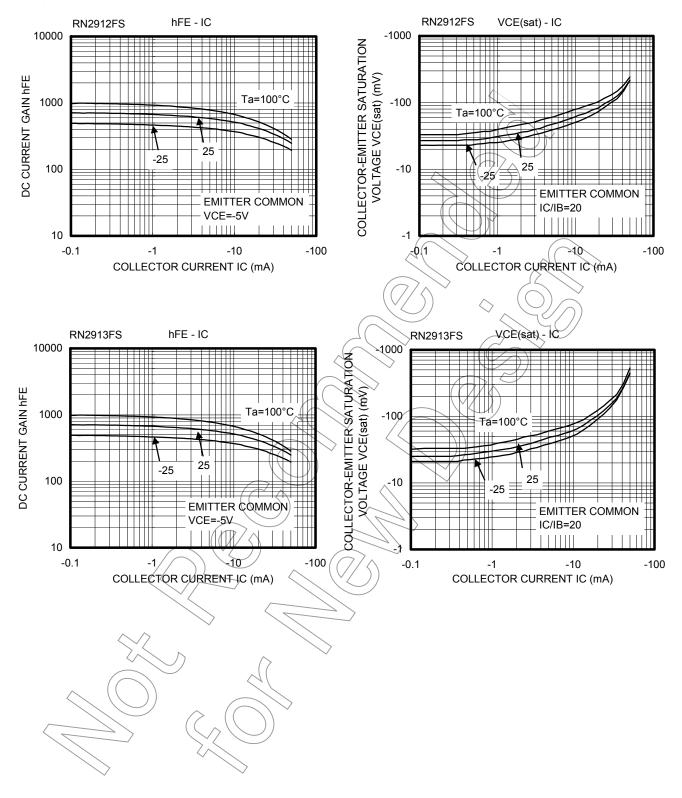
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off curre	ent	I <sub>CBO</sub>	$V_{CB} = -20 \text{ V}, I_{E} = 0$	_	_	-100	nA
Emitter cut-off curren	t	I <sub>EBO</sub>	$V_{EB} = -5 \text{ V}, I_{C} = 0$	_	_	-100	nA
DC current gain		h <sub>FE</sub>	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$	300	_	_	
Collector-emitter satu	ration voltage	V <sub>CE</sub> (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$			-0.15	V
Collector output capa	citance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	(F	) M.2	_	pF
Input resistor	RN2912FS	- R1	- (	17.6	22	26.4	kΩ
	RN2913FS			37,6	47	56.4	

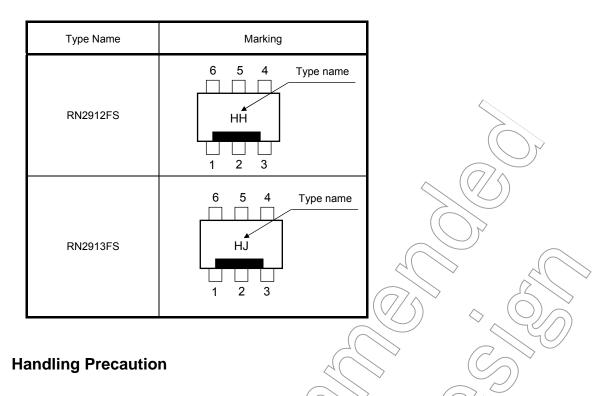


### Q1, Q2 Common



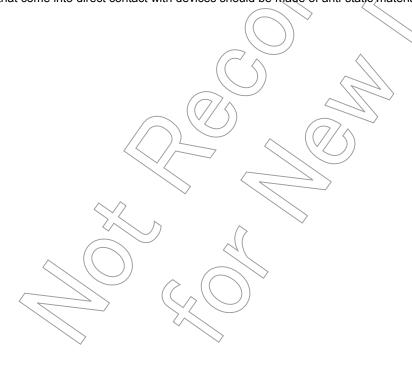
### Q1, Q2 Common





When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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