

**Low Quiescent Current
CMOS Low Dropout Voltage Regulator**
(ADVANCED INFORMATION)

FEATURES

- Extremely Low Quiescent Current3.5 μ A
- Low Dropout Voltage.....30mV @ 1mA Typ
- High Accuracy Output Voltage \pm 5%
- Wide Choice Of V_{OUT} 2.0V, 3.0V, 4.0V, 5.0V
- Offered In TO-92, SOT-89, & SOT-23

APPLICATIONS

- Battery Operating Equipment
- Post-Regulator For Boost Converters In Portable Equipment
- Cellular Phones
- Portable / Palm Top / Notebook Computers
- Portable Instrumentation's

PRODUCT DESCRIPTION

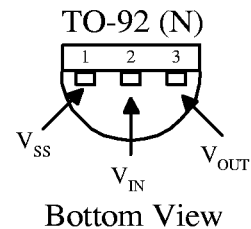
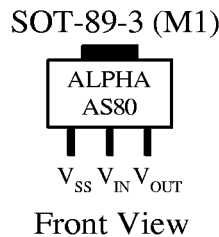
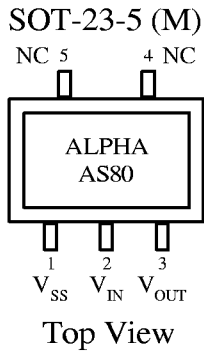
The ALPHA Semiconductor AS80 is a high accuracy 3-terminal CMOS Voltage Regulator. The output currents extend to 80mA, with quiescent currents as low as 1 μ A; the design features very low dropout voltage and fast recovery from turn-on transients, both important features for battery-operated communications equipment. The device is also suitable as a Micropower voltage reference.

Available output voltages extend from 2.0V to 6.0V in 0.1V steps. The AS80 is available in SOT-23, SOT-89, and TO-92 packages.

ORDERING INFORMATION

TO-92 3-PIN	SOT-23 5-PIN	SOT-89 3-PIN	OPER. TEMP RANGE
AS80N	AS80M	AS80M1	IND.

PIN CONFIGURATIONS



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Unit
Input Voltage	V_{IN}	+12	V
Output Current	I_{OUT}	150	mA
Output Voltage	V_{OUT}	$(V_{SS} - 0.3)$ to $(V_{IN} + 0.3)$	V
Power Dissipation TO-92, SOT-89-3, and SOT-23-5	Pd1	300	mW
	Pd2	150	
Operating Temperature Range	T_A	-40 to +85	°C
Storage Temperature Range	T_{STG}	-65 to +150	°C
Soldering Temperature	T_{SOLDER}	260°C, 10 sec	

Input Voltage (V_{IN})..... +12V

Output Current (I_{OUT})..... 150mA

ELECTRICAL CHARACTERISTICS: ($T_A = 25^\circ\text{C}$), unless otherwise specified.

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	V_{OUT}	$10\mu\text{A} \leq I_{OUT} \leq 10\text{mA}$	0.975		1.025	V
Output Current	I_{OUT}	$V_{IN} = V_{OUT} + 2.0\text{V}$				
		$V_{OUT} = 2.0\text{V}$	25	35		mA
		$V_{OUT} = 3.0\text{V}$	35	50		mA
		$V_{OUT} = 4.0\text{V}$	45	65		mA
Load Regulation	V_{OUT}	$V_{IN} = V_{OUT} + 2.0\text{V}$				
		$V_{OUT} = 2.0\text{V}, 1\text{mA} \leq I_{OUT} \leq 35\text{mA}$		30	45	mV
		$V_{OUT} = 3.0\text{V}, 1\text{mA} \leq I_{OUT} \leq 50\text{mA}$		40	60	mV
		$V_{OUT} = 4.0\text{V}, 1\text{mA} \leq I_{OUT} \leq 65\text{mA}$		50	75	mV
Dropout Voltage	V_{DIF}	$I_{OUT} = 1\text{mA}$				
		$V_{OUT} = 2.0\text{V}$		60	90	mV
		$V_{OUT} = 3.0\text{V}$		40	60	mV
		$V_{OUT} = 4.0\text{V}, 5.0\text{V}, 6.0\text{V}$		25	38	mV
Quiescent Current	I_{SS}	$V_{IN} = V_{OUT} + 2.0\text{V}$				
		$V_{OUT} = 2.0\text{V}$				
		$V_{OUT} = 3.0\text{V}$				
		$V_{OUT} = 4.0\text{V}$				
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	$I_{OUT} = 1\text{mA}$				%/V
		$(V_{OUT} + 0.5\text{V}) \leq V_{IN} \leq 10\text{V}$				
Input Voltage	V_{IN}				10	V
Temperature Coefficient	$\Delta V_{OUT}/\Delta T_A$	$I_{OUT} = 10\text{mA}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$		± 100		ppm/°C