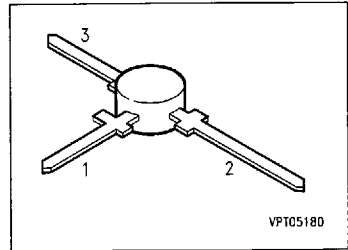


NPN Silicon RF Transistor

BFW 92

- For broadband amplifiers up to 1 GHz at collector currents from 1 mA to 20 mA.



ESD: Electrostatic discharge sensitive device, observe handling precautions!

Type	Marking	Ordering Code	Pin Configuration			Package ¹⁾
			1	2	3	
BFW 92	BFW 92	Q62702-F321	E	C	B	T-plast

Maximum Ratings

Parameter	Symbol	Values	Unit
Collector-emitter voltage	V_{CE0}	15	V
Collector-base voltage	V_{CB0}	25	
Emitter-base voltage	V_{EB0}	2.5	
Collector current	I_C	25	mA
Peak base current, $f \geq 10$ MHz	I_{CM}	50	
Total power dissipation, $T_s \leq 101$ °C ³⁾	P_{tot}	280	mW
Junction temperature	T_j	150	°C
Ambient temperature range	T_A	- 65 ... + 150	
Storage temperature range	T_{stg}	- 65 ... + 150	

Thermal Resistance

Junction - ambient ²⁾	$R_{th JA}$	≤ 255	K/W
Junction - soldering point ³⁾	$R_{th JS}$	≤ 175	

1) For detailed information see chapter Package Outlines.

2) Package mounted on alumina 15 mm × 16.7 mm × 0.7 mm.

3) T_s is measured on the collector lead at the soldering point to the pcb.

Electrical Characteristicsat $T_A = 25\text{ °C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC Characteristics

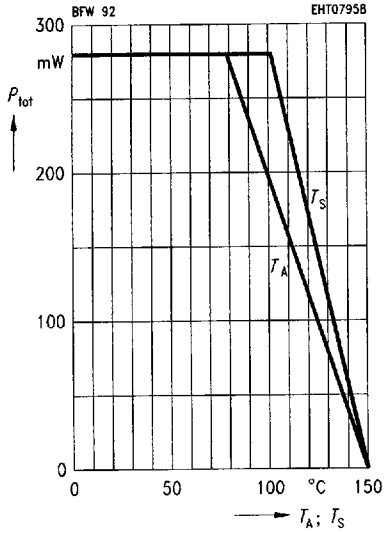
Collector-emitter breakdown voltage $I_C = 1\text{ mA}$, $I_B = 0$	$V_{(BR)CEO}$	15	–	–	V
Collector-base cutoff current $V_{CB} = 10\text{ V}$, $I_E = 0$	I_{CBO}	–	–	50	nA
Emitter-base cutoff current $V_{EB} = 2.5\text{ V}$, $I_C = 0$	I_{EBO}	–	–	100	μA
DC current gain $I_C = 2\text{ mA}$, $V_{CE} = 1\text{ V}$ $I_C = 25\text{ mA}$, $V_{CE} = 1\text{ V}$	h_{FE}	20 20	– –	150 –	–
Collector-emitter saturation voltage $I_C = 20\text{ mA}$, $I_B = 1\text{ mA}$	V_{CEsat}	–	–	0.75	V

AC Characteristics

Transition frequency $I_C = 14\text{ mA}$, $V_{CE} = 5\text{ V}$, $f = 200\text{ MHz}$	f_T	–	2.4	–	GHz
Collector-base capacitance $V_{CB} = 5\text{ V}$, $V_{BE} = v_{be} = 0$, $f = 1\text{ MHz}$	C_{cb}	–	0.48	–	pF
Collector-emitter capacitance $V_{CE} = 5\text{ V}$, $V_{BE} = v_{be} = 0$, $f = 1\text{ MHz}$	C_{ce}	–	0.3	–	
Output capacitance $V_{CE} = 5\text{ V}$, $V_{BE} = v_{be} = 0$, $f = 1\text{ MHz}$	C_{obs}	–	0.8	–	
Noise figure $I_C = 2\text{ mA}$, $V_{CE} = 5\text{ V}$, $f = 800\text{ MHz}$, $Z_S = 60\ \Omega$	F	–	4	–	dB
Power gain $I_C = 14\text{ mA}$, $V_{CE} = 5\text{ V}$, $f = 800\text{ MHz}$, $Z_S = 60\ \Omega$, $Z_L = Z_{Lopt}$	G_{pe}	–	11	–	
Linear output voltage two-tone intermodulation test $I_C = 14\text{ mA}$, $V_{CE} = 5\text{ V}$, $d_{IM} = 60\text{ dB}$, $f_1 = 806\text{ MHz}$, $f_2 = 810\text{ MHz}$, $Z_S = Z_L = 50\ \Omega$	$V_{o1} = V_{o2}$	–	100	–	mV
Third order intercept point $I_C = 14\text{ mA}$, $V_{CE} = 5\text{ V}$, $f = 800\text{ MHz}$	IP_3	–	23	–	dBm

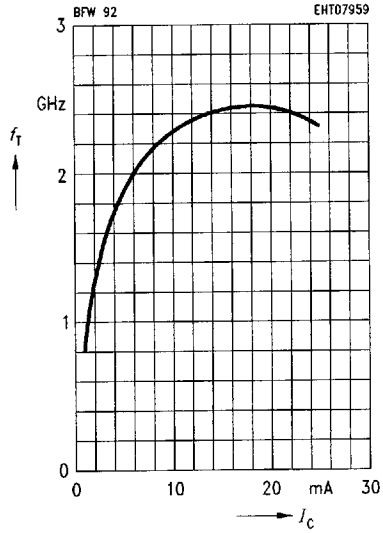
Total power dissipation $P_{tot} = f(T_A^*; T_S)$

*Package mounted on alumina



Transition frequency $f_T = f(I_C)$

$V_{CE} = 5 V, f = 200 MHz$



Collector-base capacitance $C_{cb} = f(V_{cb})$

$V_{BE} = V_{be} = 0, f = 1 MHz$

