

# 6-Ampere N-P-N Darlington Power Transistors

Complementary to the D45D Series

40, 60, and 80 Volts, 30 Watts  
Gain of 2000 at 1 A

**Features:**

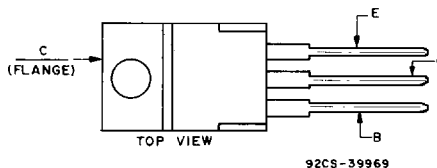
- Operates from IC without predriver

**Applications:**

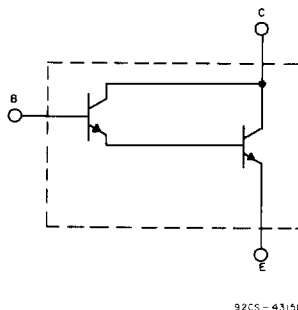
- Solenoid Driver
- Lamp Driver
- Relay Substitute
- Switching Regulator
- Inverter/Converter

The D44D-series n-p-n Darlington power transistors are designed for general purpose switching of multi-ampere loads directly from low-level logic circuitry. The monolithic base-to-emitter resistors have been deleted from the structure to enhance the gain characteristics. These devices feature minimum gains of 2000.

**TERMINAL DESIGNATIONS**



**JEDEC TO-220AB**



Schematic diagram for all types.

**2**  
POWER TRANSISTORS

**MAXIMUM RATINGS (T<sub>A</sub> = 25° C)** (unless otherwise specified)

RATING	SYMBOL	D44D1,2	D44D3,4	D44D5,6	UNITS
Collector-Emitter Voltage	V <sub>CEO</sub>	40	60	80	Volts
Collector-Emitter Voltage	V <sub>CES</sub>	50	70	90	Volts
Emitter Base Voltage	V <sub>EBO</sub>	5	5	5	Volts
Collector Current — Continuous	I <sub>C</sub>	6	6	6	A
Base Current — Continuous	I <sub>B</sub>	0.5	0.5	0.5	A
Total Power Dissipation @ T <sub>A</sub> = 25° C @ T <sub>C</sub> = 25° C	P <sub>D</sub>	2.1 30	2.1 30	2.1 30	Watts
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	-55 to +150	-55 to +150	°C

**THERMAL CHARACTERISTICS**

Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	60	60	60	°C/W
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	4.2	4.2	4.2	°C/W
Maximum Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	T <sub>L</sub>	260	260	260	°C

# D44D Series

ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ ) (unless otherwise specified)

CHARACTERISTIC	SYMBOL	MIN	TYP	MAX	UNIT
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## OFF CHARACTERISTICS<sup>(1)</sup>

Collector-Emitter Breakdown Voltage ( $I_C = 50\text{mA}$ )	D44D1,2 D44D3,4 D44D5,6	$V_{CEO(BR)}$	40 60 80	— — —	— — —	Volts
Collector Cut-off Current ( $V_{CE} = \text{Rated } V_{CES}$ ) ( $V_{CE} = \text{Rated } V_{CES}, V_{BE} = 0.4\text{V}$ )	$T_C = 25^\circ\text{C}$ $T_C = 125^\circ\text{C}$	$I_{CES}$ $I_{CEV}$	— —	— —	10 5	$\mu\text{A}$
Emitter Cutoff Current ( $V_{EB} = 5\text{V}$ )		$I_{EBO}$	—	—	10	$\mu\text{A}$

## SECOND BREAKDOWN

Second Breakdown with Base Forward Biased	FBSOA	SEE FIGURE 5
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## ON CHARACTERISTICS<sup>(1)</sup>

DC Current Gain ( $I_C = 1\text{A}, V_{CE} = 2\text{V}$ )		$h_{FE}$	2,000	5,000	—	—
Collector-Emitter Saturation Voltage ( $I_C = 3\text{A}, I_B = 3\text{mA}$ ) ( $I_C = 5\text{A}, I_B = 5\text{mA}$ )	D44D2,4,6 only	$V_{CE(sat)}$	— —	— —	1.5 1.5	V V
Base-Emitter Saturation Voltage ( $I_C = 5\text{A}, I_B = 5\text{mA}$ )		$V_{BE(sat)}$	—	—	2.5	Volts

## DYNAMIC CHARACTERISTICS

Collector Capacitance ( $V_{CB} = 10\text{V}, f = 1\text{MHz}$ )	$C_{CBO}$	—	—	45	$\text{pF}$
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## SWITCHING CHARACTERISTICS

Resistive Load	$I_C = 3\text{A}, I_{B1} = I_{B2} = 3\text{mA}$ $V_{CC} = 40\text{V}, t_p = 25 \mu\text{sec}$					
Delay Time + Rise Time		$t_d + t_r$	—	0.5	—	$\mu\text{S}$
Storage Time		$t_s$	—	1.2	—	
Fall Time		$t_f$	—	0.8	—	

(1) Pulse Test:  $PW \leq 300\text{ms}$  Duty Cycle  $\leq 2\%$ .

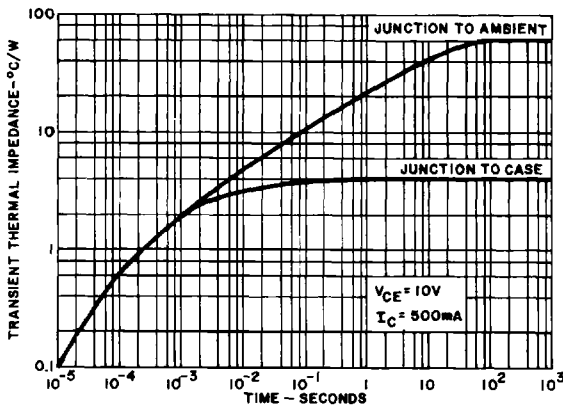


FIG. 1  
MAXIMUM TRANSIENT THERMAL IMPEDANCE

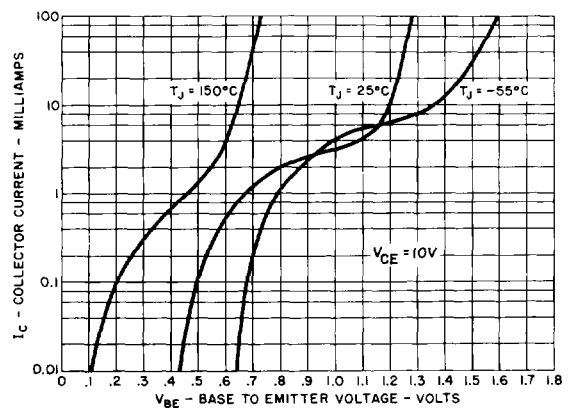


FIG. 2  
TYPICAL TRANSCONDUCTANCE CHARACTERISTICS

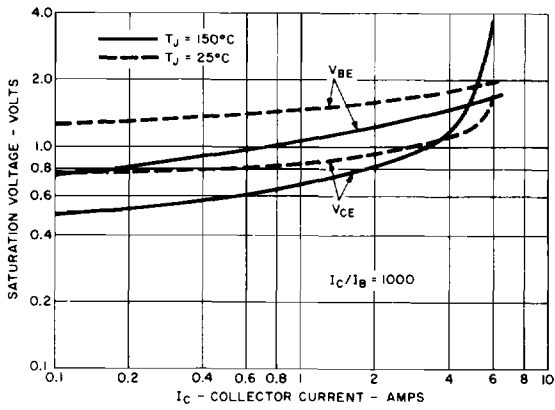


FIG. 3

TYPICAL SATURATION VOLTAGE CHARACTERISTICS

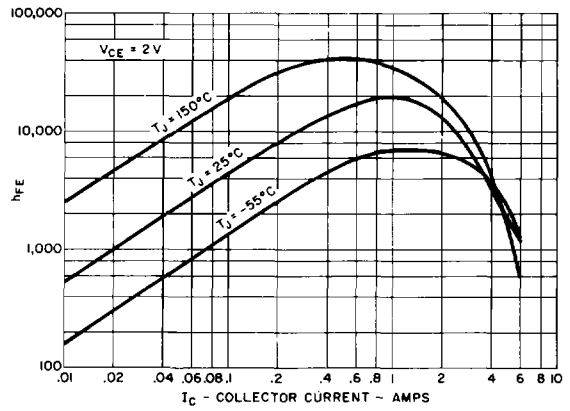


FIG. 4 TYPICAL h<sub>FE</sub> VS. I<sub>C</sub>

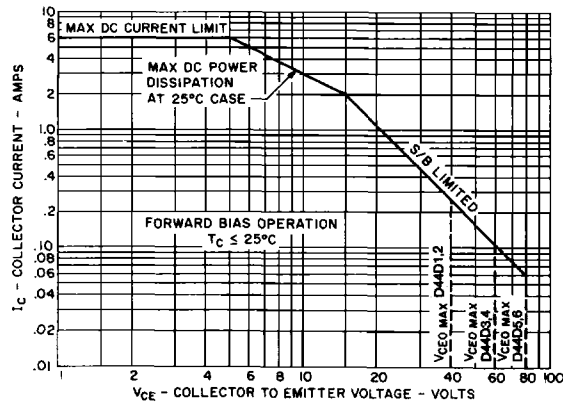


FIG. 5 SAFE REGION OF OPERATION