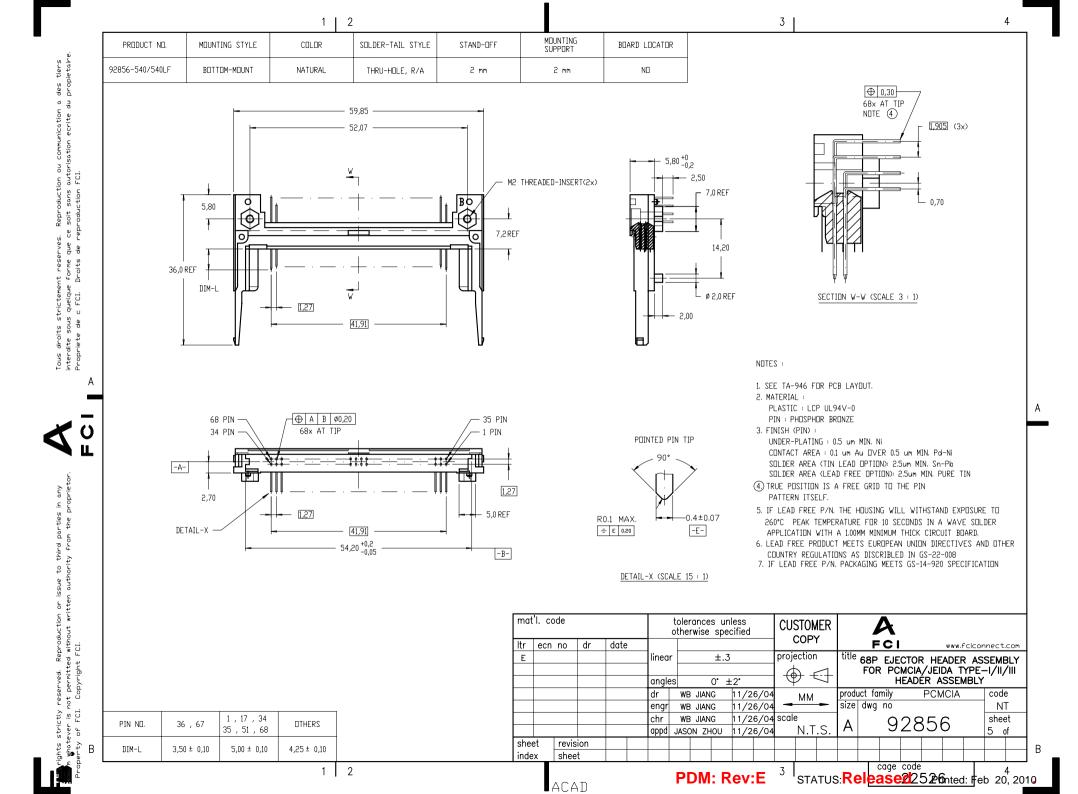
			1 2					3		4
	PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	BOARD LOCATOR	STAND-DFF	MOUNTING SUPPORT	PAGE	APPLICATION DRAWING	
des tiers propietaire.	92856-000/000LF					ND	ND	2		
s tie opieł	92856-040/040LF					2 mm	2 mm	4		
a d a d	92856-010/040LF					4 mm	4 mm	16		
ite d	92856-020/020LF			THRU-HOLE, R/A	ND	5 mm	5 mm	6		
ecr	92856-080/080LF	TOP-MOUNT	BLACK			2 mm	ND	14		
Comm. A tion	92856-090/090LF		BLAUN			5 mm		15		
s. Reproduction ou commu ce soit sans autorisation reproduction FCI.	92856-001/001LF			SMT, STAGGERED (DDUBLE-RDW)				8		
ction is au	92856-002/002LF			SMT, IN-LINE (SINGLE-ROW)	YES	ND	ND	10	REFER TO TA-946	
t san uctic	92856-003/003LF			THRU-HOLE, R/A				12	FOR BOARD LAYOUT	
Rep. prod	92856-500/500LF					ND	ND	3		
e ce	92856-540/540LF			THRU-HOLE, R/A	ND	2 mm	2 mm	5		
s serv ts a dr	92856-510/510LF			INKU-NULL, K/H		4 mm	4 mm	17		
nt reserves. · forme que c Droits de re	92856-520/520LF	BOTTOM-MOUNT	NATURAL			5 mm	5 mm	7		
Tous alroits strictenen interdite sous quelque f Propriete de C FCI. D	92856-501/501LF			SMT, STAGGERED (DOUBLE-ROW)				9		
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	92856-502/502LF			SMT, IN-LINE (SINGLE-ROW)	YES	ND	ND	11		
oits sou	92856-503/503LF			THRU-HOLE, R/A				13		
issue to third parties in any authority from the proprietor.	1 PIN - 35 PIN -	BOTT FIG.A ME	IIII IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	ATOR	BOTTOM-MOUNT INDICATOR			FIG.B SOLD	ER-TAIL STYLE	
Wil rights strictly reserved. Reproduction or li form whatever is not permitted without written Property of FCI. Copyright FCI. B			FIG.D STAND-OFF & MOUNTING SUPP PORT		mat'l. code	date 11/26/04 angl dr engr chr engr chr appd E E 1 2	es 0° ±2° WB JIANG 11 WB JIANG 11 WB JIANG 11 JASON ZHOU 11 E E E E E	ed COSTO COF projectic 26/04 — MN 26/04 scale	PY FCI Dn title 68P EJECTOR H FOR PCMCIA/JI HEADER M product family M size dwg no T.S. A 9285 E E	ASSEMBLY
All rig form - Prope			1 2		ACAD	_	PDM: Re	7	atus: Rel<u>ease</u>2 252	

1 PIN 1					1	2			1	3	4
			PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-DFF	MOUNTING SUPPORT	BOARD LOCATOR		
A A C C C C C C C C C C C C C			92856-000/000LF	TOP-MOUNT	BLACK	THRU-HOLE, R/A	ND	ND	ND		
A	$\frac{1}{2} PIN \underbrace{\left(\frac{1}{68} + \frac{1}{60} + \frac{1}{68} + \frac{1}{60} + \frac{1}{68} + 1$	$\frac{1}{33} \frac{\text{PN}}{\text{PN}} + \frac{1}{68 \times \text{AT TP}} + \frac{1}{100 \times \text{C}^{3}} + \frac{1}{100 \times \text{C}^{3}}$:	3,80 1 36,0 REF		- 52,07				5,80 ⁺⁰ -2,80	
	Itr ecn no dr date E Inear 1 <td>Itr ecn no dr date conterwise specified COSTOMER COPY FC1 www.fciconnect. Itr ecn no dr date intervise specified COSTOMER COPY FC1 www.fciconnect. E Intervise intervise intervise for <</td> <td>5</td> <td>35 PIN -</td> <td>68× AT TIF</td> <td>→ <u> </u></td> <td></td> <td>R [-</td> <td></td> <td> 1. SEE TA-946 FOR PCB LAYOUT. 2. MATERIAL : PLASTIC : LCP UL94V-0 PIN : PHOSPHOR BRONZE 3. FINISH (PIN) : UNDER-PLATING : 0.5 um MIN. NI CONTACT AREA : 0.1 um Au DVER 0.5 um MIN. Pd-NI SOLDER AREA : 2.5um MIN. Sn-Pb (4) TRUE POSITION IS A FREE GRID TO THE PIN PATERN ITSELF. 5. IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLT APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BDARD. 6. LEAD FREE PRDUCT MEETS EUROPEAN UNION DIRECTIVES ANI COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008 7. IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICAT </td> <td>ER) OTH</td>	Itr ecn no dr date conterwise specified COSTOMER COPY FC1 www.fciconnect. Itr ecn no dr date intervise specified COSTOMER COPY FC1 www.fciconnect. E Intervise intervise intervise for <	5	35 PIN -	68× AT TIF	→ <u> </u>		R [-		 1. SEE TA-946 FOR PCB LAYOUT. 2. MATERIAL : PLASTIC : LCP UL94V-0 PIN : PHOSPHOR BRONZE 3. FINISH (PIN) : UNDER-PLATING : 0.5 um MIN. NI CONTACT AREA : 0.1 um Au DVER 0.5 um MIN. Pd-NI SOLDER AREA : 2.5um MIN. Sn-Pb (4) TRUE POSITION IS A FREE GRID TO THE PIN PATERN ITSELF. 5. IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLT APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BDARD. 6. LEAD FREE PRDUCT MEETS EUROPEAN UNION DIRECTIVES ANI COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008 7. IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICAT 	ER) OTH

PRODUCT I	ND. MOUNTI	NG STYLE	COLOR	SOLDER-TAIL STYLE	STAND-DFF	MDUNTING SUPPORT	BOARD	LOCATOR					
92856-500/500L	F BOTTO	IM-MOUNT	NATURAL	THRU-HOLE, R/A	ND	ND	1	ND					
	36,0 REF			- 59,85 - 52,07		M2 THREADED-INS 2REF T	ERT(2x)	5,80 ⁺⁰)	SECTION	N W-W (SCALE 3 : 1	(3x) (0,70)	
			⊕ A B G 68× AT TI 68× AT TI 1.27			1 1.27] - DETAIL-X	₽] R 0.1 MAX. [=] [] 0.20	DINTED PIN TIP	2. M/ P 3. F1 U C 5. F1 P 7. If 6. LE C 7. If	EE TA-946 FOR PC ATERIAL : PLASTIC : LCP UL9 INI : PHOSPHOR BR INISH (PIN) : JNDER-PLATING : 0 CONTACT AREA : 0. SOLDER AREA (ILA SOLDER AREA (LEA RUE POSITION IS & PATTERN ITSELF. F LEAD FREE P/N. 60°C PEAK TEMPEL AD FREE PRODUCT COUNTRY REGULATION	94∨-0 RDNZE	IM MIN. Sn-Pb 5um MIN. PURE TIN E PIN . WITHSTAND EXPO IDNDS IN A WAVE HICK CIRCUIT BOAF UNION DIRECTIVES IN GS-22-008	SURE TO SOLDER RD. S AND DT
						mat'l. code Itr ecn no E	dr date	toleranc otherwise	ces unless se specified ±.3	CUSTOMER COPY projection	FCI		iconnec ASSEN PE-I/I
PIN ND. DIM-L	36 , 67	1 , 17 , 34 35 , 51 , 68	OTHERS			sheet revis		angles dr WB JIA engr WB JIA chr WB JIA appd JASON Z	ANG 11/26/04 ANG 11/26/04	4 MM 4 scale	product family size dwg no	PCMCIA	SLY co sh 3

$\frac{1}{100 \text{ M}^{2}} + \frac{1}{100 \text{ M}^{2}} $	PRODUCT NO.	MOUNTING STYLE	COLOR	2 SOLDER-TAIL STYLE	STAND-DFF	MDUNTING	BOARD LOCATOR	3
						SUPPORT 2 mm		_
$\frac{1}{10000000000000000000000000000000000$	36,	0.REF		· 52,07				68x AT TIP NUTE (4) 10,2 2,50 7,0 REF 14,20 0,70 9 2,0 REF SECTION V-W (SCALE 3 : 1)
mat'l. code tolerances unless otherwise specified CUSTOMER COPY A Itr ecn no dr date angles or ±2* for engr www.fciconnect Itr ecn no dr date date group for engr www.fciconnect Itr ecn no dr www.fciconnect date group for engr for engr www.fciconnect Itr ecn no dr www.fciconnect www.fciconnect www.fciconnect for engr group for engr for engr group for engr www.fciconnect for engr group for engr group for engr for engr group for engr for engr group for engr		35 PIN A- 2,70	68x AT TIP		68 PIN		PDINTED PIN 90° - 90° - - € 020	 1. SEE TA-946 FOR PCB LAYDUT. 2. MATERIAL : PLASTIC : LCP UL94V-0 PIN : PHOSPHOR BRONZE 3. FINISH (PIN) : UNDER-PLATING : 0.5 um MIN. Ni CONTACT AREA : 0.1 um Au DVER 0.5 um MIN. Pd-Ni SOLDER AREA (TIN LEAD DPTION): 2.5um MIN. Sn-Pb SOLDER AREA (LEAD FREE DPTION): 2.5um MIN. Sn-Pb SOLDER AREA (LEAD FREE GRID TO THE PIN PATTERN ITSELF. 0.4±0.07
3 DIM-L 3,50 ± 0,10 5,00 ± 0,10 4,25 ± 0,10 Sheet revision		35,51,68				tr ecn no dr	date otl	therwise specified COSTONNELY COPY ±.3 projection ±.3 projection WB JIANG 11/26/04 WB JIANG 11/26/04 WB JIANG 11/26/04



A The series of	PRODUCT NO.	MOUNTING STYLE	1 2	SOLDER-TAIL STYLE	STAND-DFF	MOUNTING SUPPORT	BOARD LOCATOR	3	4
$\frac{1}{1000} + \frac{1}{1000} + 1$									
$1 \xrightarrow{10}{35 \text{ PM}} \xrightarrow{10}{66 \text{ A H TP}} \xrightarrow{10}{66 \text{ A H A H P}} \xrightarrow{10}{66 A H A H A H A H A H A H A H A H A H A $	-	5,80 01 0				HREADED-INSERT(2x)		68x AT TIP NUTE ④ 0,2 2,80 0,70 0,70 0,70 0,70 0,70 0,70 0,70 SECTION A-A (SCALE 3 : 1)](3x)
Itr ecn no dr date cost offerwise specified COSTOMICINC COPY FC1 www.fciconnect.st Itr ecn no dr date inear ±.3 projection title 68P EJECTOR HEADER ASSEMBLY Itr angles 0* ±2* Itree 68P EJECTOR HEADER ASSEMBLY Inear Itree 68P EJECTOR HEADER ASSEMBLY Itree 68P EJECTOR HEADER ASSEMBLY Inear Inear Itree 68P EJECTOR HEADER ASSEMBLY Inear Itree 68P EJECTOR HEADER ASSEMBLY Itree 68P EJECTOR HEADER ASSEMBLY Inear Inear <td>A</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>NOTES :</td> <td></td>	A							NOTES :	
PIN NU. 36, 67 35, 51, 68 UTHERS appd JASON ZHOU 11/26/04 N.T.S. A 92030 6 0		35 PIN 2,70	68x AT TIP		68 PIN 1.27	R0.1 MAX = E 0.	90° 0.4±0.07 20	 SEE TA-946 FOR PCB LAYDUT. MATERIAL : PLASTIC : LCP UL94V-0 PIN : PHOSPHOR BRONZE FINISH (PIN) : UNDER-PLATING : 0.5 um MIN. Ni CONTACT AREA : 0.1 um Au DVER 0.5 um MIN. Pd-Ni SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Pn-Pb SOLDER AREA(LEAD FREE OPTION) : 2.5um MIN. PURE TIN IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD. LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHE COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008 	ΞR
	L	35 PIN 2,70 ETAIL-X	68x AT TIP		68 PIN 1.27 4,0 REF -B-	R0.1 MAX <u>=</u> E 0. <u>DETAIL</u> - mat'l. code tr ecn no dr	90* 0.4±0.07 -E- 	1. SEE TA-946 FOR PCB LAYDUT. 2. MATERIAL : PLASTIC : LCP UL94V-0 PIN : PHOSPHOR BRDNZE 3. FINISH (PIN) : UNDER-PLATING : 0.5 um MIN. NI CONTACT AREA : 0.1 um Au DVER 0.5 um MIN. Pd-Ni SOLDER AREA (TIN LEAD OPTION) : 2.5um MIN. Sn-Pb SOLDER AREA (TIN LEAD DIPTION) : 2.5um MIN. PURE TIN 4. IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BDARD. 5. LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHE COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008 6. IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION Delerances unless therwise specified CUSTOMER 0° ±2' WB JIANG WB JIANG 11/26/04	² ciconnect. R ASSEME YPE-I/II/ IBLY Code

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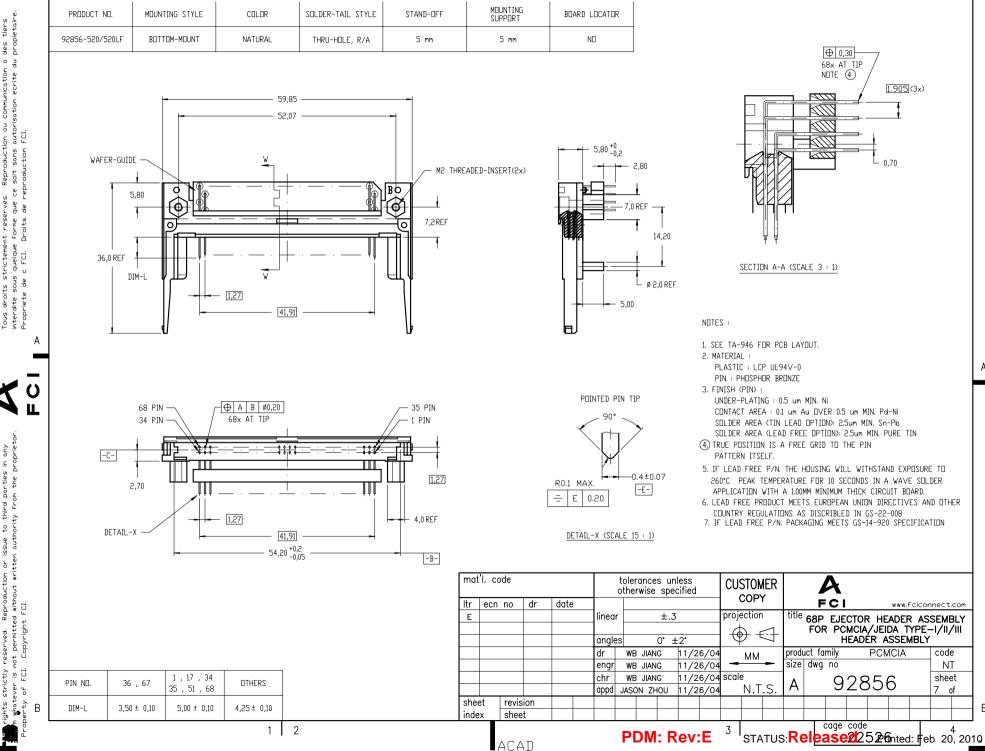
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PRODUCT N	. MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-DFF	MOUNTING SUPPORT	BOARD LOCATOR		
92856-001/00	F TOP-MOUNT	BLACK	SMT, STAGGERED (DOUBLE-ROW)	ND	ND	YES		
merate sous querque norme que ce sons autorisation echrie au p Propriete de c FCI. Droits de reproduction FCI.	-C- 5,80 36,0 REF DIM-L		- 59,85 - 52,07 - 42,545 - 42,545 - 42,545 		M2 THREADED-INSERT(2x		i,80 +0 BOARD LOCATOR SECTION V-V (SCALE 3 + 1)	Ţ 5±0,1 ⊥
	1 PIN 35 PIN -A- 2,70			34 PIN 68 PIN			NDTES : 1. SEE TA-946 FOR PCB LAYDUT. 2. MATERIAL : PLASTIC : LCP UL94V-0 PIN : PHDSPHOR BRONZE 3. FINISH (PIN) : UDDER-PLATING : 0.5 um MIN. NI CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. PC SOLDER AREA (TIM LEAD OPTION): 2.5um MIN. SO SOLDER AREA (LEAD FREE P/N. THE HOUSING WILL WITHSTAN 260*C PEAK TEMPERATURE FOR 40 SECONDS IN A INFRA-RED OR VAPOR PHASE REFLOW OVEN. 5. LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRI COUNTRY REGULATIONS AS DISCRIBLED IN GS-22- 6. IF LEAD FREE P/N. PACKAGING MEETS GS-14-920	N-PW PURE TIN DEXPOSURE TO CONVECTION, ECTIVES AND DTHER -008
v of FCI. Copyright FCI. I uld III	36,67 1,17,3 35,51,6	58 UTHERS		-	mat'l. code	date linear angles dr engr		IDA TYPE-I/II/II ASSEMBLY MCIA code NT
ਊਊ B DIM-L	3,50 ± 0,10 5,00 ± 0,1	l0 4,25 ± 0,10			index sheet			

		1	2				3 4
PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-DFF	MOUNTING SUPPORT	BOARD LOCATOR	
92856-501/501LF	BOTTOM-MOUNT	NATURAL	(DOUBLE-ROW)	ND	ND	YES	
			0,635	4,445REF 4,445REF 7,2REF	ADED-INSERT(2x)	5,80 ⁺⁰ -0,2	D LOCATOR D LOCATOR D LOCATOR NOTES : 1. SEE TA-946 FOR PCB LAYOUT. 2. MATERIAL :
_A-	68 PIN 34 PIN	⊕ A B Ø0,20 68× AT TIP		35 PIN 1 PIN 1.27 			 PLASTIC : LCP UL94V-0 PIN : PHDSPHOR BRONZE FINISH (PIN) : UNDER-PLATING : 0.5 um MIN. Ni CONTACT AREA : 0.1 um Au OVER 0.5 um MIN. Pd-Ni SOLDER AREA (TIN LEAD OPTION): 2.5um MIN. Sn-Pb SOLDER AREA (LEAD FREE OPTION): 2.5um MIN. PURE TIN IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 40 SECONDS IN A CONVECTION, INFRA-RED OR VAPOR PHASE REFLOW OVEN. LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHE COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008 IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION
40 X	, 67 1, 17, 34 35, 51, 68 ± 0,10 5,00 ± 0,10			- - - - - - - - - - - - - - - - - - -	mat'l. code	date Innear angles dr engr	olerances unless therwise specified CUSTOMER COPY FCI www.fciconnect.cc ±.3 projection title 68P EJECTOR HEADER ASSEMBLY 0° ±2° Image: the second

PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-DFF	MOUNTING SUPPORT	BOARD LOCATOR		
92856-002/002LF	TOP-MOUNT	BLACK	SMT IN-LINE (SINGLE-RDW)	ND	ND	YES		
	-C- 5.80 5.80 5.80 5.80 5.80 5.80 5.80 5.80				30(68X) // C 020 - M2 THREADED-INSERT(2 .2REF		5.80-0.2 BOARD LOCATOR	CD-PLANARITY 0 ±0,15 + 4,75 +
	1 PIN 35 PIN -A- 2.70	⊕ A B 68× AT					 IF LEAD FREE P/N. THE HE 260°C PEAK TEMPERATURE INFRA-RED DR VAPDR PHAS LEAD FREE PRODUCT MEETS COUNTRY REGULATIONS AS 	IN. Ni DVER 0.5 um MIN. Pol-Ni PTIDN): 2.5um MIN. Sn-Po JPTIDN): 2.5um MIN. PURE TIN JUSING VILL WITHSTAND EXPOSURE TO FOR 40 SECONDS IN A CONVECTION, SE REFLOW DVEN. SE URDPEAN UNION DIRECTIVES AND OTHER
		BTUEDS			mat'l. code	date linear angles dr engr chr	S 0° ±2° WB JIANG 11/26/04 MM	

			1	2	1				3	4
	PRODUCT NO.	MOUNTING STYLE	COLOR		STAND-DFF	MDUNTING SUPPORT	BOARD LOCATOR	2		
Source	92856-502/502LF	BOTTOM-MOUNT	NATURAL		ND	ND	YES			
 1. SEE TA-946 FOR PCB LAYDUT. 2. NATERIAL: PLASTIC: LCP UL94V-0 PIN: PHOSPHOR BRONZE 3. FINSH FOR PCB LAYDUT. 3. MATERIAL: PLASTIC: LCP UL94V-0 PIN: PHOSPHOR BRONZE 3. FINSH FOR PCB LAYDUT. 3. FINSH FOR PCB LAYDUT. 3. FINSH FOR PCB LAYDUT. 3. FINSH FOR DCB und NUN PARTINE SUDER AREA(LEAD FREE DPTIDN): 25um MIN. Nu CONTACT AREA: 10. Lun AU UVER SU und MIN. PARTINE SUDER AREA(LEAD FREE DPTIDN): 25um MIN. PURE TIN 4. IF LEAD FREE PCN THE HOUSING VILL VITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 405 ECONDS IN A CONVECTION, INFRA-RED IN VAPOR PHASE REFLUE VIEN 5. LEAD FREE PCDUCT MEETS EUROPEAN UNION DIRECTIVES AND DTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008 6. IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION 	. Droits de reproduction	5.80 5.80 36.0REF			Bo	M2 THREADED-INSERT(2 REF	2x)	BOARD LOCATOR		
mat'l. code tolerances unless otherwise specified CUSTOMER COPY A Itr ecn no dr date date itr state for angles 0° ±2° title 68P EJECTOR HEADER ASSEMBLY Itr engr www.fciconnect.c mat'l. code mat'l. code mat'l. code title 68P EJECTOR HEADER ASSEMBLY Itr engr www.fciconnect.c mat'l. code title 68P EJECTOR HEADER ASSEMBLY Itr engr www.fciconnect.c mat'l. code <				TIP		<u>1</u> <u>1</u> .27		2. MATERIAL : PLASTIC : PIN : PHOS 3. FINISH (PIN UNDER-PLA CONTACT A SOLDER AR SOLDER AR 4. IF LEAD FF 260°C PEA INFRA-RED 5. LEAD FREE COUNTRY R	LCP UL94V-0 PHOR BRONZE): TING: 0.5 um MIN RECA: 0.1 um Au D EA(LEAD FREE DP IEE P/N, THE HOU (TEMPERATURE F OR VAPOR PHASE PRODUCT MEETS I EGULATIONS AS DI	I. NI IVER 0.5 um MIN. Pd-Ni TIDN): 2.5um MIN. Sn-Pb TIDN): 2.5um MIN. PURE TIN SING WILL WITHSTAND EXPOSURE TO OR 40 SECONDS IN A CONVECTION, REFLOW OVEN. EURIPEAN UNION DIRECTIVES AND OTHER ISCRIBLED IN GS-22-008
Image: second	ight FCI.					ltr ecn no dr	date linear	therwise specified	COPY projection	FCI www.fciconnect.c
PIN ND. 36, 67 1, 17, 34 35, 51, 68 DTHERS PIN ND. 36, 67 1, 17, 34 Sheet revision Sheet revision	Copyr				-		dr	WB JIANG 11/26/04	ММ	product family PCMCIA code
	PIN ND.	36 , 67 1 , 17 , 34 35 , 51 . 68	DTHERS		-		chr	WB JIANG 11/26/04	scale	A 92856 shee
g t B DIM-L 3,50 ± 0,10 5,00 ± 0,10 4,25 ± 0,10 index sheet	B DIM-L 3					sheet revision index sheet				

92856-003/003LF	TOP-MOUNT								
		BLACK	THRU-HOLE, R/A	ND	ND	YES			
	36,0 REF	<u> </u>	59,85 52,07 		– M2 THREADED-INSER <u>1</u> 7,2REF		5,80 ⁺⁰ - 2,80 - 2	BUARD LOCATOR 7	⊕ 0.30 68x AT TIP NDTE ④ 1.9983
	1 PIN 35 PIN -A- 2,70	⊕ A B 68× AT 68× AT − − − − − − − − − − − − − − −				PDINTE 90 R0.1 MAX. = E 0.20	CUNTACT AREA SOLDER AREA SOLDER AREA SOLDER AREA (4) TRUE POISITION PATTERN ITSEL COVC PEAK TE APPLICATION W 6. LEAD FREE PRC COUNTRY REGUL	UL94V-0 2 BRONZE : 0.5 um MIN. Ni : 0.1 um Au OVER 0.5 um 1 TIN LEAD OPTION» 2.5um M LEAD FREE OPTION» 2.5um IS A FREE GRID TO THE P	MIN. Sn-Pb MIN. PURE TIN IN THSTAND EXPOSURE TI S IN A WAVE SOLDER < CIRCUIT BOARD. ON DIRECTIVES AND D GS-22-008
PIN ND.	36 , 67 <u>1 , 17 , 34</u> 35 , 51 , 68	DTHERS		_	mat'l. code	date linear date chr with angles of the second seco	lerances unless herwise specified ±.3 0° ±2° WB JIANG 11/26/04 WB JIANG 11/26/04 N.T.	FCI title 68P EJECTO FOR PCMC// HEAD product family size dwg no	www.fciconned R HEADER ASSEI A/JEIDA TYPE-I/ ER ASSEMBLY PCMCIA cc 356 sh 12

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PRODUCT NO.	MOUNTING STYLE	COLOR	SOLDER-TAIL STYLE	STAND-DFF	MOUNTING SUPPORT	BOARD LOCATOR		
dite sous quelque forme que ce s riete de c FCI. Droits de repr cerebr		NATURAL 59,85 52,07 V V 1,27 (1,27) (1,91)	THRU-HOLE, R/A	ND M2 THREA	ND DED-INSERT(2x)	YES		-⊕ 0,30 68x AT TIP NDTE ④
oprietor	68 PIN 34 PIN	⊕ A B Ø0,20 68× AT TIP ∴				POINTED PIN TIF	CINITACT AREA : 01 un AU DVER 0.5 um SILDER AREA (TIN LEAD OPTION): 2.5um SILDER AREA (LEAD FREE OPTION): 2.5um SILDER AREA (LEAD FREE OPTION): 2.5um (4) TRUE POSITION IS A FREE GRID TO THE F PATTERN ITSELF. 5. IF LEAD FREE P/N. THE HOUSING WILL W 260°C PEAK TEMPERATURE FOR 10 SECON APPLICATION WITH A 1.00MM MINIMUM THIC 6. LEAD FREE PRODUCT MEETS EUROPEAN UN COUNTRY REGULATIONS AS DISCRIBLED IN 7. IF LEAD FREE P/N. PACKAGING MEETS GS 5 : 1) Olerances unless therwise specified CUSTOMER COPY	MIN. Sn-Pb MIN. PURE TIN PIN ITHSTAND EXPOSURE TO DS IN A WAVE SOLDER K CIRCUIT BOARD. ION DIRECTIVES AND OTHER GS-22-008
FCI. Copyrigh	ر ر م 1 , 17 , 34	TUSSS		-			O* ±2* FOR PCMCI WB JIANG 11/26/04 MM product family WB JIANG 11/26/04 MM size WB JIANG 11/26/04 MM size	A/JEIDA TYPE-I/I/III DER ASSEMBLY PCMCIA code NT
PIN ND. 3	$\begin{array}{c} 6 & , & 67 \\ \hline 35 & , & 51 \\ \hline 50 \pm 0,10 \\ \hline 5,00 \pm 0,1$	UTHERS			sheet revision		JASON ZHOU 11/26/04 N.T.S. A 926	856 sheet 13 of
			2		ndex sheet		PDM: Rev:E ³ STATUS:Release22	le 4 25 20 fonted: Each 20 2

		1		OTAND DEE	MOUNTING			3		
PRODUCT NO. 92856-080/080LF	MOUNTING STYLE	BLACK	SOLDER-TAIL STYLE	STAND-DFF	SUPPORT	BOARD LOCATOR				
36,			- 59,85		: THREADED-INSERT(2x) F		5,80 ⁺⁰ -0,2 	SECT:	(3x) 68x AT TIP NUTE (1) 1,505 (3x) 0,70 10N V-V (SCALE 3 + 1)	
	1 PIN 35 PIN A- 2,70 DETAIL-X	← A B Ø 68× AT TI		34 PIN 68 PIN 1.27 4,0 REF]	PDINTED PI 90° () R0.1 MAX. = E 0.20	2. IN TIP 3. (4) -0.4±0.07 E- 6.	SOLDER AREA (TIN SOLDER AREA (LEA TRUE POSITION IS 4 PATTERN ITSELF. IF LEAD FREE P/N. 260°C PEAK TEMPEI APPLICATION WITH LEAD FREE PRODUC' COUNTRY REGULATIO	94V-0 RONZE	ISURE ⁻ SOLDE RD.
	-		54,20 +0,2 -0,05	- B−		<u>DETAIL-X (SCA</u>				FICATIE
	 -		54,20 ^{+0,2} -0,05	r I	nat'l. code tr ecn no dr E	date linear angles	tolerances unless otherwise specified $\pm .3$ s 0° ±2°	CUSTOMER COPY projection	title 68P EJECTOR HEADER FOR PCMCIA/JEIDA TY HEADER ASSEMI	Elconne ASSE PE-IJ BLY
	5, 67 1, 17, 34 35, 51, 68	DTHERS	54,20 ^+0,2	r I	tr ecn no dr	date linear angles dr engr chr	tolerances unless otherwise specified ±.3	COPY projection 04 MM 04 scale	FCI www.fc	Liconne ASSE PE-D

PRD.			1	2					3	3		4
92856-02		DUNTING STYLE	COLOR BLACK	SOLDER-TAIL STYLE	STAND-DFF	MOUNTING SUPPORT NO	BOARD LOO	ATOR				
	WAFER 7 36,0			59,85 52,07 		– M2 THREADED-IN 7,2REF 	SERT(2x)	5,80 ⁺⁰ 	2,80 L REF	SECTION		.905(3x)
A TO TO	<u>.</u> - <u>-</u> -	1 PIN 35 PIN 2,70						ED PIN TIP	2. MATE PLA PIN 3. FINI: UND CON SOL SOL (4) TRUE PAT 5. IF L	TA-946 FOR PC ERIAL : ISTIC : LCP UL9 PHOSPHOR BR SH (PIN) : IER-PLATING : 0 ITACT AREA : 0.1 DER AREA (LA DER AREA (LA DER AREA (LA E POSITION IS 4 TERN ITSELF. EAD FREE P/N.	94V-0 RONZE	(POSURE TO
	DE	TAIL-X		[41,91] 54,20 ^{+0,2}	[-B- mat'l. code	dr date li	(SCALE 15 : 1) tolerances up otherwise spe near ± ngles 0*	APPL 6. LEAT COUN 7. IF L scified 3 P	LICATION WITH D FREE PRODUCT NTRY REGULATIO	A 1.00MM MINIMUM THICK CIRCUIT J T MEETS EUROPEAN UNION DIRECTI INS AS DISCRIBLED IN GS-22-008 PACKAGING MEETS GS-14-920 SPO FCI www title 68P EJECTOR HEADE FOR PCMCIA/JEIDA HEADER ASSE	IDARD. VES AND DTH CIFICATION .fciconnect. R ASSEME TYPE-I/II/ IMBLY
PIN M	ND. 36,67	, 1 , 17 , 34	DTHERS		-		d e c		11/26/04 11/26/04 11/26/04 11/26/04	-	product family PCMCIA size dwg no A 92856	

					MDUNTING	BOARD LOCATOR	3 4
PRDDUCT ND 92856-010/010	TOP-MOUNT	COLOR BLACK	SOLDER-TAIL STYLE	STAND-OFF	SUPPORT 4 mm	ND	
Propriete de c FCI. Broits de reproduction fCI.	5,80 T				ED-INSERT(2x)	5,80 ⁺⁰ -0,2 	$50 \\ -7,0 \text{ REF} \\ 14,20 \\ - \phi 2,0 \text{ REF} \\ NDTES : $
written authority from the proprietor.	35 PIN 2,70	⊕ A B Ø0,20 68× AT TIP		34 PIN 68 PIN 1.27 - 4,0 REF		POINTED PIN	 1. SEE TA-946 FOR PCB LAYDUT. 2. MATERIAL : PLASTIC : LCP UL94V-0 PIN : PHOSPHOR BRONZE 3. FINISH (PIN) : UNDER-PLATING : 0.5 um MIN. Ni CONTACT AREA : 0.1 um Au DVER 0.5 um MIN. Pd-Ni SOLDER AREA (TIN LEAD OPTION) 2.5um MIN. PNPb SOLDER AREA (LEAD FREE DPTION) 2.5um MIN. PURE TIN 4. TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF. 5. IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMM THICK CIRCUIT BDARD. 6. LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008 7. IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION
Matever is not permitted without writ operity of FCI. Copyright FCI. I-MID - MID - MID	36 , 67 1 , 17 , 3 35 , 51 , 6 3,50 ± 0,10 5,00 ± 0,1	B UTHERS			mat'l. code	date linear angles dr engr	CUSTOMER COPY CUSTOMER COPY FCI www.fciconnect.cr ±.3 projection title 68P EJECTOR HEADER ASSEMBL FOR PCMCIA/JEIDA TYPE-I/II/I HEADER ASSEMBLY WB JIANG 11/26/04 MM product family PCMCIA code size dwg no NT WB JIANG 11/26/04 MM Size dwg no NT WB JIANG 11/26/04 MM Size dwg no NT WB JIANG 11/26/04 N.T.S. A 92856 sheet 16 of

