

## SILICON PLANAR EPITAXIAL TRANSISTORS

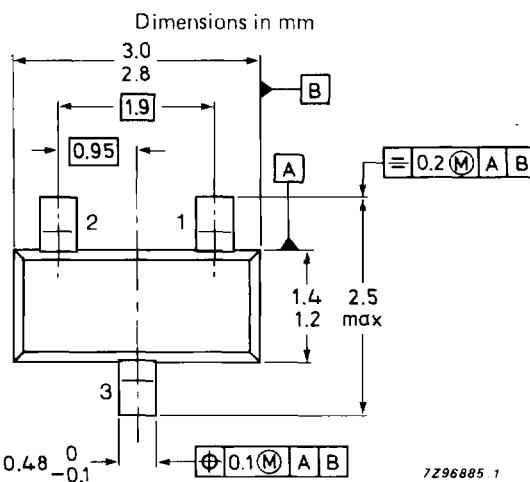
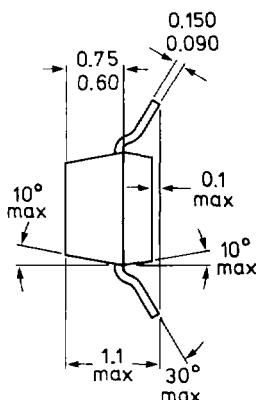
General purpose n-p-n transistors in a plastic SOT-23 package.

### QUICK REFERENCE DATA

		BC846	BC847	BC848	
Collector-emitter voltage ( $V_{BE} = 0$ )	$V_{CES}$	max.	80	50	30 V
Collector-emitter voltage (open base)	$V_{CEO}$	max.	65	45	30 V
Collector current (peak value)	$I_{CM}$	max.	200	200	200 mA
Total power dissipation up to $T_{amb} = 25^\circ\text{C}$	$P_{tot}$	max.	250	250	250 mW
Junction temperature	$T_j$	max.	150	150	150 $^\circ\text{C}$
DC current gain $I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$	$h_{fe}$	$>$ $<$	110 450	110 800	110 800
Transition frequency at $f = 100 \text{ MHz}$ $I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}$	$f_T$	$>$	100	$> 100$	$> 100 \text{ MHz}$
Noise figure at $R_S = 2 \text{ k}\Omega$ $I_C = 200 \mu\text{A}; V_{CE} = 5 \text{ V}$ $f = 1 \text{ kHz}; B = 200 \text{ Hz}$	F	typ.	2	2	2 dB

### MECHANICAL DATA

Fig. 1 SOT-23.



TOP VIEW

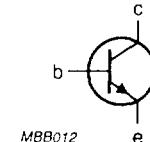
Reverse pinning types are available on request.

### Marking code:

- BC846 = 1Dp
- BC846A = 1Ap
- BC846B = 1Bp
- BC847 = 1Hp
- BC847A = 1Ep
- BC847B = 1Fp
- BC847C = 1Gp
- BC848 = 1Mp
- BC848A = 1Jp
- BC848B = 1Kp
- BC848C = 1Lp

### Pinning:

- 1 = base
- 2 = emitter
- 3 = collector



### RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

		BC846	BC847	BC848	
Collector-base voltage (open emitter)	$V_{CBO}$	max.	80	50	30 V
Collector-emitter voltage ( $V_{BE} = 0$ )	$V_{CES}$	max.	80	50	30 V
Collector-emitter voltage (open base)	$V_{CEO}$	max.	65	45	30 V
Emitter-base voltage (open collector)	$V_{EBO}$	max.	6	6	5 V
Collector current (d.c.)	$I_C$	max.		100	mA
Collector current (peak value)	$I_{CM}$	max.		200	mA
Emitter current (peak value)	$-I_{EM}$	max.		200	mA
Base current (peak value)	$I_{BM}$	max.		200	mA
Total power dissipation*	$P_{tot}$	max.		250	mW
Storage temperature	$T_{stg}$			-65 to + 150	°C
Junction temperature	$T_j$	max.		150	°C

### THERMAL RESISTANCE

From junction to ambient\*  $R_{th\ j-a} = 500 \text{ K/W}$

\* Mounted on an FR4 printed-circuit board 8 mm x 10 mm x 0.7 mm.

**CHARACTERISTICS** $T_j = 25^\circ\text{C}$  unless otherwise specified

Collector cut-off current

 $I_E = 0; V_{CB} = 30 \text{ V}$  $I_E = 0; V_{CB} = 30 \text{ V}; T_j = 150^\circ\text{C}$ 

Base-emitter voltage\*

 $I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$  $|I_{CBO}| < 15 \text{ nA}$  $|I_{CBO}| < 5 \mu\text{A}$  $I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}$  $V_{BE} \text{ typ. } 660 \text{ mV}$  $V_{BE} \text{ 580 to } 700 \text{ mV}$ 

Saturation voltage\*\*

 $I_C = 10 \text{ mA}; I_B = 0,5 \text{ mA}$  $V_{BEsat} \text{ typ. } 770 \text{ mV}$  $V_{CEsat} \text{ typ. } 90 \text{ mV}$  $V_{BEsat} \text{ typ. } 250 \text{ mV}$  $I_C = 100 \text{ mA}; I_B = 5 \text{ mA}$  $V_{BEsat} \text{ typ. } 700 \text{ mV}$  $V_{CEsat} \text{ typ. } 200 \text{ mV}$  $V_{CEsat} \text{ typ. } 600 \text{ mV}$  $V_{BEsat} \text{ typ. } 900 \text{ mV}$ Collector capacitance at  $f = 1 \text{ MHz}$  $I_E = I_e = 0; V_{CB} = 10 \text{ V}$  $C_C \text{ typ. } 2,5 \text{ pF}$ Transition frequency at  $f = 100 \text{ MHz}$  $I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}$  $f_T > 100 \text{ MHz}$ \*  $V_{BE}$  decreases by about 2 mV/K with increasing temperature.\*\*  $V_{BEsat}$  decreases by about 1,7 mV/K with increasing temperature.

#### **Small signal current gain at f = 1 kHz**

$$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$$

h<sub>fe</sub>

110 – 800

**Noise figure at  $R_S = 2 \text{ k}\Omega$**

$$I_C = 200 \mu A; V_{CE} = 5 V;$$

F typ. < 10 dB

### **DC current gain**

$$I_C = 10 \mu\text{A}; V_{CE} = 5 \text{ V}$$

$h_{FF}$  typ.

BC846A	BC846B	
BC847A	BC847B	BC847C
BC848A	BC848B	BC848C
90	150	270
110	200	420
180	290	520
220	450	800

$$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$$

$h_{FE}$  typ.

180      290      520

$$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$$

$h_{FE}$  typ.

180      290      520