# 2SC4208, 2SC4208A

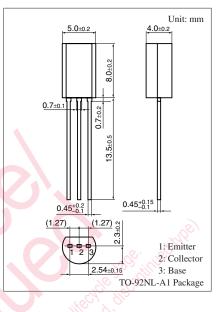
### Silicon NPN epitaxial planar type

For low-frequency output amplification and driver amplification Complementary to 2SA1619 and 2SA1619A

#### Features

- $\bullet$  Low collector-emitter saturation voltage  $V_{CE(sat)}$
- Output of 1 W is obtained with a complementary pair with 2SA1619 and 2SA1619A
- Allowing supply with the radial taping

#### Absolute Maximum Ratings $T_a = 25^{\circ}C$ Symbol Parameter Rating Unit 2SC4208 V<sub>CBO</sub> 30 v Collector-base voltage (Emitter open) 2SC4208A 60 2SC4208 V<sub>CEO</sub> 25 V Collector-emitter voltage (Base open) 50 2SC4208A Emitter-base voltage (Collector open) V<sub>EBO</sub> 7 V Collector current $I_{C}$ 500 mA Peak collector current 1 А I<sub>CP</sub> W Collector power dissipation $P_C$ 1 150 °C Junction temperature Ti -55 to +150 °C Storage temperature T<sub>stg</sub>



#### Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage	2SC4208	V <sub>CBO</sub>	$I_{C} = 10 \ \mu A, I_{E} = 0$	30			V
(Emitter open)	2SC4208A	, , , , , , , , , , , , , , , , , , ,	O'ES Man	60			
Collector-emitter voltage	2SC4208	V <sub>CEO</sub>	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	25			V
(Base open)	2SC4208A	torio.	S. S	50			
Emitter-base voltage (Collector open)		V <sub>EBO</sub>	$I_E = 10 \ \mu A, I_C = 0$	7			V
Forward current transfer ratio *1		h <sub>FE1</sub> *2	$V_{CE} = 10 \text{ V}, I_C = 150 \text{ mA}$	85		340	
		h <sub>FE2</sub>	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 500 \text{ mA}$	40			
Collector-emitter saturation voltage		V <sub>CE(sat)</sub>	$I_{\rm C} = 300 \text{ mA}, I_{\rm B} = 30 \text{ mA}$		0.35	0.60	V
Base-emitter saturation voltage		V <sub>BE(sat)</sub>	$I_{\rm C} = 300 \text{ mA}, I_{\rm B} = 30 \text{ mA}$		1.1	1.5	V
Transition frequency		f <sub>T</sub>	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance		C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		6	15	pF
(Common base, input open circuited)							

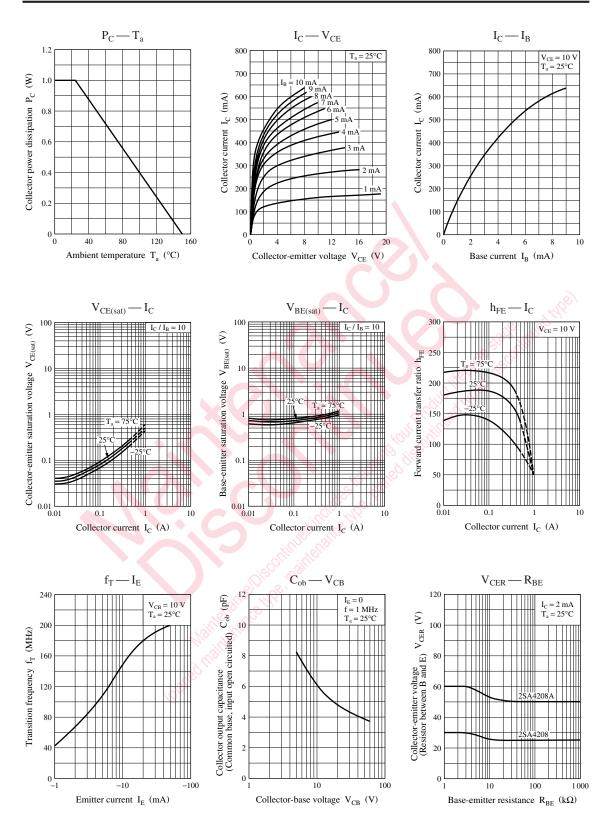
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. \*1: Pulse measurement

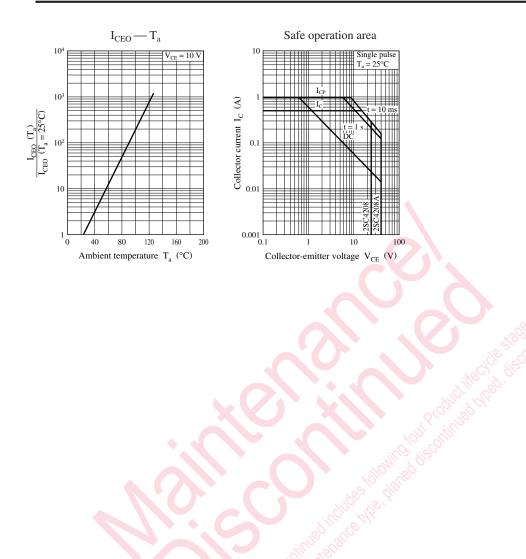
\*2: Rank classification

Rank	Q	R	S	
h <sub>FE1</sub>	85 to 170	120 to 240	170 to 340	

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