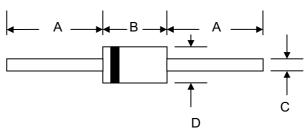
## **SEMICONDUCTOR**

### **5.0A SCHOTTKY BARRIER RECTIFIER**

## Data Sheet 2828, Rev.A

### **Features**

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Green Products in Compliance with the RoHS Directive



### **Mechanical Data**

Case: Molded Plastic

Terminals: Plated Leads Solderable per

MIL-STD-202, Method 208
Polarity: Cathode Band
Weight: 1.2 grams (approx.)

Mounting Position: AnyMarking: Type Number

DO-201AD									
Dim	Min	Max	Min	Max					
Α	25.40	_	1.000	_					
В	8.50	9.50	0.334	0.374					
С	1.20	1.30	0.047	0.051					
D	5.00	5.60	0.197	0.220					
	In mm		In inch						

## Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.

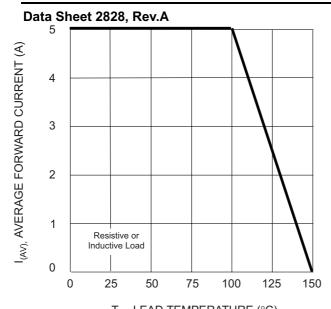
For capacitive load, derate current by 20%.

Characteristic		Symbol	SB520	SB530	SB540	SB550	SB560	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		Vrrm Vrwm Vr	20	30	40	50	60	V
RMS Reverse Voltage	VR(RMS)	14	21	28	35	42	٧	
Average Rectified Output Current (Note 1)	lo	5.0					Α	
Non-Repetitive Peak Forward Surge Current 8 Single half sine-wave superimposed on rated ( (JEDEC Method)	IFSM	150					А	
Forward Voltage (Note 2)	$@I_F = 5.0A$	VFM	0.55		0.63		٧	
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage (Note 2) @T <sub>A</sub> = 100°C		IRM	0.2					mA
, , , , , , , , , , , , , , , , , , ,		_	20					
Typical Junction Capacitance (Note 3)		Cj	550			400		pF
Typical Thermal Resistance Junction to Ambient (Note 1) (Note 4)		$R_{ heta}JA$	25				K/W	
		$R_{\theta}JL$	8					
Operating and Storage Temperature Range	Тj, Тsтg	-65 to +150					°C	

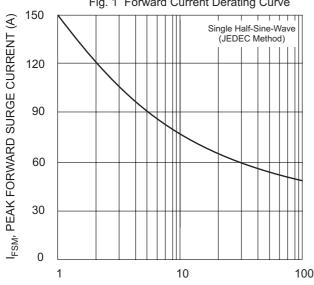
- Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.
  - 2. Short duration test pulse used to minimize self-heating effect.
  - 3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.
  - 4. Thermal resistance junction to lead vertical P.C.B. mounted, 0.375" (9.5mm) lead length.
- 221 West Industry Court ☐ Deer Park, NY 11729-4681 ☐ (631) 586-7600 FAX (631) 242-9798
  - World Wide Web Site http://www.sensitron.com E-Mail Address sales@sensitron.com •

# **SEMICONDUCTOR**

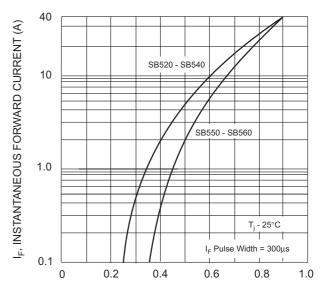
### **5.0A SCHOTTKY BARRIER RECTIFIER**







NUMBER OF CYCLES AT 60Hz Fig. 3 Max Non-Repetitive Peak Fwd Surge Current



V<sub>F</sub>, INSTANTANEOUS FORWARD VOLTAGE (V) Fig. 2 Typical Forward Characteristics

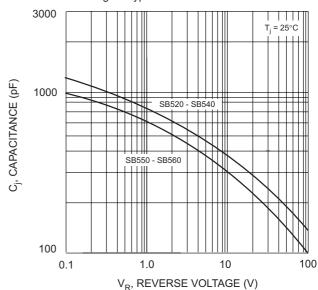
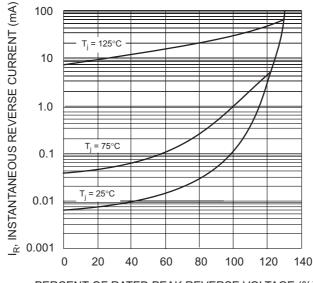


Fig. 4 Typical Junction Capacitance



PERCENT OF RATED PEAK REVERSE VOLTAGE (%) Fig. 5 Typical Reverse Characteristics

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**SENSITRON** 

SB520-SB560

## **SEMICONDUCTOR**

### **5.0A SCHOTTKY BARRIER RECTIFIER**

### Data Sheet 2828, Rev.A

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