

**TYPES SN54ALS160A THRU SN54ALS163A, SN54AS160 THRU SN54AS163
SN74ALS160A THRU SN74ALS163A, SN74AS160 THRU SN74AS163
SYNCHRONOUS 4-BIT DECADE AND BINARY COUNTERS**

D2661, APRIL 1982—REVISED DECEMBER 1983

- Internal Look-Ahead for Fast Counting
- Carry Output for n-Bit Cascading
- Synchronous Counting
- Synchronously Programmable
- Package Options Include Both Plastic and Ceramic Chip Carriers in Addition to Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

These synchronous, presettable counters feature an internal carry look-ahead for application in high-speed counting designs. The 'ALS160A, 'ALS162A, 'AS160, and 'AS162 are decade counters, and the 'ALS161A, 'ALS163A, 'AS161, and 'AS163 are 4-bit binary counters. Synchronous operation is provided by having all flip-flops clocked simultaneously so that the outputs change coincident with each other when so instructed by the count-enable inputs and internal gating. This mode of operation eliminates the output counting spikes that are normally associated with asynchronous (ripple clock) counters. A buffered clock input triggers the four flip-flops on the rising (positive-going) edge of the clock input waveform.

These counters are fully programmable; that is, the outputs may be preset to either level. As presetting is synchronous, setting up a low level at the load input disables the counter and causes the outputs to agree with the setup data after the next clock pulse regardless of the levels of the enable inputs.

The clear function for the 'ALS160A, 'ALS161A, 'AS160, and 'AS161 is asynchronous and a low level at the clear input sets all four of the flip-flop outputs low regardless of the levels of the clock, load, or enable inputs.

The clear function for the 'ALS162A, 'ALS163A, 'AS162, and 'AS163 is synchronous and a low level at the clear input sets all four of the flip-flop outputs low after the next clock pulse, regardless of the levels of the enable inputs. This synchronous clear allows the count length to be modified easily as decoding the maximum count desired can be accomplished with one external NAND gate. The gate output is connected to the clear input to synchronously clear the counter to 0000 (LLLL).

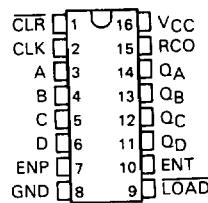
The carry look-ahead circuitry provides for cascading counters for n-bit synchronous applications without additional gating. Instrumental in accomplishing this function are two count-enable inputs and a ripple carry output. Both count-enable inputs (ENP and ENT) must be high to count, and ENT is fed forward to enable the ripple carry output. The ripple carry output (RCO) thus enabled will produce a high-level pulse while the count is maximum (9 or 15 with QA high). This high-level overflow ripple carry pulse can be used to enable successive cascaded stages. Transitions at the ENP or ENT are allowed regardless of the level of the clock input.

These counters feature a fully independent clock circuit. Changes at control inputs (ENP, ENT, or LOAD) that will modify the operating mode have no effect on the contents of the counter until clocking occurs. The function of the counter (whether enabled, disabled, loading, or counting) will be dictated solely by the conditions meeting the stable setup and hold times.

The SN54ALS160A through SN54ALS163A and SN54AS160 through SN54AS163 are characterized for operation over the full military temperature range of -55°C to 125°C . The SN74ALS160A through SN74ALS163A and SN74AS160 through SN74AS163 are characterized for operation from 0°C to 70°C .

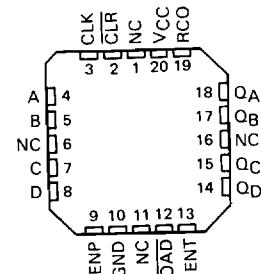
**SN54ALS', SN54AS' . . . J PACKAGE
SN74ALS', SN74AS' . . . N PACKAGE**

(TOP VIEW)



**SN54ALS', SN54AS . . . FH PACKAGE
SN74ALS', SN74AS' . . . FN PACKAGE**

(TOP VIEW)



NC — No internal connection

2

ALS AND AS CIRCUITS

Copyright © 1982 by Texas Instruments Incorporated

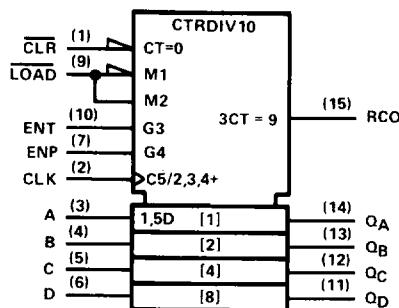
**TEXAS
INSTRUMENTS**

POST OFFICE BOX 225012 • DALLAS, TEXAS 75265

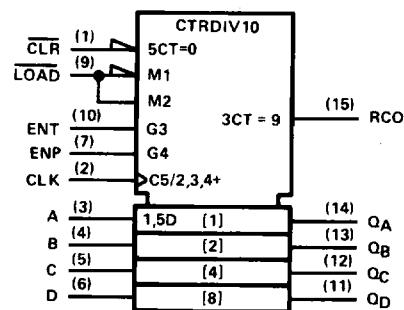
**TYPES SN54ALS160A, SN54ALS162A, SN54AS160, SN54AS162
SN74ALS160A, SN74ALS162A, SN74AS160, SN74AS162
SYNCHRONOUS 4-BIT DECADE COUNTERS**

logic symbols

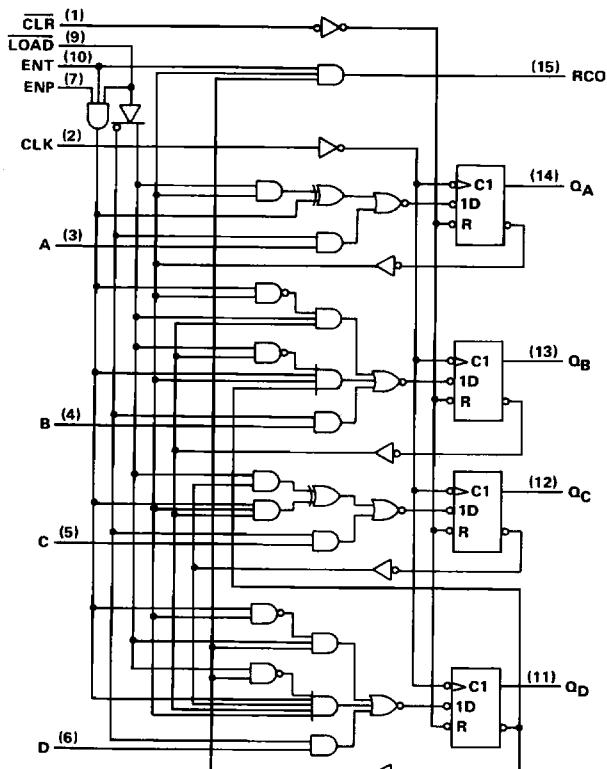
'ALS160A AND 'AS160 DECADE
COUNTERS WITH DIRECT CLEAR



'ALS162A AND 'AS162 DECADE
COUNTERS WITH SYNCHRONOUS CLEAR



'ALS160A and 'AS160 logic diagram (positive logic)



2

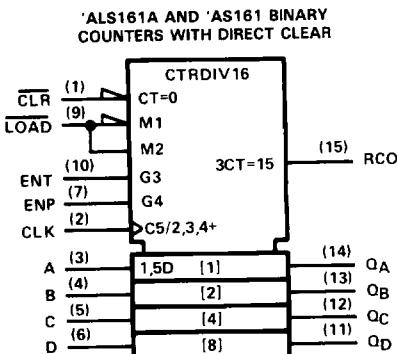
ALS AND AS CIRCUITS

'ALS162A and 'AS162 decade counters are similar; however the clear is synchronous as shown for the 'ALS163A and 'AS163 binary counters.

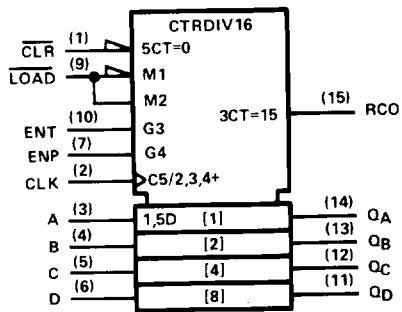
Pin numbers shown are for J and N packages.

**TYPES SN54ALS161A, SN54ALS163A, SN54AS161, SN54AS163
SN74ALS161A, SN74ALS163A, SN74AS161, SN74AS163
SYNCHRONOUS 4-BIT DECADE COUNTERS**

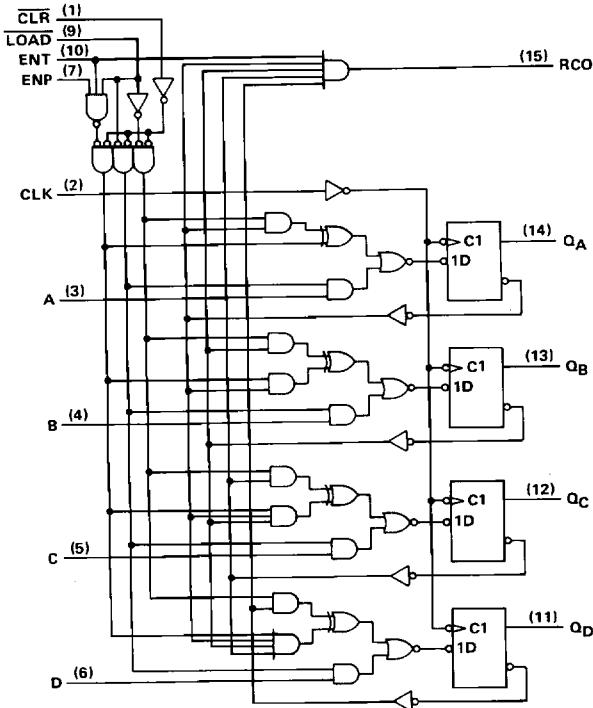
logic symbols



'ALS163A AND 'AS163 BINARY COUNTERS WITH SYNCHRONOUS CLEAR



'ALS163A and 'AS163 logic diagram (positive logic)



2

ALS AND AS CIRCUITS

'ALS161A and 'AS161 synchronous binary counters are similar; however the clear is asynchronous as shown for the 'ALS160A and 'AS160 decade counters.

Pin numbers shown are for J and N packages.

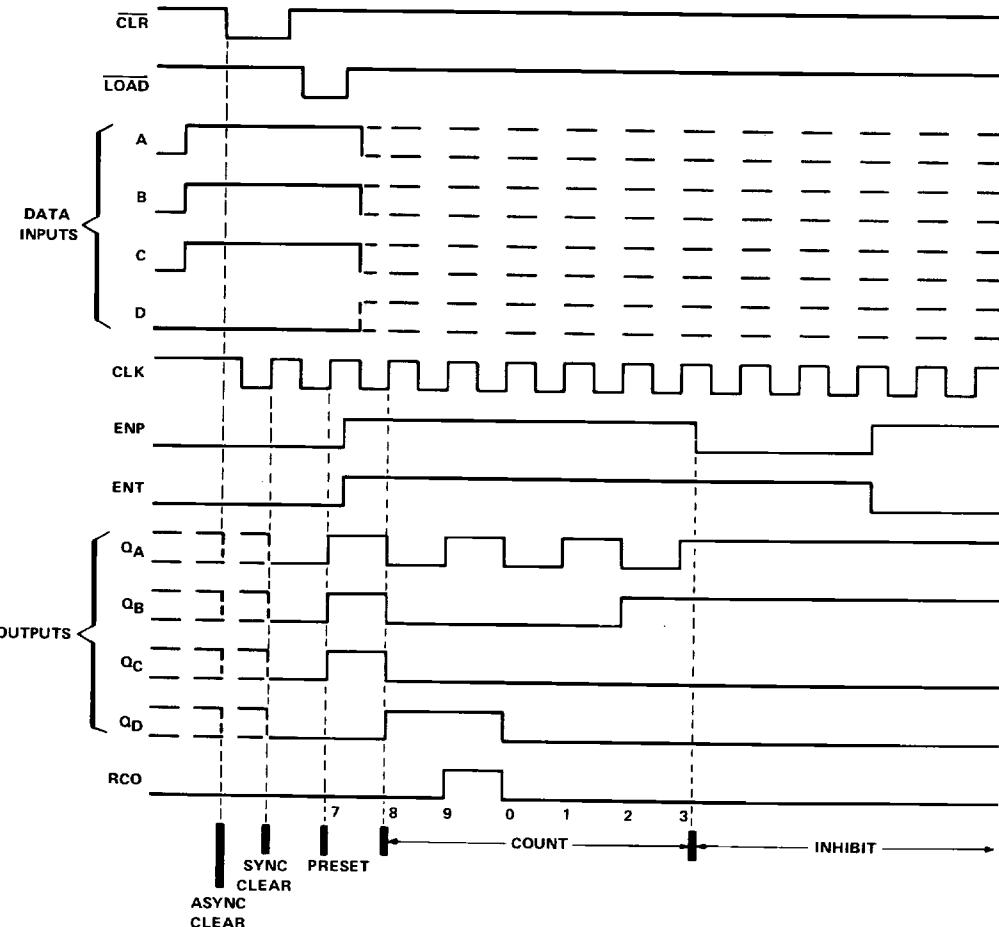
**TYPES SN54ALS160A, SN54ALS162A, SN54AS160, SN54AS162
 SN74ALS160A, SN74ALS162A, SN74AS160, SN74AS162
 SYNCHRONOUS 4-BIT BINARY COUNTERS**

typical clear, preset, count, and inhibit sequences

'ALS160A, 'AS160, 'ALS162A, 'AS162

Illustrated below is the following sequence:

1. Clear outputs to zero ('ALS160A and 'AS160 are asynchronous; 'ALS162A and 'AS162 are synchronous)
2. Preset to BCD seven
3. Count to eight, nine, zero, one, two, and three
4. Inhibit



2

ALS AND AS CIRCUITS

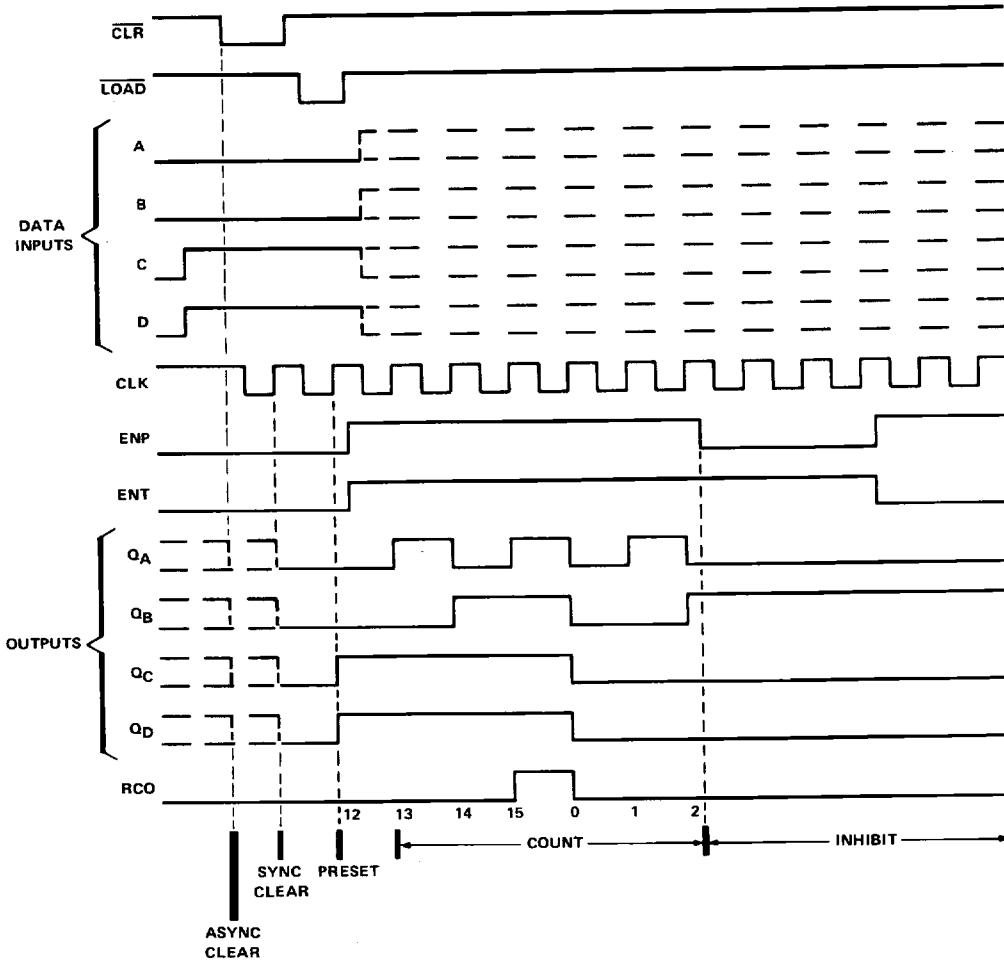
**TYPES SN54ALS161A, SN54ALS163A, SN54AS161, SN54AS163
 SN74ALS161A, SN74ALS163A, SN74AS161, SN74AS163
 SYNCHRONOUS 4-BIT DECADE COUNTERS**

typical clear, preset, count, and inhibit sequences

'ALS161A, 'AS161, 'ALS163A, 'AS163

Illustrated below is the following sequence:

1. Clear outputs to zero ('ALS161A and 'AS161 are asynchronous; 'ALS163A and 'AS163 are synchronous)
2. Preset to binary twelve
3. Count to thirteen, fourteen, fifteen, zero, one, and two
4. Inhibit



2

ALS AND AS CIRCUITS

**TYPES SN54ALS160A THRU SN54ALS163A
SN74ALS160A THRU SN74ALS163A
SYNCHRONOUS 4-BIT DECADE AND BINARY COUNTERS**

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC}	7 V
Input voltage	7 V
Operating free-air temperature range:	
SN54ALS160A thru SN54ALS163A	-55°C to 125°C
SN74ALS160A thru SN74ALS163A	0°C to 70°C
Storage temperature range	-65°C to 150°C

recommended operating conditions

			SN54ALS160A THRU			SN74ALS160A THRU			UNIT			
			SN54ALS163A			SN74ALS163A						
	MIN	NOM	MAX	MIN	NOM	MAX						
V _{CC}	Supply voltage			4.5	5	5.5	4.5	5	5.5	V		
V _{IH}	High-level input voltage			2			2			V		
V _{IL}	Low-level input voltage					0.8			0.8	V		
I _{OH}	High-level output current					-0.4			-0.4	mA		
I _{OL}	Low-level output current					4			8	mA		
f _{clock}	Clock frequency			0	25		0	30		MHz		
t _w	Pulse duration	CLK high or low			20		16.5			ns		
		'ALS160A, 'ALS161A, CLR low			20		15					
t _{su}	Setup time before CLK↑	A, B, C, D			20		15			ns		
		LOAD			20		15					
		ENP, ENT	'ALS160A, 'ALS161A		25		20					
			'ALS162A, 'ALS163A		30		25					
		'ALS160A, 'ALS161A CLR inactive			10		10					
		'ALS162A, 'ALS163A CLR low			20		15					
		'ALS162A, 'ALS163A CLR high (inactive)			10		10					
t _h	Hold time, all synchronous inputs after CLK↑			0			0			ns		
T _A	Operating free-air temperature			-55	125		0	70		°C		

2 ALS AND AS CIRCUITS

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54ALS160A THRU SN54ALS163A			SN74ALS160A THRU SN74ALS163A			UNIT
		MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	
V _{IK}	V _{CC} = 4.5 V, I _I = -18 mA	-	-1.5	-	-	-1.5	-	V
V _{OH}	V _{CC} = 4.5 V to 5.5 V, I _{OH} = -0.4 mA	V _{CC} -2			V _{CC} -2			V
V _{OL}	V _{CC} = 4.5 V, I _{OL} = 4 mA		0.25	0.4		0.25	0.4	V
	V _{CC} = 4.5 V, I _{OL} = 8 mA					0.35	0.5	
I _I	LOAD, CLK or ENT				0.2		0.2	mA
	All other	V _{CC} = 5.5 V, V _I = 7 V			0.1		0.1	
I _{IH}	LOAD, CLK or ENT				40		40	μA
	All other	V _{CC} = 5.5 V, V _I = 2.7 V			20		20	
I _{IL}	V _{CC} = 5.5 V, V _I = 0.4 V			-0.2		-0.2	-0.2	mA
I _O [‡]	RCO			-15	-70	-15	-70	mA
	Q	V _{CC} = 5.5 V, V _O = 2.25 V		-30	-112	-30	-112	
I _{IC}	V _{CC} = 5.5 V			12	21	12	21	mA

[†]All typical values are at $V_{CC} = 5$ V, $T_A = 25^\circ\text{C}$.

[†]The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current. I_Q = 10 mA.

**TYPES SN54ALS160A THRU SN54ALS163A
SN74ALS160A THRU SN74ALS163A
SYNCHRONOUS 4-BIT DECADE AND BINARY COUNTERS**

'ALS160A, 'ALS161A switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R _L = 500 Ω, T _A = MIN to MAX				UNIT	
			SN54ALS160A		SN74ALS160A			
			SN54ALS161A	SN74ALS161A	MIN	MAX		
f _{max}			25		30		MHz	
t _{PLH}	CLK	RCO	8	30	8	26	ns	
t _{PHL}			7	25	7	23		
t _{PLH}	CLK	Any Q	4	18	4	15	ns	
t _{PHL}			6	20	6	17		
t _{PLH}	ENT	RCO	3	16	3	13	ns	
t _{PHL}			3	16	3	13		
t _{PLH}	CLR	Any Q	8	27	8	24	ns	
t _{PHL}	CLR	RCO	11	31	11	28	ns	

'ALS162A, 'ALS163A switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R _L = 500 Ω, T _A = MIN to MAX				UNIT	
			SN54ALS162A		SN74ALS162A			
			SN54ALS163A	SN74ALS163A	MIN	MAX		
f _{max}			25		30		MHz	
t _{PLH}	CLK	RCO	8	30	8	26	ns	
t _{PHL}			7	25	7	23		
t _{PLH}	CLK	Any Q	4	18	4	15	ns	
t _{PHL}			6	20	6	17		
t _{PLH}	ENT	RCO	3	20	3	17	ns	
t _{PHL}			3	16	3	13		

NOTE 1: For load circuit and voltage waveforms, see page 1-12.

2

ALS AND AS CIRCUITS

**TYPES SN54AS160 THRU SN54AS163
SN74AS160 THRU SN74AS163
SYNCHRONOUS 4-BIT DECADE AND BINARY COUNTERS**

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

recommended operating conditions

			SN54AS160 THRU SN54AS163			SN74AS160 THRU SN74AS163			UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC}	Supply voltage		4.5	5	5.5	4.5	5	5.5	V
V _{IH}	High-level input voltage		2			2			V
V _{IL}	Low-level input voltage				0.8			0.8	V
I _{OH}	High-level output current				-2			-2	mA
I _{OL}	Low-level output current				20			20	mA
f _{clock}	Clock frequency								MHz
t _w	Pulse duration	CLK high or low 'AS160, 'AS161 CLR low							ns
t _{su}	Setup time before CLK↑	A, B, C, D							ns
		LOAD							
		ENP, ENT							
		'AS160, 'AS161 CLR inactive							
		'AS162, 'AS163	CLR low						
			CLR high (inactive)						
t _h	Hold time, all synchronous inputs after CLK↑								ns
T _A	Operating free-air temperature			-55	125	0	70	°C	

2 ALS AND AS CIRCUITS

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54AS160 THRU SN54AS163			SN74AS160 THRU SN74AS163			UNIT
		MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	
		-	-	-	-	-	-	
V _{IK}	V _{CC} = 4.5 V, I _I = -18 mA	-	-	-1.2	-	-	1.2	V
V _{OH}	V _{CC} = 4.5 V to 5.5 V, I _{OH} = -2 mA	V _{CC} - 2	-	-	V _{CC} - 2	-	-	V
V _{OL}	V _{CC} = 4.5 V, I _{OL} = 20 mA	-	0.25	0.5	-	0.25	0.5	V
I _I	V _{CC} = 5.5 V, V _I = 7 V	-	-	0.1	-	-	0.1	mA
I _{IH}	V _{CC} = 5.5 V, V _I = 2.7 V	-	-	20	-	-	20	μA
I _{IL}	LOAD, ENT	V _{CC} = 5.5 V, V _I = 0.4 V	-	-	-1	-	-1	mA
	All other	-	-	-	-0.5	-	-0.5	
I _O [‡]	V _{CC} = 5.5 V, V _O = 2.25 V	-30	-	-112	-30	-	-112	mA
I _{CC}	V _{CC} = 5.5 V	-	40	-	40	-	40	mA

[‡]All typical values are at $V_{CC} = 5$ V, $T_A = 25^\circ\text{C}$.

¹The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS} .

Additional information on these products can be obtained from the factory as it becomes available.

PRODUCT PREVIEW

2-144 This page contains information on a product under development. Texas Instruments reserves the right to change or discontinue this product without notice.

**TEXAS
INSTRUMENTS**

POST OFFICE BOX 225012 • DALLAS, TEXAS 75265

**TYPES SN54AS160 THRU SN54AS163
SN74AS160 THRU SN74AS163
SYNCHRONOUS 4-BIT DECADE AND BINARY COUNTERS**

'AS160, 'AS161 switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$, $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$, $T_A = \text{MIN to MAX}$			UNIT		
			SN54AS160		SN74AS160			
			SN54AS161		SN74AS161			
			MIN	TYP [†]	MAX	MIN	TYP [†]	MAX
f_{max}								MHz
t_{PLH}		RCO		7		7		
t_{PHL}	CLK	RCO (with LOAD high)		6		6		ns
t_{PHL}		RCO (with LOAD low)		10		10		
t_{PLH}	CLK	Any Q		5		5		ns
t_{PHL}				6		6		
t_{PLH}	ENT	RCO		3		3		ns
t_{PHL}				4		4		
t_{PHL}	CLR	Any Q		7		7		ns

'AS162, 'AS163 switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$, $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$, $T_A = \text{MIN to MAX}$			UNIT		
			SN54AS162		SN74AS162			
			SN54AS163		SN74AS163			
			MIN	TYP [†]	MAX	MIN	TYP [†]	MAX
f_{max}								MHz
t_{PLH}	CLK	RCO		7		7		ns
t_{PHL}		RCO (with LOAD high)		6		6		
t_{PHL}		RCO (with LOAD low)		10		10		
t_{PLH}	CLK	Any Q		5		5		ns
t_{PHL}				6		6		
t_{PLH}	ENT	RCO		3		3		ns
t_{PHL}				4		4		
t_{PHL}	CLR	Any Q		7		7		

[†]All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^\circ\text{C}$.

NOTE 1: For load circuit and voltage waveforms, see page 1-12.

Additional information on these products can be obtained from the factory as it becomes available.

2

ALS AND AS CIRCUITS

'83

PRODUCT PREVIEW

This page contains information on a product under development. Texas Instruments reserves the right to change or discontinue this product without notice.

**TEXAS
INSTRUMENTS**

POST OFFICE BOX 225012 • DALLAS, TEXAS 75265

2-145

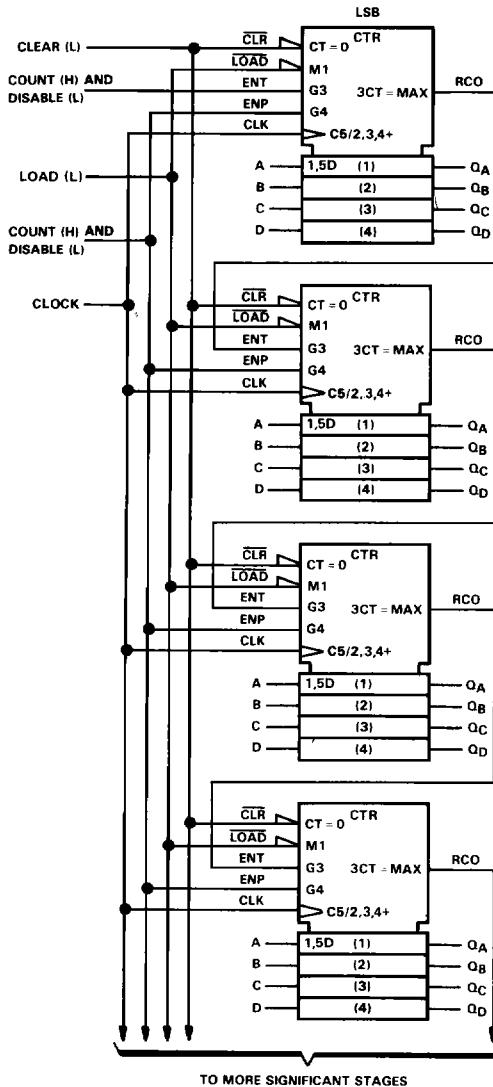
**TYPES SN54ALS160A THRU SN54ALS163A, SN54AS160 THRU SN54AS163
 SN74ALS160A THRU SN74ALS163A, SN74AS160 THRU SN74AS163
 SYNCHRONOUS 4-BIT DECADE AND BINARY COUNTERS**

TYPICAL APPLICATION DATA

N-BIT SYNCHRONOUS COUNTERS

This application demonstrates how the look-ahead carry circuit can be used to implement a high-speed n-bit counter. The 'ALS160A, 'AS160, 'ALS162A, and 'AS162 will count in BCD and the 'ALS161A, 'AS161, 'ALS163A and 'AS163 will count in binary. Virtually any count mode (modulo-N, N₁-to-N₂, N₁-to-maximum) can be used with this fast look-ahead circuit.

2
ALS AND AS CIRCUITS



128