

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHCV07FK

Hex Schmitt Buffer (open drain)

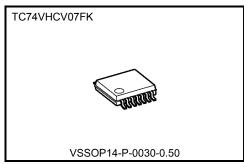
The TC74VHCV07FK is an advanced high speed CMOS BUFFER fabricated with silicon gate CMOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Output have high performance MOS N-channel transistor. (OPEN-DRAIN outputs)

Input pin have hysteresis between the positive-going and negative-going thresholds. Thus the TC74VHCV07FK is capable of squaring up transitions of slowly changing input signals such as line receivers.

Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, etc.



Weight VSSOP14-P-0030-0.50 : 0.02 g (typ)

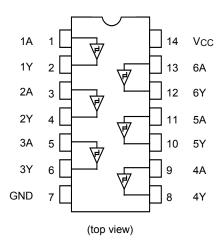
Features

- High speed: tpZ = 3.8 ns (typ.) at Vcc = 5 V
- Low power dissipation: ICC = 2 μA (max) at Ta = 25°C
- Wide operating voltage range: VCC (opr) = 1.8 V to 5.5 V
- Ouput current: IoL = 16 mA (min) (Vcc = 4.5 V)
- Available in VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 07 type

Start of commercial production 2010-01



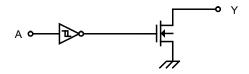
Pin Assignment



Truth Table

Α	Υ
L	L
Н	Z

System Diagram (per gate)





Absolute Maximum Ratings (Note1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	VIN	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to 7.0 (Note 2)	V
Input diode current	lıK	−50	mA
Output diode current	Іок	-50 (Note 3)	mA
DC output current	lout	50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	ICC/IGND	±100	mA
Storage temperature	T _{stg}	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in off-state. IOUT absolute maximum rating must be observed (Output in low state)

Note 3: VouT < GND

Operating Ranges (Note1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	1.8 to 5.5	٧
Input voltage	VIN	0 to 5.5	٧
output voltage	Vout	0 to 5.5	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 20 (V _{CC} = 3.3 ± 0.3 V) 0 to 1 (V _{CC} = 5 ± 0.5 V)	ms/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	1
		_		1.8	_	_	1.65	-	1.65	
				2.3	_	_	1.85	_	1.85	
Positive threshold voltage	V_P			3.0	_	_	2.20	_	2.20	
				4.5	_	_	3.15	_	3.15	
				5.5		_	3.85		3.85	V
				1.8	0.15	_	_	0.15	_	•
		_		2.3	0.45	_	_	0.45	_	
Negative threshold voltage	VN			3.0	0.90	_	_	0.90	_	
				4.5 5.5	1.35	_	_	1.35	_	
					1.65	_	_	1.65	_	
	V _H	_		1.8	0.15	_	1.05	0.15	1.05	V
				2.3	0.20	_	1.10	0.20	1.10	
Hysteresis voltage				3.0	0.30	_	1.20	0.30	1.20	
				4.5	0.40	_	1.40	0.40	1.40	
				5.5	0.50	_	1.60	0.50	1.60	
	Vol	VIN = VIL		1.8	1	0.0	0.1	1	0.1	
			I _{OL} = 50 μA	3.0	_	0.0	0.1	_	0.1	
Low-level output voltage				4.5	_	0.0	0.1	_	0.1	V
			I _{OL} = 8 mA	3.0	_	_	0.36	_	0.44	
			I _{OL} = 16 mA	4.5	_	_	0.44	_	0.55	
3-state output off-state current	loz	V _{IN} = V _{IH} , V _{OUT} = 0 to 5.5 V		1.8 to 5.5	_	_	±0.25	_	±2.5	μА
Power-off leakage current	loff	VIN/VOUT = 5.5 V		0	_	_	0.5	-	5.0	μΑ
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	1	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ



AC Characteristics (input: tr = tf = 3 ns)

Characteristics Symbol		Te	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit
Simulation Single Symbol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max		
		R _L = 1 kΩ	2.5 ± 0.2	15	_	6.7	10.4	1.0	13.0	- ns
				50	_	9.4	15.2	1.0	18.0	
Propagation delay			3.3 ± 0.3	15	_	5.2	7.1	1.0	8.5	
time	t _P ZL			50	_	7.1	10.6	1.0	12.0	
			5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	
				50	-	5.3	7.5	1.0	8.5	
Propagation delay tpLZ		R _L = 1 kΩ	2.5 ± 0.2	50	_	12.2	15.2	1.0	18.0	
	t _{pLZ}		3.3 ± 0.3	50	-	9.5	10.6	1.0	12.0	ns
			5.0 ± 0.5	50	_	7.0	7.5	1.0	8.5	
Input capacitance	C _{IN}	_			_	4	10	_	10	pF
Output capacitance	Cout		_		_	5	_	_	_	pF
Power dissipation capacitance	C _{PD}			(Note)	ı	4	-	ı	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/6 (per gate)

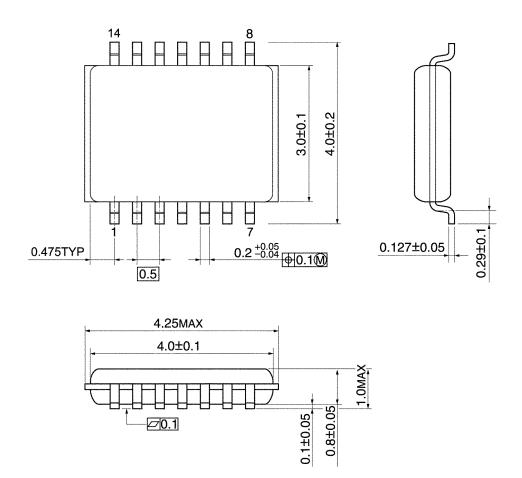
Noise Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Oh ann at a si ati a a	0	Test Condition	Ta =	l lmit		
Characteristics	Symbol		V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	C _L = 50 pF	3.3	0.3	_	V
			5.0	0.6	ı	
Quiet output minimum dynamic V _{OL}	V _{OLV}	C _L = 50 pF	3.3	-0.1	-	V
			5.0	-0.2	-	V
Minimum high level dynamic input voltage	VIHD	C _L = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	VILD	C _L = 50 pF	5.0	_	1.5	V



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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