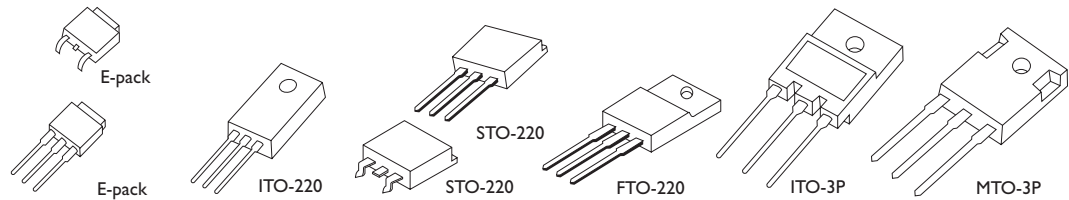


SUPER FAST RECOVERY DIODES

High-frequency rectifying



Twin Diodes

Type No.	Absolute Maximum Ratings						Electrical Characteristics							Remarks	Outline		
	V _{RM}	I _O	Conditions T _c	I _{FSM}	T _{stg}	T _j	V _F (max)	Conditions I _F	I _R (max) V _R =V _{RM}	t _{rr} (max)	θ _J (max)	θ _{JA} (max)	θ _{JC} (max)		Package	Figure	
	[V]	[A]		[A]	[°C]	[°C]	[V]		[A]	[μA]	[ns]	[°C/W]	[°C/W]				[°C/W]
D5LC20U	200	5	132	45	-40~150	150	0.98	2.5	10	35	—	50	3.3	*1	ITO-220	82-1	
D5LC20UR	400		125	50			1.3			50						*2	82-2
40	400		125	50			1.3			50						*1	82-1
D5LD20U	200	5	132	45	-40~150	150	0.98	2.5	10	35	—	50	3.3	*3	ITO-220	82-3	
DE5LC20U	200	5	81	50	-55~150	150	0.98	2.5	10	35	—	55	12	*1	E-pack	*4	
40	400		61				50			1.3							50
SF5LC20U	200	5	137	45	-40~150	150	0.98	2.5	10	35	—	—	2.3	*1	FTO-220	84-1	
30	300		131	60	-55~150		1.3			25			30				2.8
40	400		133	50	-40~150		1.3			10			50				2.3
D8LC20U	200	8	122	60	-40~150	150	0.98	4	10	35	—	50	3.3	*1	ITO-220	82-1	
20UR	400		114				1.3			50			*2			82-2	
40	400		114				1.3			50			*1			82-1	
D8LD20U	200	8	122	60	-40~150	150	0.98	4	10	35	—	50	3.3	*3	ITO-220	82-3	
40	400		114				1.3			50							
D10LC20U	200	10	114	80	-40~150	150	0.98	5	10	35	—	50	3.3	*1	ITO-220	82-1	
20UR	400		100				1.3			50			*2			82-2	
40	400		100				1.3			50			*1			82-1	
DF10LC20U	200	10	127	80	-40~150	150	0.98	5	10	35	—	—	2	*1	STO-220	*5	
SF10LC20U	200	10	124	80	-40~150	150	0.98	5	10	35	—	—	2.3	*1	FTO-220	84-1	
40	400		115				1.3			50							
D15LC20U	200	15	104	110	-40~150	150	0.98	7.5	10	35	—	—	2.8	*1	ITO-220	82-1	
D20LC20U	200	20	112	150	-40~150	150	0.98	10	10	35	—	—	1.7	*1	ITO-3P	88-1	
40	400		102	120			1.3			50							
S20LC20U	200	20	123	140	-40~150	150	0.98	10	10	35	—	—	1.2	*1	MTO-3P	86-1	
40	400		116	120			1.3			50							
DF20LC20U	200	20	114	140	-40~150	150	0.98	10	10	35	—	—	1.6	*1	STO-220	*5	
30	300		105	180	-55~150		1.3			25			30				1
SF20LC30	300	20	112	180	-55~150	150	1.3	10	25	30	—	—	1.5	*1	FTO-220	84-1	

* 1: Common cathode * 2: Common anode * 3: Doubler type

*4: Leaded package - Fig. 78-1; SMD package - Fig. 77-1. *5: Leaded package - Fig. 76-1; SMD package - Fig. 74-1.