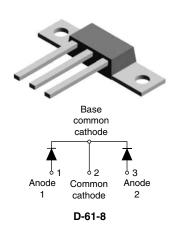
COMPLIANT



Vishay High Power Products

Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 40 A				
V_{R}	30 V			

FEATURES

- 150 °C T_J operation
- · Dual center tap module
- Very low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mould low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	80	A	
V _{RRM}		30	V	
I _{FSM}	t _p = 5 μs sine	5100	A	
V _F	40 Apk, T _J = 125 °C (per leg)	0.37	V	
T _J	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL 82CNQ030APbF		UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V _{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 119 °C, rectangular waveform		80	
Maximum peak one cycle non-repetitive surge current per leg	·	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	5100	Α
See fig. 7		10 ms sine or 6 ms rect. pulse		880	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 8 A, L = 1.12 mH		36	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		8	Α

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward	V (1)	40 A	T _J = 25 °C	0.47	V
		80 A		0.55	
voltage drop per leg See fig. 1	V FM (1)	V _{FM} ⁽¹⁾ 40 A	T _{.1} = 125 °C	0.37	
3		80 A	1J=125 C	0.47	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	- V _R = Rated V _R	5	· mA
See fig. 2	'RM`'	T _J = 125 °C		280	
Maximum junction capacitance per leg	C_{T}	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		3700	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 0		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHAN	THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance,	per leg	Б	DC operation See fig. 4	0.85	
junction to case	per package	- R _{thJC}	DC operation	0.42	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	0/11
Annyayimata wajaht				7.8	g
Approximate weight			0.28	OZ.	
	minimum			40 (35)	kgf · cm
Mounting torque	maximum			58 (50)	(lbf \cdot in)
Marking device			Case style D-61 82CNQ030A		Q030A

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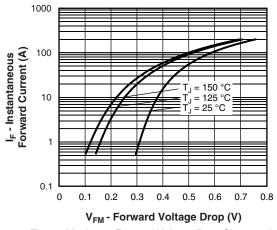


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

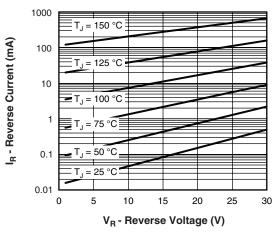


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

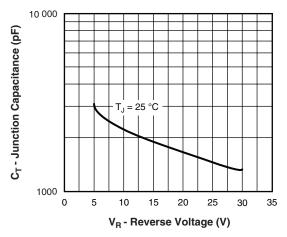


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

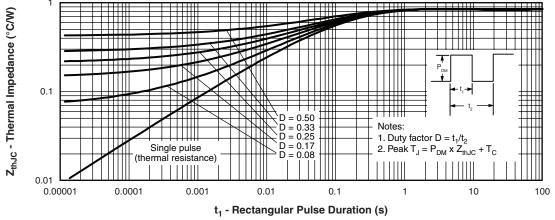


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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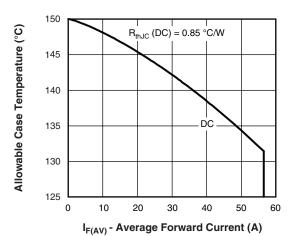


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

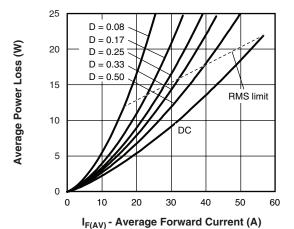


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

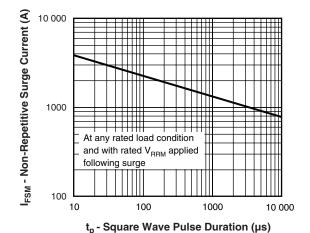


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

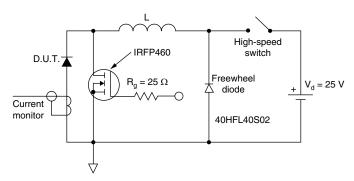


Fig. 8 - Unclamped Inductive Test Circuit



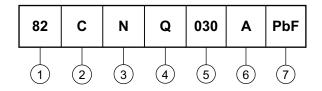


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ORDERING INFORMATION TABLE

Device code



1 - Current rating (80 A)

Circuit configuration:

C = Common cathode

- Package:

N = D-61

- Schottky "Q" series

5 - Voltage rating (030 = 30 V)

6 - A = D-61-8 package style

7 - • None = Standard production

• PbF = Lead (Pb)-free

Standard pack quantity: A = 10 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95019			
Part marking information	http://www.vishay.com/doc?95030		



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