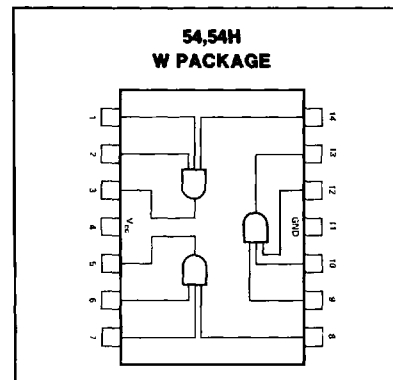
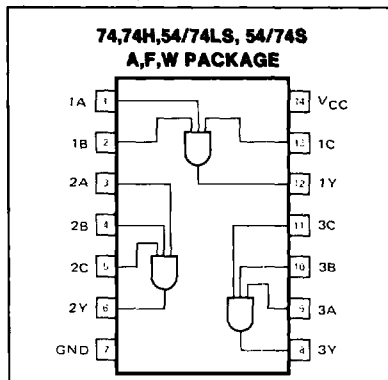


SPEED/PACKAGE AVAILABILITY

54 F,W	74 A,F
54H F,W	74H A,F
54LS F,W	74LS A,F
54S F,W	74S A,F

PIN CONFIGURATION



SWITCHING CHARACTERISTICS $V_{CC} = 5V, T_A = 25^\circ C$

TEST CONDITIONS	54/74			54/74H			54/74LS			54/74S			UNIT
	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
t_{PLH} Low-to-high		17.5	27		7.6	12		9	15	2.5	4.5	7	ns
t_{PHL} High-to-low		12	19		8.8	12		9	20	2.5	5	7.5	ns

Note: For 54/74S, $C_L = 50pF$ and $R_L = 2k\Omega$ are used for the TYP and MAX values.

Load circuit and typical waveforms are shown at the front of section.

10101

SPEED/PACKAGE AVAILABILITY

54LS F,W	74LS A,F
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SWITCHING CHARACTERISTICS $V_{CC} = 5V, T_A = 25^\circ C$

PARAMETER	TEST CONDITIONS	LIMITS			
		MIN	TYP	MAX	UNIT
t_{PLH} Propagation delay time, low-to-high-level output	$C_L = 15pF, R_L = 2k\Omega$		17	32	ns
t_{PHL} Propagation delay time, high-to-low-level output			15	28	ns

Load circuit and typical waveforms are shown at the front of section.

PIN CONFIGURATION

