							F	REVISI	ONS										
LTR					DESC	RIPTIO	N					DA	ATE (YI	R-MO-I	DA)		APPF	ROVED	
A	Char	iges in	accord	ance with I	NOR 5962	2-R025-	93.						93-0)3-11			M. A.	FRYE	
В	Add case outline N. Techical and editorial changes throughout. Redrawn.									95-03-27 M. A. FF			FRYE						
С	Char	anges in accordance with NOR 5962-R132-96.								96-05-24				M. A.	FRYE				
D	Make	e chang	es to c	ase outline	N dimen	sions A	, E, S, a	and S1	. Redra	awn	ro		98-0)4-13		R. MONNIN			
E	Add radiation hardness requirements. Add CAGEs 27851, 21845 and case outline M. Make changes to 1.2, 1.2.2, P _D , theta JC, and theta JA as specifie under paragraph 1.3, table I, figure 1 and figure 2 ro							e ified		00-01-28 R. MONNIN									
F	Add o	case ou	utlines -	4 and 5. M	ake corre	ction to	case c	outline N	N pin de	escripti	ons.		02-0)2-11			R. M	ONNIN	
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PMIC N/A				PREPAR	ED BY H A. KER	BY	2	3	4	DI	EFEN	SE SI		Y CE	NTER		UMB	US	14
STANDARD MICROCIRCUIT DRAWING CHARLES E. BESORE					COLUMBUS, OHIO 43216 http://www.dscc.dla.mil														
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									SHE	ET	1	1	OF	17					

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DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 <u>Scope</u>. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:



1.2.1 <u>RHA designator</u>. RHA marked devices shall meet the MIL-PRF-38535 or MIL-PRF-38535, Appendix A specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-device.

1.2.2 <u>Device type(s)</u>. The device type(s) identify the circuit function as follows:

Device type	<u>Generic number</u>	Circuit function
01	7905A	1.0 A negative regulator, fixed 5 volt

1.2.3 <u>Case outline(s)</u>. The case outline(s) are as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	Terminals	Package style
М	See figure 1	3	Surface mount
Ν	CBCC2-N3	3	Bottom terminal chip carrier
Т	See figure 1	3	TO-257 Single row flange mount, glass sealed
U	See figure 1	3	TO-257 Single row flange mount with isolated tab, glass sealed
Х	See figure 1	3	TO-39 Can
Y	See figure 1	2	TO-3 Flange mount
Z	MBFM4-P2	2	TO-66 Flange mount
2	CQCC1-N20	20	Square leadless chip carrier
4	See figure 1	3	Flange mount, glass sealed with gull wing leads
5	CBCC1-N3	3	Bottom terminal chip carrier

1.2.4 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

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1.3 Absolute maximum ratings.

Input voltage: Operating or input shorted to ground Transient		35 V dc 13 V dc 1/	
Storage temperature range		65°C to +150°C 300°C	
Power dissipation (P_D):			
T _C = +25°C: Cases M, N, T, U, Z, 4, and 5 Cases X and 2	1	5 W W	
$\label{eq:relation} \begin{array}{l} T_A = +25^\circ C: \\ Cases M, T, U, and Z \\ Case N \\ Case N \\ Case X \\ Case Y \\ Case 2 \\ Case 2 \\ Case 4 \\ Case 5 \\ Junction temperature (T_J) \\ Thermal resistance, junction-to-case (\theta_{JC}): \\ Case M \\ Cases N and T \\ Case M \\ Case S. N and T. \\ Case V \\ Case Y \\ Case Y \\ Case Y \\ Case Z. \\ Case A. \\ Case 5 \\ Case A. \\ Case S. \\ Case S.$.0 W 5.0 W .0 W .6 W 040 mW .7 W .3 W 150°C <u>2</u> / .5°C/W .5°C/W .5°C/W .2°C/W °C/W °C/W 5°C/W .6°C/W .6°C/W 2°C/W	
Case Y	3	5°C/W	
Case 4 Case 5	8	0°C/W	
1.4 Recommended operating conditions.			
Input voltage range (V _{IN}) Ambient operating temperature range (T _A) 1.5 Radiation features: 3/		7.5 V dc to -20 V dc 55°C to +125°C	
Maximum total dose available (dose rate - 50 - 300 rads)	Si)/s) 3	00 Krads	
 / The 43-volt input rating refers to the ability of the regulator to Since the regulator's maximum current capability is reduced under nominal loading. // The device is protected by a thermal shutdown circuit which device junction temperature is in excess of +150°C. // These parts may be dose rate sensitive in a space environm Radiation end point limits for the noted parameters are guar method 1019, condition A. 	o withstand high I d, the output may f n is designed to tu nent and may den ranteed only for th	ine or transient conditions v fall out of regulation at high rn off the output transistor v nonstrate enhanced low dos le conditions specified in MI	vithout damage. input voltages vhenever the se rate effects. L-STD-883,
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2. APPLICABLE DOCUMENTS

2.1 <u>Government specification, standards, and handbooks</u>. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883	-	Test Method Standard Microcircuits.
MIL-STD-1835	-	Interface Standard Electronic Component Case Outlines.

HANDBOOKS

DEPARTMENT OF DEFENSE

MIL-HDBK-103 -	List of Standard Microcircuit Drawings.
MIL-HDBK-780 -	Standard Microcircuit Drawings.

(Unless otherwise indicated, copies of the specification, standards, and handbooks are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.

3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 <u>Case outlines</u>. The case outlines shall be in accordance with 1.2.3 herein and on figure 1.

3.2.2 <u>Terminal connections</u>. The terminal connections shall be as specified on figure 2.

3.2.3 Radiation exposure circuit. The radiation exposure circuit shall be as specified on figure 3.

3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

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	Test Symbol		$\begin{array}{l} \mbox{Conditions } \underline{1}/\underline{2}/\underline{3}/\\ -55^{\circ}C \leq T_A \leq +125^{\circ}C\\ \mbox{unless otherwise specified} \end{array}$			Limits		Unit
						Min	Max	
Output voltage	Vout	T _A = +25°C		1	01	-4.95	-5.05	V
		V _{IN} = -7.5 V to -20	V <u>4</u> /	1,2,3		-4.85	-5.15	
		M,D,F	P,L,R,F	1	-	-4.95	-5.05	-
ine regulation <u>5</u> / <u>6</u> /	V _{RLINE}	V _{IN} = -7.5 V to -20	V	1	01		12	mV
		-55°C ≤ TJ ≤ +125°	С	2,3	_		25	-
		M,D,F	P,L,R,F	1	_		12	-
		V _{IN} = -8.0 V to -12	V	1	-		5	-
		-55°C ≤ TJ ≤ +125°	С	2,3	-		12	-
		M,D,F	P,L,R,F	1	_		5	-
_oad regulation <u>5</u> /	Vrload	I _O = 5.0 mA to 1.5	A <u>7</u> /	1	01	01 20		mV
		-55°C ≤ TJ ≤ +125°	С	2,3	-		25	1
		M,D,F	P,L,R,F	1	_		20	-
		I _O = 250 mA to 750) mA <u>7</u> /	1	-		15	-
		-55°C ≤ TJ ≤ +125°	С	2,3	-		30	-
		M,D,F	P,L,R,F	1	-		15	-
		I _O = 5.0 mA to 500	mA <u>8</u> /	1	_		25	-
		-55°C ≤ TJ ≤ +125°	С	2,3	_		50	-
Standby current drain	I _{SCD}			1	01		2.5	mA
				2,3	-		3.0	1
		M,D,F	P,L,R,F	1	_		2.5	-
Standby current drain	ΔI _{SCD}	V _{IN} = -7.0 V to -20	V	1,2,3	01		0.4	mA
	(line)	MDE		1	-		0.4	_
		11,2,1	, _, , , , ,	•			0.1	

Symbol	Conditions $1/2/3/$ -55°C \leq T _A \leq +125°C unless otherwise specified		Group A subgroups	Device type	Limits		Unit
					Min	Max	
∆I _{SCD} (load)	I _O = 5.0 mA	I _O = 5.0 mA to 1000 mA		01		0.4	mA
		M,D,P,L,R,F	1			0.4	
V _{DO}	ΔV _{OUT} = 10 I _O = 1.0 A	0 mV <u>7</u> /	1,2,3	01		2.5	V
		M,D,P,L,R,F	1	-		2.5	-
	$\Delta V_{OUT} = 100$	0 mV <u>8</u> /	1,2,3			2.5	
I _{O(PK)}	$T_A = +25^{\circ}C$	<u>7</u> /	1	01	1.5	3.3	Α
		M,D,P,L,R,F	1	-	1.5	3.3	
	T _A = +25°C	<u>8</u> /	1	-	0.5	1.7	
NO	f = 10 Hz to 100 kHz, $T_{\Delta} = +25^{\circ}\text{C}$		7	01		40	μV/V rms
ΔV _{OUT} / Δt	t = 1000 hou T _A = +25°C	t = 1000 hours, $T_A = +25^{\circ}C$		01		75	mV
los	V _{IN} = -35 V	<u>7</u> /	1	01		1.2	А
			2,3	-		2.8	
		M,D,P,L,R,F	1	-		1.2	_
	V _{IN} = -35 V	<u>8</u> /	1			0.7	_
			2,3	-		2.0	-
le.	1		1	<u> </u>			1
	Symbol ΔI_{SCD} (Ioad) V_{DO} $I_{O(PK)}$ $\Delta V_{OUT} / \Delta t$ I_{OS} e.	e. Condition Symbol Symbol Symbol Condition -55°C \leq T unless other 10 = 5.0 mA 10 = 5.0 mA $\Delta V_{OUT} = 100$ $I_0 = 1.0 A$ $\Delta V_{OUT} = 100$ $I_0 = 500 mA$ $\Delta V_{OUT} = 100$ $I_0 = 500 mA$ $T_A = +25°C$ $\Delta V_{OUT} / t = 1000 houtous$ $\Delta t T_A = +25°C$ $\Delta V_{OUT} / t = 1000 houtous$ $\Delta t T_A = +25°C$ $\Delta V_{OUT} / t = 1000 houtous$ $\Delta t T_A = +25°C$ $\Delta V_{IN} = -35 V$ VIN = -35 V e.	e. Conditions $1/2/3/$ Symbol Symbol Conditions $1/2/3/$ $-55^{\circ}C \leq T_A \leq +125^{\circ}C$ unless otherwise specified M,D,P,L,R,F M,D,P,L,R,F $AV_{OUT} = 100 \text{ mV } \frac{7}{10}$ $I_0 = 1.0 \text{ A}$ M,D,P,L,R,F $AV_{OUT} = 100 \text{ mV } \frac{8}{10}$ $I_0 = 500 \text{ mA}$ $I_0(PK)$ $T_A = +25^{\circ}C \frac{7}{7}$ M,D,P,L,R,F $T_A = +25^{\circ}C$ $AV_{OUT} / t = 1000 \text{ hours}, t = 1000 \text{ hours}, t = 1000 \text{ hours}, t = +25^{\circ}C$ $AV_{OUT} / t = 1000 \text{ hours}, t = +25^{\circ}C$ I_{OS} $V_{IN} = -35 \text{ V } \frac{7}{7}$ e.	$ \begin{array}{ c c c c c c c } \hline Conditions 1/2/3/ \\ \hline Symbol & -55^{\circ}C \leq T_A \leq +125^{\circ}C \\ unless otherwise specified & Subgroups \\ \hline \\ \hline \\ AlSCD \\ (load) & l_O = 5.0 \text{ mA to 1000 mA} & 1.2.3 \\ \hline \\ \hline \\ (load) & M,D,P,L,R,F & 1 \\ \hline \\ \hline \\ VDO & & & & & & & \\ \hline \\ VDO & & & & & & & & \\ \hline \\ VDO & & & & & & & & \\ \hline \\ VDO & & & & & & & & & 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\leq +125^{\circ}C \\ unless otherwise specified & Subgroups & Device \\ \hline Subgroups & 1.2.3 & 01 \\ \hline & M,D,P,L,R,F & 1 & 01 \\ \hline & M,D,P,L,R,F & 1 & 01 \\ \hline & & M,D,P,L,R,F & 1 & 01 \\ \hline & & M,D,P,L,R,F & 1 & 01 \\ \hline & & & M,D,P,L,R,F & 1 & 01 \\ \hline & & & & & & & & & & & & \\ \hline & & & &$	$ \begin{array}{ c c c c c c c } \hline Symbol & \hline Conditions 1/2/3' \\ Symbol & -55^\circ C \leq T_A \leq +125^\circ C \\ unless otherwise specified & Subgroups & Device type & \hline Min \\ \hline \hline Min \\ \hline Min \\ \hline Min \\ \hline Min \\ \hline \hline \hline Min \\ \hline \hline Min \\ \hline \hline Min \\ \hline \hline \hline Min \\ \hline \hline Min \\ \hline \hline Min \\ \hline \hline \hline Min \\ \hline \hline Min \\ \hline \hline \hline \hline Min \\ \hline \hline \hline \hline \hline Min \\ \hline \hline \hline \hline \hline \hline Min \\ \hline \hline \hline \hline Min \\ \hline \hline \hline \hline \hline \hline Min \\ \hline \hline \hline \hline \hline \hline \hline \hline Min \\ \hline $	$ \begin{array}{ c c c c c } \hline Symbol & \hline Conditions 1/2/3/ \\ Symbol & \hline S5^{\circ}C \leq T_A \leq +125^{\circ}C \\ unless otherwise specified \\ (load) & \hline U_0 = 5.0 \text{ mA to } 1000 \text{ mA} & 1.2.3 \\ \hline M_{D,P,L,R,F} & 1 & \hline U_0 = 5.0 \text{ mA to } 1000 \text{ mA} & 1.2.3 \\ \hline M_{D,P,L,R,F} & 1 & \hline U_0 = 1.0 \text{ mV } 1/2/1 & 1.2.3 & 01 & \hline U_0 = 1.0 \text{ A} & \hline U_0 = 1.0 \text{ A} & \hline U_0 = 1.0 \text{ M} & 1.2.3 & 01 & \hline U_0 = 500 \text{ mA} & 1.2.3 & 01 & \hline U_0 = 500 \text{ mA} & 1.2.3 & 01 & \hline U_0 = 500 \text{ mA} & 1.2.3 & \hline U_0 = 500 \text{ mA} & 1.2.3 & \hline U_0 = 500 \text{ mA} & 1.2.3 & \hline U_0 = 1.0 \text{ mV } 8/1.2.3 & \hline U_0 = 500 \text{ mA} & 1.2.3 & \hline U_0 = 1.0 \text{ mV } 8/1.2.3 & \hline U_0 = 500 \text{ mA} & 1.2.3 & \hline U_0 = 500 \text{ mA} & 1.2.3 & \hline U_0 = 500 \text{ mA} & \hline U_0 = 1.5 & 3.3 & \hline T_A = +25^{\circ}C & 1 & \hline T_A = +25^{\circ}C & 8/ & 1 & \hline T_A = +25^{\circ}C & \hline U_0 = 1.0 \text{ Rb} \text{ mD, P, L, R, F} & 1 & \hline U_A & 1.5 & 3.3 & \hline T_A = +25^{\circ}C & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline U_A & 1.5 & 3.3 & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline U_A & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline U_A & \hline U_A & 1.2 & \hline U_A & \hline $

REVISION LEVEL

F

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol Conc -55°C unless ot		Conditions $1/2/3/$ -55°C \leq T _A \leq +125°C unless otherwise specified		$\begin{array}{c} \mbox{Conditions} \ \underline{1}/\ \underline{2}/\ \underline{3}/\\ -55^\circ C \leq T_A \leq +125^\circ C\\ \mbox{unless otherwise specified} \end{array} \ ,$		Conditions $1/2/3/$ -55°C $\leq T_A \leq +125$ °C unless otherwise specified		Conditions $1/2/3/$ -55°C $\leq T_A \leq +125$ °C unless otherwise specified		bol $\begin{array}{c c} Conditions & \underline{1}/\underline{2}/\underline{3}/\\ -55^{\circ}C \leq T_A \leq +125^{\circ}C\\ unless otherwise specified \end{array}$		$\begin{tabular}{ c c c c } \hline Conditions & \underline{1/2/3/} \\ -55^\circ C \leq T_A \leq +125^\circ C \\ \mbox{unless otherwise specified} & s \end{tabular}$		Device type	Lir	mits	Unit
						Min	Max											
Ripple rejection	ΔV _{IN} /	f = 120 Hz, ΔV_{IN} = 10 V,		4	01	63		dB										
	ΔV _{OUT}	V _{IN} = -8 V to -18 V																
				5,6 <u>9</u> /		60												
			M,D,P,L,R,F	4		63												

<u>1</u>/ Devices supplied to this drawing have been characterized through all levels M, D, P, L, R, F of irradiation. However, this device is only tested at the "F" level. Pre and Post irradiation values are identical unless otherwise specified in table I. When performing post irradiation electrical measurements for any RHA level, T_A = +25°C.

- 2/ These parts may be dose rate sensitive in a space environment and may demonstrate enhanced low dose rate effects. Radiation end point limits for the noted parameters are guaranteed only for the conditions specified in MIL-STD-883, method 1019, condition A.
- $\underline{3}$ / Unless otherwise specified, V_{IN} = -10 V and I_O = 500 mA for cases M, N, T, U, Y, Z, 4, and 5.

 V_{IN} = -10 V and I_O = 100 mA for cases X and 2. Maximum test current for cases X and 2 is 500 mA.

<u>4</u>/ For cases X and 2: $I_0 = 5$ mA to 500 mA, $P \le 2$ W. For case Y: $I_0 = 5$ mA to 1.0 A, $P \le 20$ W.

- For cases M, N, T, U, Z, 4, and 5: $I_0 = 5$ mA to 1.0 A, P \leq 15 W.
- 5/ All measurements except output noise voltage and ripple rejection are made at constant junction temperature and with low duty cycle.
- 6/ Minimum load current for full line regulation is 5.0 mA.
- $\overline{7}$ / For cases M, N, T, U, Y, Z, 4, and 5 only.
- 8/ For cases X and 2 only.
- <u>9</u>/ Guaranteed, if not tested, to the limits specified.
- <u>10</u>/ Short circuit protection is only assured up to $V_{IN} = -35$ V.

3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 <u>Marking</u>. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-HDBK-103 (see 6.6 herein). For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 <u>Certification/compliance mark</u>. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

3.6 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DSCC-VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

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Case outline M





Symbol	Inches Millimeters			
Symbol	Inches		Willinfielers	
	Min	Max	Min	Max
А	.160		4.06	
A ₂	.080		2.03	
b		.035		0.89
С	.020		0.51	
D		.425		10.8
E		.425		10.8
e	.200 BSC		5.08	BSC
e1	.100	BSC	2.54	BSC
L	.350		8.89	
L ₁		.135		3.43

NOTES:

1. The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.

2. Metric equivalents are given for general information only.

3. Three leads.

FIGURE 1. Case outlines.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000

REVISION LEVEL

F

5962-88746

Case outlines T and U



Inches	<u>mm</u>
.005	0.13
.010	0.25
.020	0.51

Letter	Inches		Millim	neters
	Min	Max	Min	Max
A	.190	.200	4.83	5.08
A1	.035	.045	0.89	1.14
A2	.120	BSC	3.05	BSC
φb	.025	.035	0.64	0.89
D	.645	.665	16.38	16.89
D1	.410	.430	10.41	10.92
D3	.000	.065	0.00	1.65
е	.100	.100 BSC		BSC
E	.410	.422	10.41	10.71
L	.500	.750	12.70	19.05
0	.527	.537	13.39	16.64
φP	.140	.150	3.56	3.81

NOTE:

The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.

FIGURE 1. Case outlines. - Continued.

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Case outline X



Letter	Inch	nes	Millimeters		Notes
	Min	Max	Min	Max	
A	.165	.185	4.19	4.70	
A1	.100	TP	2.54	T.P.	
φb	.016	.019	0.41	0.48	2
φb1	.016	.021	0.41	0.53	2
φD	.335	.370	8.51	9.40	
φD1	.305	.335	7.75	8.51	
е	.200 T.P.		5.08 T.P.		4
e1	.100 T.P.		2.54 T.P.		4
F		.050		1.27	
k	.028	.034	0.71	0.86	
k1	.029	.045	0.74	1.14	3
k2	.009	.041	0.23	1.04	
L	.500		12.70		
L1		.050		1.27	
L2	.250		6.35		
α	45°	T.P.	45°	T.P.	

NOTES:

- 1. The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.
- 2. (All leads) ϕ b applies between the L1 and L2. ϕ b1 applies between the L2 and .500 inch (12.70 mm) from the reference plane. Diameter is uncontrolled in L1 and beyond .500 inch (12.70 mm) from the reference plane.
- 3. Measured from the maximum diameter of the product.
- Leads having a maximum diameter of .019 inch (0.48 mm) measured in gauging plane .054 inch (1.37 mm) + .001 inch (0.03 mm) .000 inch (0.00 mm) below the base plane of the product shall be within .007 inch (0.18 mm) of their true-position relative to a maximum width tab.
- 5. The product may be measured by direct methods or by gauge.

FIGURE 1. Case outlines - Continued

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Case outline Y



Letter	Inc	hes	Millim	neters	Notes
	Min	Max	Min	Max	
A	.250	.450	6.35	11.43	
A1	1.177	1.197	29.90	30.40	
φb	.038	.043	0.97	1.09	2, 6
φD		.875		22.22	
е	.655	.675	16.64	17.14	4
e1	.420	.440	10.67	11.16	4
e2	.205	.225	5.21	5.72	
F	.060	.135	1.52	3.43	
φH	.151	.161	3.84	4.09	4, 5
L	.312	.500	7.92	12.70	3
L1		.050		1.27	2, 3
R	.495	.525	12.57	13.34	
R1	.131	.188	3.33	4.78	

NOTES:

- 1. The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.
- 2. (Two leads) ϕ b applies between the L1 and .500 inch (12.70 mm) from the seating plane. Diameter is uncontrolled in L1 and beyond .500 inch (12.70 mm) from the seating plane.
- 3. Two leads.
- 4. Two holes.
- 5. Two holes located at true position within diameter .010 inch (0.25 mm).
- Leads having a maximum diameter of .043 inch (1.09 mm) measured in gauging plane .054 inch (1.37 mm) + .001 inch (0.03 mm) .000 inch (0.00 mm) below the seating plane shall be located at true-position within diameter .014 inch (0.36 mm).
- 7. The mounting surface of the header shall be flat to convex within .003 inch (0.08 mm) inside a .930 inch (23.62 mm) diameter circle on the center of the header and flat to convex within .006 inch (0.15 mm) overall.

FIGURE 1. Case outlines - Continued.

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Case outline 4 Α E۰ D1 TERM 1 L1 TERM 2 b TERM 3 3 PLS P e1 Symbol Inches Millimeters

	Min	Max	Min	Max
А	.190	.210	4.83	5.33
b		.030		0.76
D	.410	.430	10.41	10.92
D1	.580	.610	14.73	15.49
е		.100		2.54
e1		.200		5.08
E	.410	.420	10.41	10.67
L1	.090	.110	2.29	2.79
L	.115	.125	2.92	3.18
N	3	3	3	3

NOTE:

The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.

FIGURE 1. <u>Case outline</u> – Continued.

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Device type			01		
Case outlines	Μ	N	2	4	5
Terminal			Terminal symbol		
1	Vout	OUTPUT	NC	GROUND	OUTPUT
2	ADJUST	GROUND	V _{IN}	INPUT	GROUND
3	V _{IN}	INPUT	NC	OUTPUT	INPUT
4	ISOLATED (CASE)		VOUT		
5			Vout		
6			NC		
7			Vout		
8			NC		
9			NC		
10			NC		
11			NC		
12			NC		
13			NC		
14			NC		
15			GND		
16			NC		
17			GND		
18			NC		
19			NC		
20			V _{IN}		

NOTE:

1. For case outline 2 normal operation, the input pins (2 and 20) are to be connected together, the output pins (4, 5, and 7) are to be connected together, and GND pins (15 and 17) are to be connected together.

2. NC = No connection

FIGURE 2. <u>Terminal connections</u> - Continued.

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Case outline U



3.8 <u>Notification of change</u>. Notification of change to DSCC-VA shall be required in accordance with MIL-PRF-38535, appendix A.

3.9 <u>Verification and review</u>. DSCC, DSCC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
 - (2) $T_A = +125^{\circ}C$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 <u>Quality conformance inspection</u>. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

- 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.
- 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.3 <u>Group E inspection</u>. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels shall be as specified in MIL-PRF-38535 or MIL-PRF-38535, Appendix A. End-point parameters shall be as specified in table II herein.

4.3.3.1 <u>Total dose irradiation testing</u>. Total dose irradiation testing shall be performed in accordance with MIL-STD-883 method 1019, condition A and as specified herein.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*, 2, 3, 4
Group A test requirements (method 5005)	1, 2, 3, 4, 5**, 6**, 7**
Groups C and D end-point electrical parameters (method 5005)	1
Group E end-point electrical parameters (method 5005)	1, 4

* PDA applies to subgroup 1.

** Subgroups 5, 6, and 7, if not tested, shall be guaranteed to the limits specified in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

6. NOTES

6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.4 <u>Record of users</u>. Military and industrial users shall inform Defense Supply Center Columbus when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0544.

6.5 <u>Comments</u>. Comments on this drawing should be directed to DSCC-VA, Columbus, Ohio 43216-5000, or telephone (614) 692-0547.

6.6 <u>Approved sources of supply</u>. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DSCC-VA.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 02-02-11

Approved sources of supply for SMD 5962-88746 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535.

Standard microcircuit drawing PIN <u>1</u> /	Vendor CAGE number	Vendor similar PIN <u>2</u> /
5962-8874601MA	69210	OM1905SM/883B
5962-8874601NA	21845	SDP7905ANMD
	69210	OM7905NM/883B
5962-8874601TA	21845	SDP7905ATMD
	69210	OM7905AH/883B
5962-8874601UA	21845	SDP7905AUMD
	27851	FM905S7
	34333	SG7905AIG/883B
	69210	OM7905AIH/883B
5962-8874601XA	34333	SG7905AT/883B
5962-8874601YA	34333	SG7905AK/883B
	69210	OM7905NK/883B
5962-8874601ZA	21845	SDP7905AZMD
	34333	SG7905AR/883B

Standard microcircuit drawing PIN <u>1</u> /	Vendor CAGE number	Vendor similar PIN <u>2</u> /
5962-88746012A	21845	SDP7905A2MD
	34333	SG7905AL/883B
	69210	OM1905N2/883B
5962-88746014A	69210	OM1905SRM/883B
5962-88746015A	69210	OM1905N5M/883B
5962F8874601UA	69210	OM1905STM/883B

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number	Vendor name and address
21845	Solitron Devices, Incorporated 3301 Electronics Way West Palm Beach, FL 33407-4697
27851	Film Microelectronics, Incorporated 530 Turnpike Street North Andover, MA 01845-5812
34333	Linfinity Microelectronics Incorporated 11861 Western Avenue Garden Grove, CA 92641-1816
69210	Omnirel Corporation 205 Crawford Street Leominster, MA 01453-2353

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.