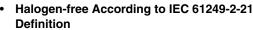




N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ)			
30	0.050 at V _{GS} = 10 V	4.5	3.16 nC			
	0.080 at V _{GS} = 4.5 V	3.4	3.10110			

FEATURES





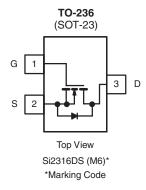
- PWM Optimized
- 100 % R_q tested
- Compliant to RoHS Directive 2002/95/EC





APPLICATIONS

- · Battery Switch
- DC/DC Converter



Ordering Information: Si2316BDS-T1-E3 (Lead (Pb)-free)

Si2316BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted							
Parameter	Symbol	Limit	Unit				
Drain-Source Voltage	V _{DS}	30	V				
Gate-Source Voltage	V _{GS}	± 20	V				
	T _C = 25 °C		4.5				
Continuous Drain Current (T = 150 °C)	T _C = 70 °C	I _D	3.6	A			
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C		3.9 ^{b, c}				
	T _A = 70 °C		3.13 ^{b, c}				
Pulsed Drain Current	I _{DM}	20					
Continuous Source-Drain Diode Current	T _C = 25 °C		1.39				
Continuous Source-Diairi Diode Current	T _A = 25 °C	I _S	1.04 ^{b, c}				
	T _C = 25 °C		1.66				
Maximum Power Dissipation	T _C = 70 °C	В	1.06	w			
Maximum Power Dissipation	T _A = 25 °C	P_{D}	1.25 ^{b, c}	·			
	T _A = 70 °C		0.8 ^{b, c}				
Operating Junction and Storage Temperature Range	T _J , T _{stq}	- 55 to 150	°C				

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	80	100	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{th IF}	60	75]		

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 moard.
- c. t = 5 s.
- d. Maximum under Steady State conditions is 130 $^{\circ}\text{C/W}.$

Si2316BDS

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					L		
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 \text{ V, } I_{D} = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			23.92			
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		5.2		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valta da Duais Comunant		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Durin Course On Otata Basistana d		$V_{GS} = 10 \text{ V}, I_D = 3.9 \text{ A}$		0.041	0.050	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 3.3 A		0.064	0.080		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15V, I _D = 3.9 A		6		S	
Dynamic ^b							
Input Capacitance	C _{iss}			350			
Output Capacitance	C _{oss}			65		1 _	
Reverse Transfer Capacitance	C _{rss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		37		pF	
Total Gate Charge	Q_g $V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.9 \text{ A}$	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 3.9 \text{ A}$		6.35	9.6		
			3.16	4.8			
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 3.9 \text{ A}$		1.56		nC	
Gate-Drain Charge	Q_{gd}			1.1			
Gate Resistance	R_{g}	f = 1 MHz		2.6	3.9	Ω	
Turn-On Delay Time	t _{d(on)}			4.5	6.75		
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 4.8 \Omega$		11	16.5		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 3.13 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 1 \Omega$		12	18	ns	
Fall Time	t _f			7	10.5		
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 6.25 \Omega$		65	98	nc	
Turn-Off Delay Time	t _{d(off)}	$I_D = 2.4 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		11	17	ns	
Fall Time	t _f			23	35	1	
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			1.39	٨	
Pulse Diode Forward Current ^a	I _{SM}				20	A	
Body Diode Voltage	V_{SD}	I _S = 2.0 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			10	15	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 2.0 A dl/dt = 100 A/up T = 25 °C		4	6	nC	
Reverse Recovery Fall Time	t _a	$I_F = 2.0 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		6.6			
Reverse Recovery Rise Time	t _b			3.5		ns	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

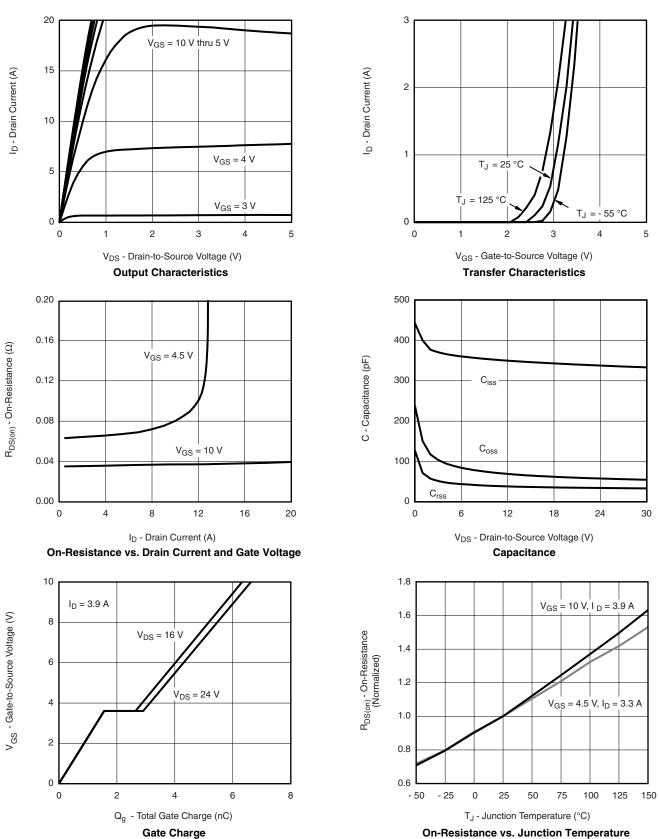
Notes: a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.





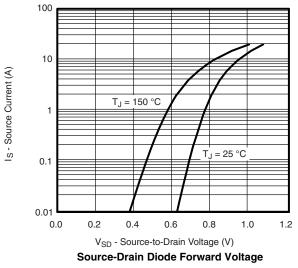


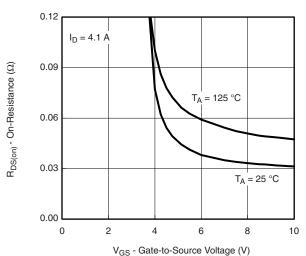
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

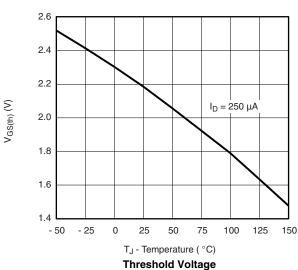


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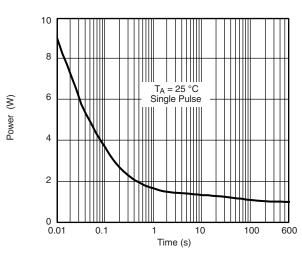
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



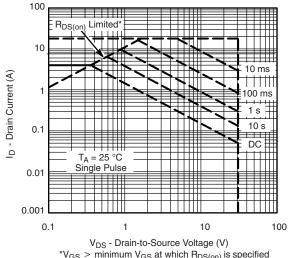




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power

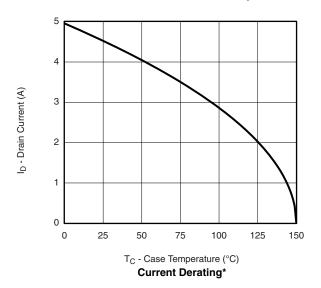


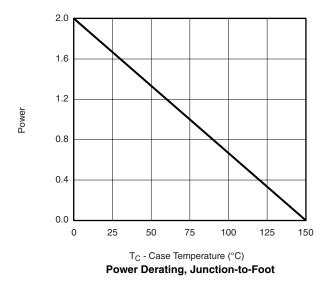
 $^{\star}V_{GS}$ > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

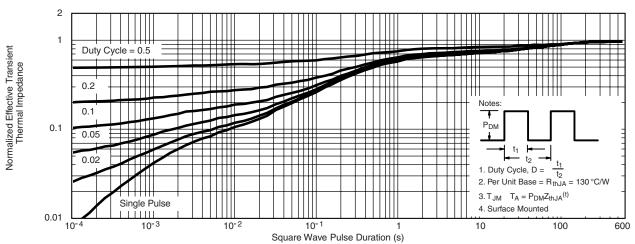


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





*The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg270445.

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SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.9	5 BSC	0.037	4 Ref	
e ₁	1.90 BSC		0.074	18 Ref	
L	0.40	0.60	0.016	0.024	
L ₁	0.6	64 Ref 0.025 Ref		5 Ref	
S	0.5	50 Ref 0.020 Ref) Ref	
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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