



048227

# HS-C<sup>2</sup>MOS™ INTEGRATED CIRCUITS

## PRODUCT PREVIEW

### OCTAL BUS BUFFER WITH 3-STATE OUTPUTS

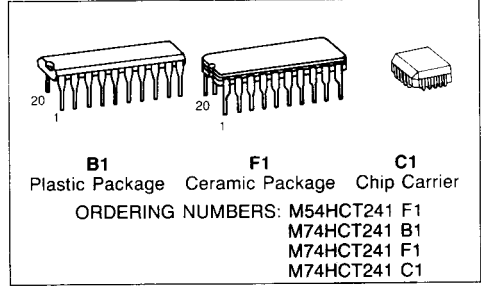
#### DESCRIPTION

The M54/74HCT241 is a high speed CMOS OCTAL BUS BUFFER WITH 3-STATE OUTPUTS fabricated with silicon gate C<sup>2</sup>MOS technology. This is a non inverting buffer that has one active low enable and one active high enable, each again controlling 4 buffers.

These devices are designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge or transient excess voltage. This integrated circuit has totally compatible, input and output characteristic, with standard 54/74 LSTTL logic families.

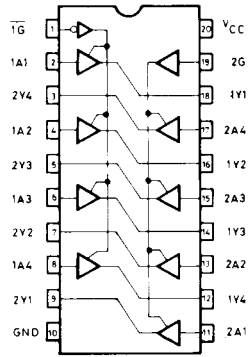
M54HCT/74HCT devices are designed to directly interface HSC<sup>2</sup>MOS systems with TTL and NMOS components. These devices are also plug in replacements for LSTTL devices giving a reduction of power consumption.



#### FEATURES

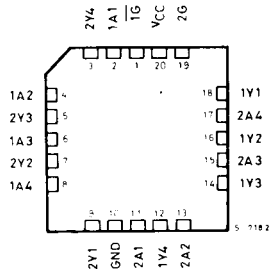
- Low Power Dissipation  
 $I_{CC} = 4 \mu A$  (Max.) at  $T_A = 25^\circ C$
- High Noise Immunity  
 $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (Min.)
- Output Drive Capability  
15 LSTTL Loads
- Symmetrical Output Impedance  
 $I_{OH} = I_{OL} = 6 mA$  (Min.)
- Balanced Propagation Delays  
 $t_{PLH} = t_{PHL}$
- Pin and Function compatible with 54/74LS241

#### PIN CONNECTIONS (top view)



Dual in line

#### CHIP CARRIER



#### INPUT AND OUTPUT EQUIVALENT CIRCUIT

