■ Static RAMs

Process	Capacity	Configuration (words x bits)	Model No.	Access time (ns) MAX. Cycle time (ns) MIN.	Supply current Operating (mA) MAX.	Supply current Standby (mA) MAX.	Supply voltage (V)	Operating temp. (°C)	Package
Full CMOS	16k	2k x 8	LH5116/NA/D-10	100	40	0.001	5 ± 10%	0 to 70	24DIP/24SOP/24SK-DIP
			LH5116H/HN/HD-10	100	40	0.001	5 ± 10%	−40 to 85	24DIP/24SOP/24SK-DIP
			LH5116SN	1 000	10	0.001	3 ± 10%	0 to 50	24SOP
	64k	8k x 8	LH5164A/AN-80L	80	55	0.001	5 ± 10%	−10 to 70	28DIP/28SOP
			LH5164A/AN/AD/AT-10L	100	45	0.001			28DIP/28SOP/28SK-DIP/ 28TSOP(I) Normal
			LH5164AH/AHN/AHD/AHT-10L	100	50	0.001*6	5 ± 10%	−40 to 85	28DIP/28SOP/28SK-DIP/ 28TSOP(I) Normal
			LH5164AVN/AVT	200	45	0.001	2.7 to 5.5	−10 to 70	28SOP/28TSOP(I) Normal
			LH5164AVHN/AVHT	200	25	0.001	3.0 to 3.6	−40 to 85	28SOP/28TSOP(I) Normal
			LH5164ASHN/ASHT	500	20	0.001*6	2.5 to 5.5	−40 to 85	28SOP/28TSOP(I) Normal
	256k	32k x 8	LH51256/N-10L	100	50	0.001*7	5 ± 10%	−40 to 85	28DIP/28SOP
			LH51V256N/T-85SL	250*1 85*2	25	0.003	1.8 to 3.6	0 to 70	28SOP/28TSOP(I) Normal
			LH51V256HN/HT-85SL	250*1 85*2	25	0.005	1.8 to 3.6	−40 to 85	28SOP/28TSOP(I) Normal
CMOS Periphery	64k	8k x 8	LH5288A/AN/AD-10LL	100	40	0.04	5 ± 10%	0 to 70	28DIP/28SOP/28SK-DIP
	256k	32k x 8	LH52258AD/AK-20	20	150	1	5 ± 10%	0 to 70	28SK-DIP/28SOJ(300mil)
			LH52258AD/AK-25	25	140	1			
			LH52256AN/AT-70LL	70	45	0.04	5 ± 10%	0 to 70	28SOP/28TSOP(I) Normal
			LH52256AN/AT-85LL	85	40	0.04			
			LH52256AN/AT-10LL	100	40	0.04			
			LH52256CN/CT-70LL	70	45	0.04	5 ± 10%	0 to 70	28SOP/28TSOP(I) Normal
			LH52256CN/CT-85LL	85	40	0.04			
			LH52256CN/CT-10LL	100	40	0.04			
			LH52256CHN/CHT-85LL	85	40	0.04	5 ± 10%	−40 to 85	28SOP/28TSOP(I) Normal
			LH52256AVN/AVT/AVTR	200*3 150*4	30	0.04	2.7 to 5.5	−10 to 70	28SOP/ 28TSOP(I) Normal / Reverse
			LH52256AVHN/AVHT	200*3 150*4	15	0.04	2.7 to 3.6	−25 to 85	28SOP/28TSOP(I) Normal
			LH52256ASN/AST	500	10	0.02	2.5 to 3.6	−10 to 70	28SOP/28TSOP(I) Normal
			LH52256CVN/CVT	200*2 150*5	15	0.02	2.7 to 3.6	0 to 70	28SOP/28TSOP(I) Normal
			LH52256CSN/CST	500	10	0.02	2.5 to 3.6	0 to 70	28SOP/28TSOP(I) Normal
	512k	64k x 8	LH52512N-85LL	85	50	0.05	5 ± 10%	0 to 70	32SOP
			LH52512N-10LL	100	50	0.05			
	1M	256k x 4	LH521002AK-20	20	140	2	5 ± 10%	0 to 70	28SOJ(400mil)
			LH521002AK-25	25	130	2			
		128k x 8	LH521007AK-20	20	175	2	5 ± 10%	0 to 70	32SOJ(400mil)
			LH521007AK-25	25	165	2			
		64k x 18	LH521028AU-15	15	300	4	5 ± 10%	0 to 70	52QFJ
			LH521028AU-17	17	300	4			
			LH521028AU-20	20	300	4			
			LH521028AU-25	25	300	4			
		32k x 32	LH51V1032C4M-15	8*8 15*9	220	2	3.3 ± 5%	0 to 70	100LQFP
			LH51V1032C4M-17	9*8 17*9	200	2			

*1 1.8 V ≦ Vcc ≦ 3.6 V *3 2.7 V ≦ Vcc ≦ 5.5 V *5 3.0 V ≦ Vcc ≦ 3.6 V *6 Standby : Ta = 0 to 70°C *8 Access time QFJ = PLCC
*2 2.7 V ≦ Vcc ≦ 3.6 V *4 3.0 V ≦ Vcc ≦ 5.5 V *7 Standby : Ta = 0 to 60°C *9 Cycle time