



**National
Semiconductor**

Analog Switches/Multiplexers

**AM181/281, 182/282, 184/284, 185/285,
187/287, 188/288, 190/290, 191/291**

Monolithic N-Channel Junction FET Switches with High Speed Drivers

AM181/AM281, AM182/AM282 dual driver with SPST switches

AM184/AM284, AM185/AM285 dual driver with DPST switches

AM187/AM287, AM188/AM288 single driver with SPDT switches

AM190/AM290, AM191/AM291 dual driver with SPDT switches

General Description

These devices combine N-channel junction FETs and bipolar transistors on a single chip for the first time in a new N-channel Bi-FET process.

This technology provides the industry's only low "ON" resistance, high speed, monolithic N-channel junction FET analog switch. Unique circuit techniques are employed to achieve break-before-make switching action and constant "ON" resistance over the analog voltage range. The switch can block 20V peak-to-peak signals, and because of the driver design, an "OFF" isolation greater than 60 dB is achieved at 10 MHz.

Features

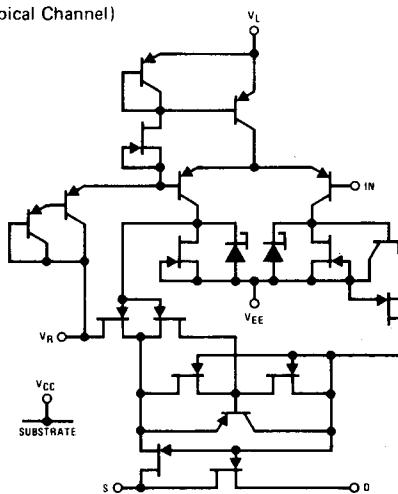
- Interfaces with standard DTL, TTL and CMOS
- Constant "ON" resistance with signals to $\pm 10V$

- "ON" resistance match 2Ω typ
- "OFF" isolation and crosstalk less than -60 dB at 10 MHz (typ)
- $t_{ON}/t_{OFF} = 105$ ns/95 ns typ
- Break-before-make action

Applications

- A-to-D/D-to-A converters
- Data acquisition
- Signal multiplexers
- Sample and hold
- Video switch

Schematic Diagram (Typical Channel)



Application Hints*

V_{CC} Positive Supply Voltage (V)	V_{EE} Negative Supply Voltage (V)	V_L Logic Supply Voltage (V)	V_R Reference Supply Voltage (V)	V_{IN} Logic Input Voltage V_{INH} Min/ V_{INL} Max— (V)	100 Series V_S Analog Voltage Range (V)	200 Series V_S Analog Signal Range (V)
+15**	-15	+5	Gnd	2.0/0.8	-7.5 to +15	-10 to +15
+10	-20	+5	Gnd	2.0/0.8	-12.5 to +10	-15 to +10
+12	-12	+5	Gnd	2.0/0.8	-4.5 to +12	-7 to +12

* Applications Hints are for design aid only, not guaranteed and not subject to production testing.

** Electrical Parameter Chart based on $V_{CC} + 15V$, $V_{EE} = -15V$, $V_L = 5V$, $V_R = \text{Gnd}$

**AM181/281, 182/282, 184/284, 185/285,
187/287, 188/288, 190/290, 191/291**

Absolute Maximum Ratings

V _{CC} - V _{EE}	36V
V _{CC} - V _D	33V
V _D - V _{EE}	33V
V _D - V _S	±22V
V _L - V _{EE}	36V
V _L - V _{IN}	8V
V _L - V _R	8V
V _{IN} - V _R	8V
V _R - V _{EE}	27V
V _R - V _{IN}	2V
Current (Any Terminal)	30 mA

Storage Temperature	~65 ^b °C to +150°C
Operating Temperature	-55°C to +125°C
Power Dissipation*	
Metal Can**	450 mW
14-Pin DIP***	825 mW
16-Pin DIP****	900 mW

* All leads soldered to PC board

** Derate 6 mW/°C above 75°C

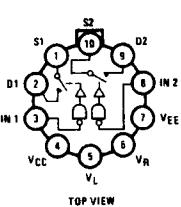
*** Derate 11 mW/°C above 75°C

**** Derate 12 mW/°C above 75°C

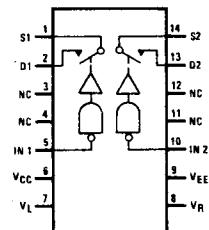
Connection Diagrams

AM181/AM281, AM182/AM282^a

Metal Can Package
See NS Package H10A
Order by Part Number
Followed by H Suffix

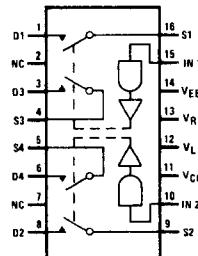


Switch states are for logical "1" input



Dual-In-Line Package
See NS Package D14A
Order by Part Number
Followed by D Suffix

AM184/AM284, AM185/AM285^a

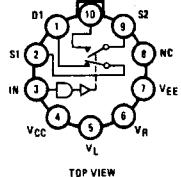


Dual-In-Line Package
See NS Package D16A
Order by Part Number
Followed by D Suffix

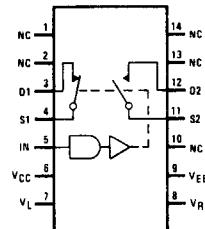
Switch states are for logical "0" input

AM187/AM287, AM188/AM288^a

Metal Can Package
See NS Package H10A
Order by Part Number
Followed by H Suffix

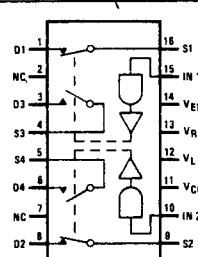


Switch states are for logical "1" input



Dual-In-Line Package
See NS Package D14A
Order by Part Number
Followed by D Suffix

AM190/AM290, AM191/AM291^a



Dual-In-Line Package
See NS Package D16A
Order by Part Number
Followed by D Suffix

Switch states are for logical "1" input

^aConsult local sales representative or factory for information concerning the 14-pin flat package

Electrical Characteristics AM181/AM281, AM182/AM282

dc parameters are 100% tested at 25°C; ac parameters, high and low temperatures, and t_{ON}, t_{OFF} are sampled to ensure conformance with specifications.

PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0			MAX LIMITS						UNITS	
				AM181			AM281				
				-55°C	25°C	125°C	-20°C	25°C	85°C		
r _{DSON}	Drain-Source "ON" Resistance	I _S = -10 mA, V _{IN} = 0.8V	V _D = -7.5V	30	30	60	50	50	75	Ω	
I _{S(OFF)}	Source "OFF" Leakage Current	V _{IN} = 2V	V _S = 10V, V _D = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA	
			V _S = 7.5V, V _D = -7.5V		1	100		5	100		
I _{D(OFF)}	Drain "OFF" Leakage Current	V _{IN} = 2V	V _D = 10V, V _S = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA	
			V _D = 7.5V, V _S = -7.5V		1	100		5	100		
I _{D(ON)} + I _{S(ON)}	Channel "ON" Leakage Current	V _{IN} = 0.8V	V _D = V _S = -7.5V		-2	-200		-10	-200		
I _{INL}	Input Current, Input Voltage Low	V _{IN} = 0		-250	-250	-250	-250	-250	-250	μA	
I _{INH}	Input Current, Input Voltage High	V _{IN} = 5V			10	20		10	20		
t _{ON}	Turn "ON" Time	See Switching Time Test Circuit				150		180		ns	
t _{OFF}	Turn "OFF" Time		130			150					
PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0			MAX LIMITS						UNITS	
				AM182			AM282				
				-55°C	25°C	125°C	-20°C	25°C	85°C		
r _{DSON}	Drain-Source "ON" Resistance	I _S = -10 mA, V _{IN} = 0.8V	V _D = -10V	75	75	100	100	100	150	Ω	
I _{S(OFF)}	Source "OFF" Leakage Current	V _{IN} = 2V	V _S = 10V, V _D = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA	
			V _S = 10V, V _D = -10V		1	100		5	100		
I _{D(OFF)}	Drain "OFF" Leakage Current	V _{IN} = 2V	V _D = 10V, V _S = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA	
			V _D = 10V, V _S = -10V		1	100		5	100		
I _{D(ON)} + I _{S(ON)}	Channel "ON" Leakage Current	V _{IN} = 0.8V	V _D = V _S = -10V		-2	-200		-10	-200		
I _{INL}	Input Current, Input Voltage Low	V _{IN} = 0		-250	-250	-250	-250	-250	-250	μA	
I _{INH}	Input Current, Input Voltage High	V _{IN} = 5V			10	20		10	20		
t _{ON}	Turn "ON" Time	See Switching Time Test Circuit				250		300		ns	
t _{OFF}	Turn "OFF" Time		130			150					
PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0			MAX LIMITS						UNITS	
				AM181, AM182			AM281, AM282				
				-55°C	25°C	125°C	-20°C	25°C	85°C		
C _{S(OFF)}	Source "OFF" Capacitance	f = 1 MHz	V _S = -5V, I _D = 0				9 Typical, (Note 1)			pF	
C _{D(OFF)}	Drain "OFF" Capacitance		V _D = -5V, I _S = 0				6 Typical, (Note 1)				
C _{D(ON)} + C _{S(ON)}	Channel "ON" Capacitance		V _D = V _S = 0				14 Typical, (Note 1)				
"OFF" Isolation				R _L = 75 Ω > 60 dB at 10 MHz Typical, (Note 1)							
ICC	Positive Supply Current	Both V _{IN} = 0, All Channels "ON"				0.1		0.1		mA	
IEE	Negative Supply Current					-5		-5			
IL	Logic Supply Current					4.5		4.5			
IR	Reference Supply Current					-2		-2			
ICC	Positive Supply Current					0.1		0.1			
IEE	Negative Supply Current	Both V _{IN} = 5V, All Channels "OFF"				-5		-5			
IL	Logic Supply Current					4.5		4.5			
IR	Reference Supply Current					-2		-2			

Note 1: Typical values are for Design Aid *only*, not guaranteed and not subject to production testing.

Electrical Characteristics AM184/AM284, AM185/AM285

dc parameters are 100% tested at 25°C; ac parameters, high and low temperatures, and t_{ON}, t_{OFF} are sampled to ensure conformance with specifications.

PARAMETER	TEST CONDITIONS, UNLESS NOTED: $V_{CC} = 15V, V_{EE} = -15V, V_L = 5V, VR = 0$	MAX LIMITS						UNITS				
		AM184			AM284							
		-55°C	25°C	125°C	-20°C	25°C	85°C					
$r_{DS(ON)}$	Drain-Source ON Resistance	$I_S = -10 \text{ mA}, V_{IN} = 2V$	$V_D = -7.5V$	30	30	60	50	50	75	Ω		
$I_{S(OFF)}$	Source OFF Leakage Current	$V_{IN} = 0.8V$	$V_S = 10V, V_D = -10V, V_{CC} = 10V, V_{EE} = -20V$		1	100		5	100	nA		
			$V_S = 7.5V, V_D = -7.5V$		1	100		5	100			
			$V_D = 10V, V_S = -10V, V_{CC} = 10V, V_{EE} = -20V$		1	100		5	100			
			$V_D = 7.5V, V_S = -7.5V$		1	100		5	100			
$I_{D(OFF)} + I_{S(ON)}$	Channel ON Leakage Current	$V_{IN} = 2V$	$V_D = V_S = -7.5V$		-2	-200		-10	-200			
I_{INL}	Input Current, Input Voltage Low	$V_{IN} = 0$		-250	-250	-250	-250	-250	-250	μA		
I_{INH}	Input Current, Input Voltage High	$V_{IN} = 5V$			10	20		10	20			
t_{ON}	Turn ON Time	See Switching Time Test Circuit			150			180		ns		
t_{OFF}	Turn OFF Time				130			150				
PARAMETER	TEST CONDITIONS, UNLESS NOTED: $V_{CC} = 15V, V_{EE} = -15V, V_L = 5V, VR = 0$	MAX LIMITS						UNITS				
		AM185			AM285							
		-55°C	25°C	125°C	-20°C	25°C	85°C					
$r_{DS(ON)}$	Drain-Source ON Resistance	$I_S = -10V, V_{IN} = 2V$	$V_D = -10V$	75	75	150	100	100	150	Ω		
$I_{S(OFF)}$	Source OFF Leakage Current	$V_{IN} = 0.8V$	$V_S = 10V, V_D = -10V, V_{CC} = 10V, V_{EE} = -20V$		1	100		5	100	nA		
			$V_S = 10V, V_D = -10V$		1	100		5	100			
			$V_D = 10V, V_S = -10V, V_{CC} = 10V, V_{EE} = -20V$		1	100		5	100			
			$V_D = 10V, V_S = -10V$		1	100		5	100			
$I_{D(ON)} + I_{S(ON)}$	Channel ON Leakage Current	$V_{IN} = 2V$	$V_D = V_S = -10V$		-2	-200		-10	-200			
I_{INL}	Input Current, Input Voltage Low	$V_{IN} = 0$		-250	-250	-250	-250	-250	-250	μA		
I_{INH}	Input Current, Input Voltage High	$V_{IN} = 5V$			10	20		10	20			
t_{ON}	Turn ON Time	See Switching Time Test Circuit			250			300		ns		
t_{OFF}	Turn OFF Time				130			150				
PARAMETER	TEST CONDITIONS, UNLESS NOTED: $V_{CC} = 15V, V_{EE} = -15V, V_L = 5V, VR = 0$	MAX LIMITS						UNITS				
		AM184, AM185			AM284, AM285							
		-55°C	25°C	125°C	-20°C	25°C	85°C					
$C_{S(OFF)}$	Source OFF Capacitance	$f = 1 \text{ MHz}$	$V_S = -5V, I_D = 0$	9 Typical, (Note 1)						pF		
$C_{D(OFF)}$	Drain OFF Capacitance		$V_D = -5V, I_S = 0$	6 Typical, (Note 1)								
$C_{D(ON)} + C_{S(ON)}$	Channel ON Capacitance		$V_D = V_S = 0$	14 Typical, (Note 1)								
"OFF" Isolation	$R_L = 75 \Omega$			> 60 dB at 10 MHz Typical, (Note 1)								
I_{CC}	Positive Supply Current	Both $V_{IN} = 5V$, All Channels "ON"		0.1			0.1			mA		
I_{EE}	Negative Supply Current			-4			-4					
I_L	Logic Supply Current			4.5			4.5					
I_R	Reference Supply Current			-2			-2					
I_{CC}	Positive Supply Current	Both $V_{IN} = 0$, All Channels "OFF"		0.1			0.1			mA		
I_{EE}	Negative Supply Current			-5.5			-5.5					
I_L	Logic Supply Current			4.5			4.5					
I_R	Reference Supply Current			-2			-2					

Note 1: Typical values are for Design Aid only, not guaranteed and not subject to production testing.

**AM181/281, 182/282, 184/284, 185/285,
187/287, 188/288, 190/290, 191/291**

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Electrical Characteristics AM187/AM287, AM188/AM288

dc parameters are 100% tested at 25°C; ac parameters, high and low temperatures, and t_{ON}, t_{OFF} are sampled to ensure conformance with specifications.

PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0	MAX LIMITS						UNITS			
		AM187			AM287						
		-55°C	25°C	125°C	-20°C	25°C	85°C				
I _{D(ON)}	I _S = -10 mA, V _{IN} = 2V, Ch. 1 "ON", V _{IN} = 0.8V, Ch. 2 "ON"	V _D = -7.5V	30	30	60	50	50	75	Ω		
I _{S(OFF)}	Source "OFF" Leakage Current	V _S = 10V, V _D = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA		
		V _S = 7.5V, V _D = -7.5V		1	100		5	100			
I _{D(OFF)}	Drain "OFF" Leakage Current	V _D = 10V, V _S = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA		
		V _D = 7.5V, V _S = -7.5V		1	100		5	100			
I _{D(ON) + I_{S(ON)}}	Channel "ON" Leakage Current	V _D = V _S = -7.5V		-2	-200		-10	-200			
I _{INL}	Input Current, Input Voltage Low	V _{IN} = 0	-250	-250	-250	-250	-250	-250	μA		
I _{INH}	Input Current, Input Voltage High	V _{IN} = 5V		10	20		10	20			
t _{ON}	Turn "ON" Time			150			180		ns		
t _{OFF}	Turn "OFF" Time	See Switching Time Test Circuit		130			150				
PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0	MAX LIMITS						UNITS			
		AM187			AM288						
		-55°C	25°C	125°C	-20°C	25°C	85°C				
I _{D(ON)}	I _S = -10 mA, V _{IN} = 0.8V, Ch. 2 "ON", V _{IN} = 2V, Ch. 1 "ON"	V _D = -10V	75	75	150	100	100	150	Ω		
I _{S(OFF)}	Source "OFF" Leakage Current	V _S = 10V, V _D = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA		
		V _S = 10V, V _D = -10V		1	100		5	100			
I _{D(OFF)}	Drain "OFF" Leakage Current	V _D = 10V, V _S = -10V, V _{CC} = 10V, V _{EE} = -20V		1	100		5	100	nA		
		V _D = 10V, V _S = -10V		1	100		5	100			
I _{D(ON) + I_{S(ON)}}	Channel "ON" Leakage Current	V _D = V _S = -10V		-2	-200		-10	-200			
I _{INL}	Input Current, Input Voltage Low	V _{IN} = 0	-250	-250	-250	-250	-250	-250	μA		
I _{INH}	Input Current, Input Voltage High	V _{IN} = 5V		10	20		10	20			
t _{ON}	Turn "ON" Time			250			300		ns		
t _{OFF}	Turn "OFF" Time	See Switching Time Test Circuit		130			150				
PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0	MAX LIMITS						UNITS			
		AM187, AM188			AM287, AM288						
		-55°C	25°C	125°C	-20°C	25°C	85°C				
C _{S(OFF)}	Source "OFF" Capacitance	V _S = -5V, I _D = 0	9 Typical, (Note 1)						pF		
C _{D(OFF)}	Drain "OFF" Capacitance	V _D = 5V, I _S = 0	6 Typical, (Note 1)								
		V _D = V _S = 0	14 Typical, (Note 1)								
C _{D(ON) + C_{S(ON)}}	Channel "ON" Capacitance "OFF" Isolation	R _L = 75Ω	> 60 dB at 10 MHz Typical, (Note 1)						mA		
				0.1			0.1				
I _{CC}	Positive Supply Current			-3			-3				
I _{EE}	Negative Supply Current	V _{IN} = 0, Ch. 2 "ON", Ch. 1 "OFF"		3.2			3.2				
I _L	Logic Supply Current			-2			-2				
I _R	Reference Supply Current			0.1			0.1				
I _{CC}	Positive Supply Current	V _{IN} = 5V, Ch. 2 "OFF", Ch. 1 "ON"		-3			-3				
I _{EE}	Negative Supply Current			3.2			3.2				
I _L	Logic Supply Current			-2			-2				
I _R	Reference Supply Current			0.1			0.1				

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187/287, 188/288, 190/290, 191/291

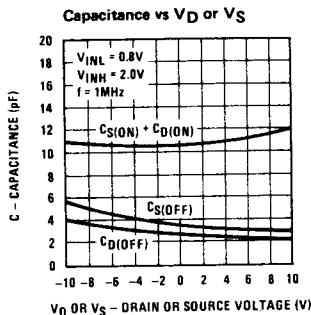
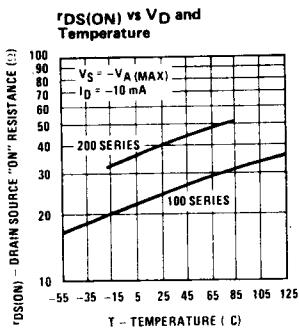
Electrical Characteristics AM190/AM290, AM191/AM291

dc parameters are 100% tested at 25°C; ac parameters, high and low temperatures, and t_{ON}, t_{OFF} are sampled to ensure conformance with specifications.

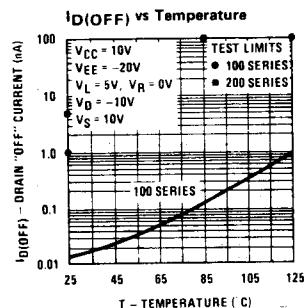
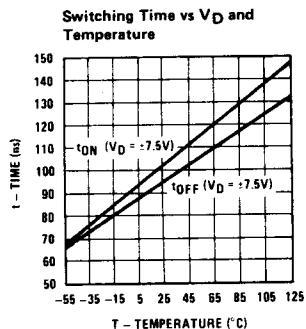
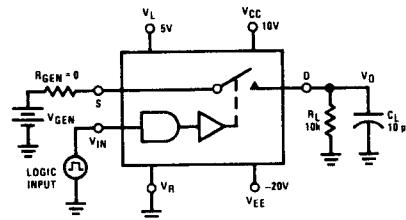
PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0	MAX LIMITS						UNITS			
		AM190		AM290							
		-55°C	25°C	125°C	-20°C	25°C	85°C				
I _{D(ON)}	Drain-Source ON Resistance	I _S = -10 mA, V _{IN} = 2V, Ch. 1 and 2 "ON", V _{IN} = 0.8V, Ch. 3 and 4 "ON"	V _D = -7.5V	30	30	60	50	50	75	Ω	
I _{S(OFF)}	Source OFF Leakage Current	V _{IN} = 2V, Ch. 3 and 4 "OFF" V _{IN} = 0.8V, Ch. 1 and 2 "OFF"	V _S = 10V, V _D = -10V, V _{CC} = 10V, V _{EE} = -20V V _S = 7.5V, V _D = -7.5V	1	100		5	100		nA	
I _{D(OFF)}	Drain OFF Leakage Current	V _{IN} = 2V, Ch. 3 and 4 "OFF" V _{IN} = 0.8V, Ch. 1 and 2 "OFF"	V _D = 10V, V _S = -10V, V _{CC} = 10V, V _{EE} = -20V V _D = 7.5V, V _S = -7.5V	1	100		5	100		nA	
I _{D(ON)} + I _{S(ON)}	Channel ON Leakage Current	V _{IN} = 2V, Ch. 1 and 2 "ON" V _{IN} = 0.8V, Ch. 3 and 4 "ON"	V _D = V _S = -7.5V	-2	-200		-10	-200			
I _{INL}	Input Current, Input Voltage Low	V _{IN} = 0		-250	-250	-250	-250	-250	-250	μA	
I _{INH}	Input Current, Input Voltage High	V _{IN} = 5V			10	20		10	20		
t _{ON}	Turn ON Time				150			180		ns	
t _{OFF}	Turn OFF Time	See Switching Time Test Circuit			130			150			
PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0	MAX LIMITS						UNITS			
		AM191		AM291							
		-55°C	25°C	125°C	-20°C	25°C	85°C				
I _{D(ON)}	Drain-Source ON Resistance	I _S = -10 mA, V _{IN} = 0.8V, Ch. 3 and 4 "ON", V _{IN} = 2V, Ch. 1 and 2 "ON"	V _D = -10V	75	75	150	100	100	150	Ω	
I _{S(OFF)}	Source OFF Leakage Current	V _{IN} = -0.8V, Ch. 1 and 2 "OFF" V _{IN} = -2V, Ch. 3 and 4 "OFF"	V _S = 10V, V _D = -10V, V _{CC} = 10V, V _{EE} = -20V V _S = 10V, V _D = -10V	1	100		5	100		nA	
I _{D(OFF)}	Drain OFF Leakage Current	V _{IN} = 2V, Ch. 3 and 4 "OFF"	V _D = 10V, V _S = -10V, V _{CC} = 10V, V _{EE} = -20V V _D = 10V, V _S = -10V	1	100		5	100		nA	
I _{D(ON)} + I _{S(ON)}	Channel ON Leakage Current	V _{IN} = 0.8V, Ch. 3 and 4 "ON" V _{IN} = 2V, Ch. 1 and 2 "ON"	V _D = V _S = -10V	-2	-200		-10	-200			
I _{INL}	Input Current, Input Voltage Low	V _{IN} = 0		-250	-250	-250	-250	-250	-250	μA	
I _{INH}	Input Current, Input Voltage High	V _{IN} = 5V			10	20		10	20		
t _{ON}	Turn ON Time				250			300		ns	
t _{OFF}	Turn OFF Time	See Switching Time Test Circuit			130			150			
PARAMETER	TEST CONDITIONS, UNLESS NOTED: V _{CC} = 15V, V _{EE} = -15V, V _L = 5V, V _R = 0	MAX LIMITS						UNITS			
		AM190, AM191		AM290, AM291							
		-55°C	25°C	125°C	-20°C	25°C	85°C				
C _{S(OFF)}	Source OFF Capacitance	f = 1 MHz	V _S = -5V, I _D = 0	9 Typical, (Note 1)					pF		
C _{D(OFF)}	Drain OFF Capacitance		V _D = 5V, I _S = 0	6 Typical, (Note 1)							
C _{D(ON)} + C _{S(ON)}	Channel ON Capacitance		V _D = V _S = 0	14 Typical, (Note 1)							
"OFF" Isolation	R _L = 75 Ω	> 60 dB at 10 MHz Typical, (Note 1)									
I _{CC}	Positive Supply Current	VIN = 0, Ch. 3 and 4 "ON", Ch. 1 and 2 "OFF"		0.1			0.1		mA		
I _{EE}	Negative Supply Current			-5			-5				
I _L	Logic Supply Current			4.5			4.5				
I _R	Reference Supply Current			-2			-2				
I _{CC}	Positive Supply Current			0.1			0.1				
I _{EE}	Negative Supply Current	VIN = 5V, Ch. 3 and 4 "OFF", Ch. 1 and 2 "ON"		-5			-5				
I _L	Logic Supply Current			4.5			4.5				
I _R	Reference Supply Current			-2			-2				

Note 1: Typical values are for Design Aid only, not guaranteed and not subject to production testing.

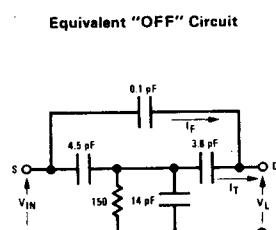
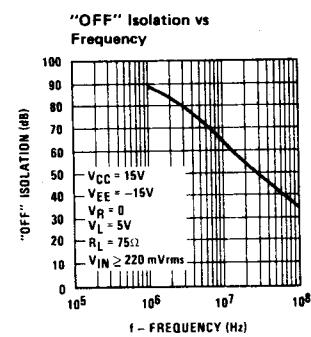
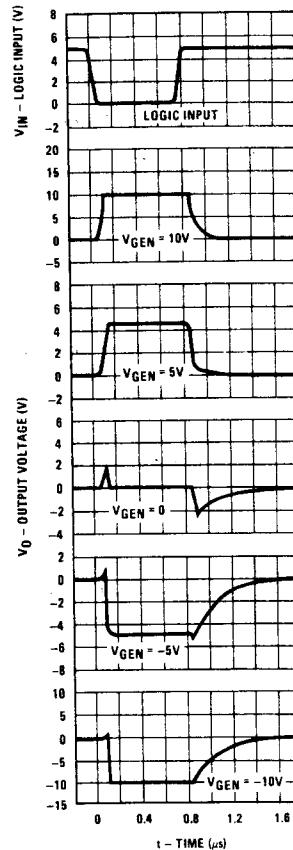
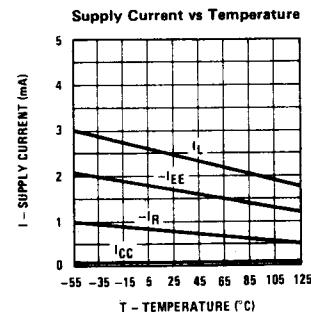
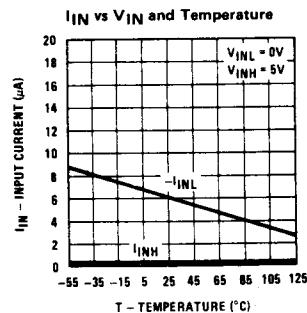
Typical Performance Characteristics $V_{CC} = 15V$, $V_{EE} = -15V$, $V_L = 5V$, $V_R = 0$ unless otherwise noted.



Typical delay, rise, fall, settling times, and switching transients in this circuit.



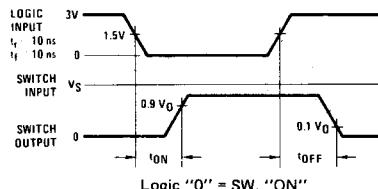
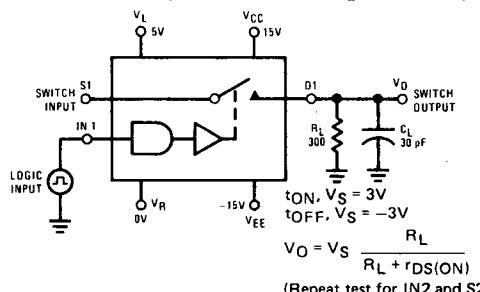
If R_{GEN} , R_L or C_L is increased there will be proportional increases in rise and/or fall RC times.



AM181/281, 182/282, 184/284, 185/285,
187/287, 188/288, 190/290, 191/291

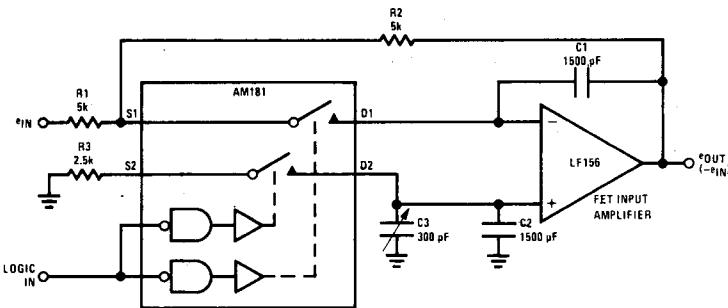
Switching Time Test Circuit

Switch output waveform shown for $V_S = \text{constant}$ with logic input waveform as shown. Note that V_S may be + or - as per switching time test circuit. V_O is the steady state output with switch "ON". Feedthrough via gate capacitance may result in spikes at leading and trailing edge of output waveform.



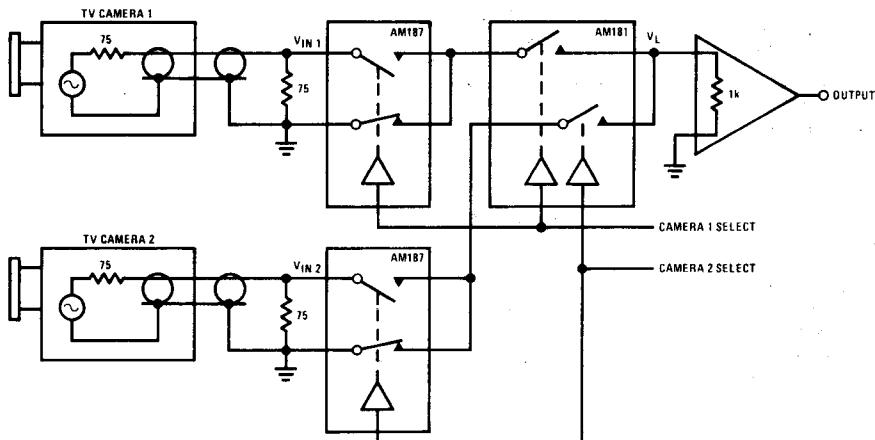
Typical Applications

Low Drift-Compensated Sample and Hold



- Input impedance 5 k Ω
- Slew rate limiting and 3 dB point: 20V swing: 3.2K C; 5V swing: 12K C; small signal: 21K C
- Droop rate @ 25°C 0.5 nV per μ s
- Sample to hold offset adjustable to zero
- Acquisition time—98 μ s
- Aperture time—80 ns
- Aperture uncertainty—2 ns

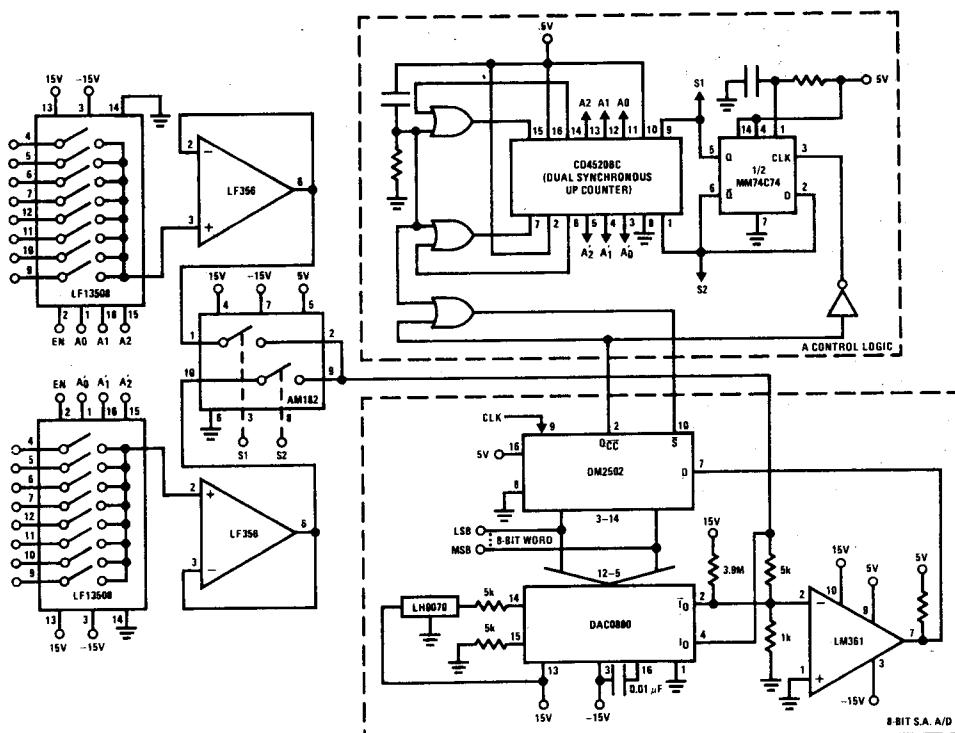
Video Switch with Very High "OFF" Isolation (f = dc to 10 MHz)



- 116 dB isolation at 10 MHz, "OFF" camera to "ON" camera
- 98 dB isolation at 10 MHz, load from each camera when both cameras are "OFF"
- < 1 dB on insertion loss

Typical Applications (Continued)

A 16-Channel Data Acquisition Unit with Second Level Multiplexing



- Maximum A/D clock frequency: 4.5 MHz
- Maximum throughput rate: 31.25k samples/sec
- Minimum switch "ON" time for the 2-channel MUX: $t_{ON(min)} \leq 1/4.5 \text{ MHz}$
- Maximum input signal bandwidth 15.6 kHz
- Maximum input signal variation during conversion for 8-bit accuracy and 10V full scale: $\Delta V_{IN}/\Delta T = 19.5 \text{ mV}/\mu\text{s}$

Timing Diagram

