Single Digitally-Controlled (XDCP™) Potentiometer (Push Button Controlled)

FEATURES

- **Push button controlled**
- **Low power CMOS**
  - **—Active current, 3mA max**
  - **—Standby current, 200µA max**
- **31 resistive elements**
  - **—Temperature compensated**
  - **—±20% end to end resistance range**
  - **—-5V to +5V range**
- **32 wiper tap points**
  - **—Wiper positioned via two push button inputs**
  - **—Slow & fast scan modes**
  - **—AUTOSTORE® option**
  - **—Manual store option**
  - **—Wiper position stored in nonvolatile memory and recalled on power-up**
- **100 year wiper position data retention**
- **X9511W = 10kΩ**
- **Packages**
  - **—8 Ld PDIP**
  - **—8 Ld SOIC**
- **Pb-free plus anneal available (RoHS compliant)**

DESCRIPTION

The Intersil X9511 is a push button controlled potentiometer that is ideal for push button controlled resistance trimming.

The X9511 is a resistor array composed of 31 resistive elements. Between each element and at either end are tap points accessible to the wiper element. The position of the wiper element is controlled by the $\overline{PU}$ and $\overline{PD}$ inputs. The position of the wiper can be automatically stored in E^2 memory and then be recalled upon a subsequent power-on operation.

The resolution of the X9511 is equal to the maximum resistance value divided by 31. As an example, for the X9511W (10kΩ) each tap point represents 323Ω.

All Intersil nonvolatile products are designed and tested for applications requiring extended endurance and data retention.

ORDERING INFORMATION

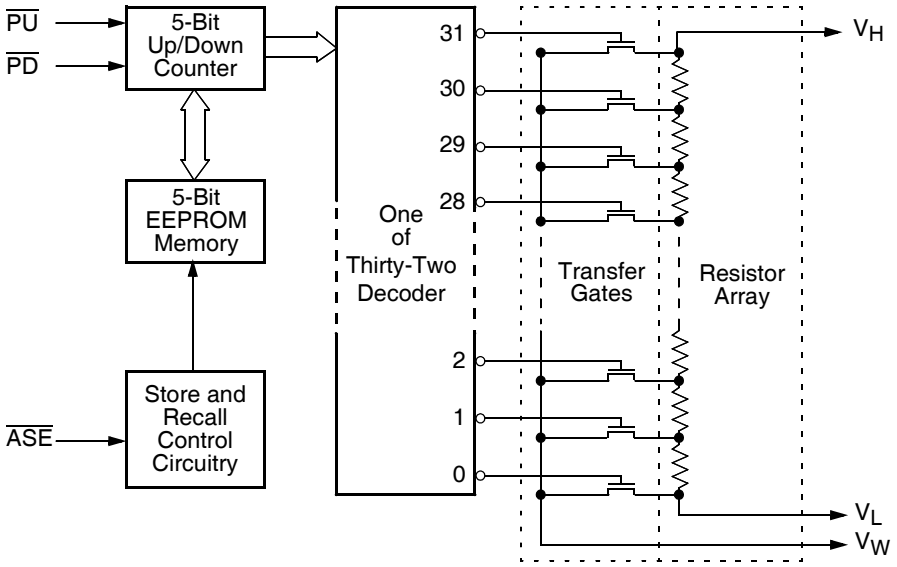
PART NUMBER	PART MARKING	R_{TOTAL} (kΩ)	TEMPERATURE RANGE (°C)	PACKAGE
X9511WP	X9511WP	10	0 to 70	8 Ld PDIP
X9511WPZ (Note)	X9511WP Z		0 to 70	8 Ld PDIP** (Pb-free)
X9511WPI	X9511WP I		-40 to 85	8 Ld PDIP
X9511WPIZ (Note)	X9511WP Z I		-40 to 85	8 Ld PDIP** (Pb-free)
X9511WS*	X9511W		0 to 70	8 Ld SOIC
X9511WSZ* (Note)	X9511W Z		0 to 70	8 Ld SOIC (Pb-free)
X9511WSI*	X9511W I		-40 to 85	8 Ld SOIC
X9511WSIZ* (Note)	X9511W Z I		-40 to 85	8 Ld SOIC (Pb-free)

NOTE: Intersil Pb-free plus anneal products employ special Pb-free material sets; molding compounds/die attach materials and 100% matte tin plate termination finish, which are RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.

*Add "T1" suffix for tape and reel.

**Pb-free PDIPs can be used for through hole wave solder processing only. They are not intended for use in Reflow solder processing applications.

BLOCK DIAGRAM



PIN DESCRIPTIONS

V_H/R_H and V_L/R_L

The high (VH/RH) and low (VL/RL) terminals of the X9511 are equivalent to the fixed terminals of a mechanical potentiometer. The minimum voltage is -5V and the maximum is +5V. It should be noted that the terminology of VL/RL and VH/RH reference the relative position of the terminal in relation to wiper movement direction selected by the PU and PD inputs, and not the voltage potential on the terminal.

$\overline{PU}$

The debounced $\overline{PU}$ input is for incrementing the wiper position. An on-chip pull-up holds the $\overline{PU}$ input HIGH. A switch closure to ground or a LOW logic level will, after a debounce time, move the wiper to the next adjacent higher tap position.

$\overline{PD}$

The debounced $\overline{PD}$ input is for decrementing the wiper position. An on-chip pull-up holds the $\overline{PD}$ input HIGH. A switch closure to ground or a LOW logic level will, after a debounce time, move the wiper to the next adjacent lower tap position.

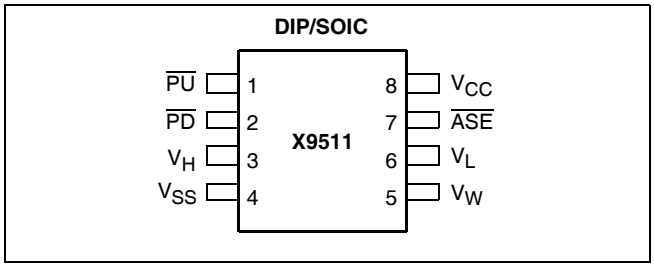
$\overline{ASE}$

The debounced $\overline{ASE}$ (AUTOSTORE enable) pin can be in one of two states:

V_{IL} - AUTOSTORE is enabled. When V_{CC} powers down, an automatic store cycle takes place.

V_{IH} - AUTOSTORE is disabled. A LOW to HIGH will initiate a manual store operation. This is for the user who wishes to connect a push button switch to this pin. For every valid push, the X9511 will store the current wiper position to the EEPROM.

PIN CONFIGURATION



PIN NAMES

Symbol	Description
V_H/R_H	High Terminal
V_W/R_W	Wiper Terminal
V_L/R_L	Low Terminal
V_{SS}	Ground
V_{CC}	Supply Voltage
$\overline{PU}$	Push Up Input
$\overline{PD}$	Push Down Input
$\overline{ASE}$	AUTOSTORE Enable Input

DEVICE OPERATION

There are three sections of the X9511: the input control, counter and decode section; the EEPROM memory; and the resistor array. The input control section operates just like an up/down counter. The output of this counter is decoded to turn on a single electronic switch, connecting a point on the resistor array to the wiper output. Under the proper conditions the contents of the counter can be stored in EEPROM memory and retained for future use. The resistor array is comprised of 31 individual resistors connected in series. At either end of the array and between each resistor is an electronic switch that transfers the potential at that point to the wiper.

The X9511 is designed to interface directly to two push button switches for effectively moving the wiper up or down. The $\overline{PU}$ and $\overline{PD}$ inputs increment or decrement a 5-bit counter respectively. The output of this counter is decoded to select one of the thirty-two wiper positions along the resistive array. The wiper increment input, $\overline{PU}$ and the wiper decrement input, $\overline{PD}$ are both connected to an internal pull-up so that they normally remain HIGH. When pulled LOW by an external push button switch or a logic LOW level input, the wiper will be switched to the next adjacent tap position.

Internal debounce circuitry prevents inadvertent switching of the wiper position if $\overline{PU}$ or $\overline{PD}$ remain LOW for less than 40ms, typical. Each of the buttons can be pushed either once for a single increment/decrement or continuously for a multiple increments/decrements. The number of increments/decrements of the wiper position depend on how long the button is being pushed. When making a continuous push, after the first second, the increment/decrement speed increases. For the first second the device will be in the slow scan mode. Then if the button is held for longer than 1 second the device will go into the fast scan mode. As soon as the button is released the X9511 will return to a standby condition.

The wiper, when at either fixed terminal, acts like its mechanical equivalent and does not move beyond the last position. That is, the counter does not wrap around when clocked to either extreme.

AUTOSTORE

The value of the counter is stored in EEPROM memory whenever the chip senses a power-down of V_{CC} while $\overline{ASE}$ is enabled (held LOW). When power is restored, the content of the memory is recalled and the counter reset to the last value stored.

If AUTOSTORE is to be implemented, $\overline{ASE}$ is typically hard wired to V_{SS}. If $\overline{ASE}$ is held HIGH during power-up and then taken LOW, the wiper will not respond to the $\overline{PU}$ or $\overline{PD}$ inputs until $\overline{ASE}$ is brought HIGH and held HIGH.

Manual (Push Button) Store

When $\overline{ASE}$ is not enabled (held HIGH) a push button switch may be used to pull $\overline{ASE}$ LOW and released to perform a manual store of the wiper position.

R_{TOTAL} with V_{CC} Removed

The end to end resistance of the array will fluctuate once V_{CC} is removed.

Figure 1. Typical circuit with $\overline{ASE}$ store pin controlled by push button switch

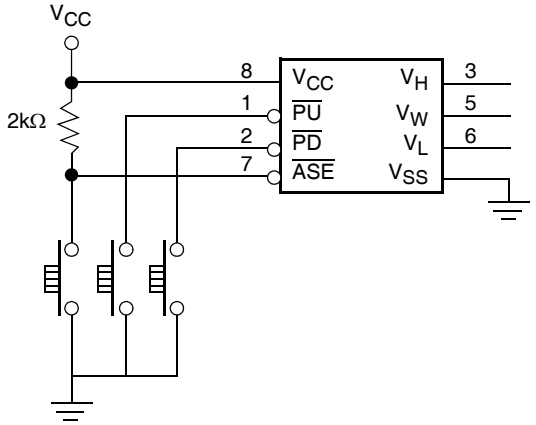
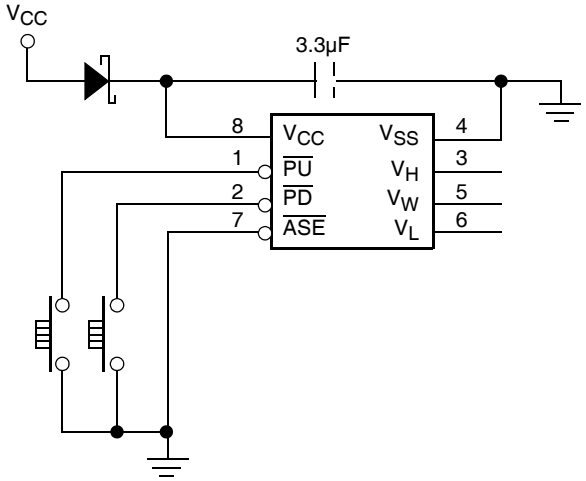


Figure 2. Typical circuit with $\overline{ASE}$ store pin used in AUTOSTORE mode



ABSOLUTE MAXIMUM RATINGS

Temperature under bias ..... -65°C to +135°C
Storage temperature ..... -65°C to +150°C
Voltage on $\overline{PU}$, $\overline{PD}$, and V_{CC}
with respect to V_{SS} ..... -1V to +7V
Voltage on V_H and V_L
referenced to V_{SS} ..... -8V to +8V
$\Delta V = |V_H - V_L|$
X9511W 10V
Lead temperature (soldering 10 seconds)..... 300°C
Wiper current ..... ±1mA

ANALOG CHARACTERISTICS

Electrical Characteristics

End-to-end resistance tolerance .....±20%
Power rating at 25°C
X9511W 10mW
Wiper current .....±1mA Max.
Typical wiper resistance ..... 40Ω at 1mA
Typical noise .....< -120dB/√Hz Ref: 1V

Resolution

Resistance 3%

Linearity

Absolute linearity(1)..... ±1.0 MI(2)
Relative linearity(3)..... ±0.2 MI(2)

COMMENT

Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only; functional operation of the device (at these or any other conditions above those listed in the operational sections of this specification) is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Temperature Coefficient

-40°C to +85°C
X9511W ..... +300 ppm/°C Typical
Ratiometric temperature coefficient .....±20 ppm

Wiper Adjustability

Unlimited wiper adjustment..... (Non-Store operation)
Wiper position store operations.....100,000 data changes

Physical Characteristics

Marking Includes
Manufacturer’s Trademark
Resistance Value or Code
Date Code

Notes: (1) Absolute linearity is utilized to determine actual wiper voltage versus expected voltage = ($V_{w(n)}$(actual) - $V_{w(n)}$(expected)) = ±1 MI Maximum.
(2) 1 MI = Minimum Increment = R_{TOT}/31.
(3) Relative linearity is a measure of the error in step size between taps = $V_{W(n+1)}$ - [$V_{w(n)}$ + MI] = +0.2 MI

SYMBOL TABLE

WAVEFORM	INPUTS	OUTPUTS
	Must be steady	Will be steady
	May change from Low to High	Will change from Low to High
	May change from High to Low	Will change from High to Low
	Don’t Care: Changes Allowed	Changing: State Not Known
	N/A	Center Line is High Impedance

RECOMMENDED OPERATING CONDITIONS

Temp	Min.	Max.
Commercial	0°C	+70°C
Industrial	-40°C	+85°C

Supply Voltage	Limits
X9511	5V ± 10%

D.C. OPERATING CHARACTERISTICS (Over recommended operating conditions unless otherwise specified.)

Symbol	Parameter	Limits			Unit	Test Conditions
		Min.	Typ.(4)	Max.		
I_{CC}	V_{CC} active current		1	3	mA	$\overline{PU}$ or $\overline{PD}$ held at V_{IL} the other at V_{IH}
I_{SB}	Standby supply current		100	200	µA	$\overline{PU} = \overline{PD} = V_{IH}$
I_{LI}	$\overline{PU}$, $\overline{PD}$, $\overline{ASE}$ input leakage current			10	µA	$V_{IN} = V_{SS}$ to V_{CC}
V_{IH}	$\overline{PU}$, $\overline{PD}$, $\overline{ASE}$ input HIGH voltage	2		V_{CC} + 1	V	
V_{IL}	$\overline{PU}$, $\overline{PD}$, $\overline{ASE}$ input LOW voltage	-1		0.8	V	
R_W	Wiper resistance		40	100	Ω	Max. Wiper Current ±1mA
V_{VH}	VH terminal voltage	-5		+5	V	
V_{VL}	VL terminal voltage	-5		+5	V	
C_{IN}(5)	$\overline{ASE}$, $\overline{PU}$, $\overline{PD}$ input capacitance			10	pF	V_{CC} = 5V, V_{IN} = 0V, T_A = 25°C, f = 1MHz

Notes: (4) Typical values are for T_A = 25°C and nominal supply voltage.
(5) This parameter is periodically sampled and not 100% tested.

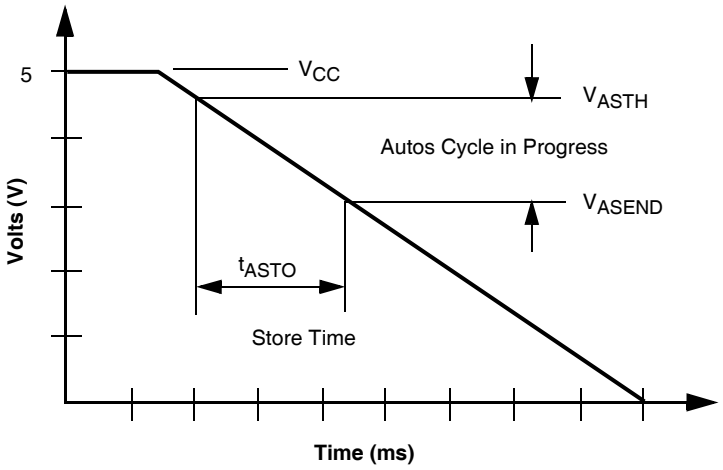
A.C. OPERATING CHARACTERISTICS (Over recommended operating conditions unless otherwise specified.)

Symbol	Parameter	Limits			Unit
		Min.	Typ.(6)	Max.	
t_{GAP}	Time between two separate push button events	0			µs
t_{DB}	Debounce time		30	60	ms
$t_{S\ SLOW}$	After debounce to wiper change on a slow mode	100	250	375	ms
$t_{S\ FAST}$(7)	Wiper change on a fast mode	25	50	75	ms
t_{PU}(7)	Power-up to wiper stable			500	µs
$t_R\ V_{CC}$(7)	V_{CC} power-up rate	0.2		50	mV/µs
t_{ASTO}(7)	AUTOSTORE cycle time	2			ms
V_{ASTH}(7)	AUTOSTORE threshold voltage		4		V
V_{ASEND}(7)	AUTOSTORE cycle end voltage		3.5		V

POWER-UPPOWER-DOWN AND DOWN REQUIREMENTS

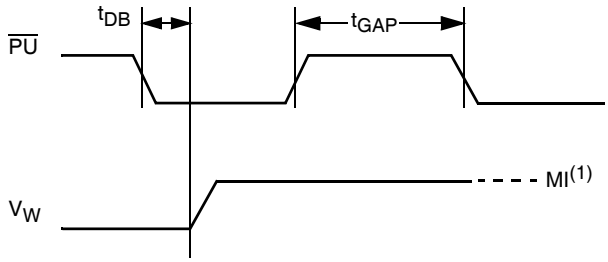
The are no restrictions on the sequencing of V_{CC} and the voltage applied to the potentiometer pins during power-up or power-down conditions. During power-up, the data sheet parameters for the DCP do not fully apply until 1ms after V_{CC} reaches its final value. The V_{CC} ramp rate spec is always in effect.

AUTOSTORE Cycle Timing Diagram



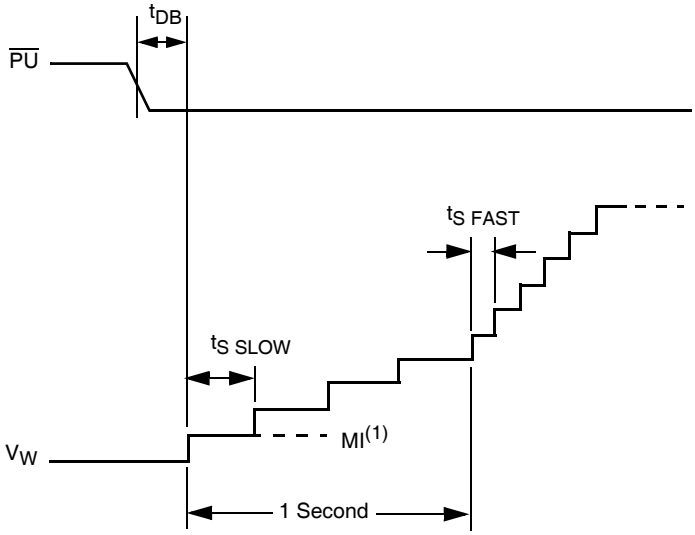
Notes: V_{ASTH} - AUTOSTORE threshold voltage
V_{ASEND} - AUTOSTORE cycle end voltage
t_{ASTO} - AUTOSTORE cycle time
(6) Typical values are for T_A = 25°C and nominal supply voltage.
(7) This parameter is periodically sampled and not 100% tested.

Slow Mode Timing



Note: (1) MI in the A.C. timing diagram refers to the minimum incremental change in the wiper voltage.

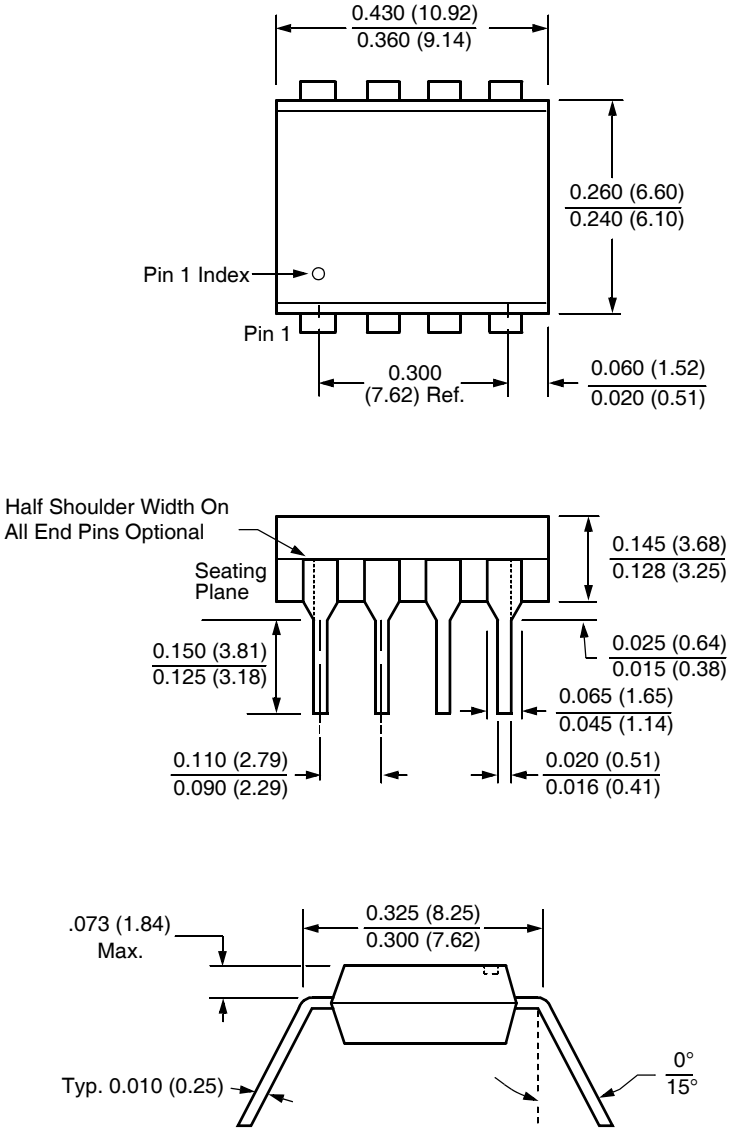
Fast Mode Timing



Note: (1) MI in the A.C. timing diagram refers to the minimum incremental change in the wiper voltage.

PACKAGING INFORMATION

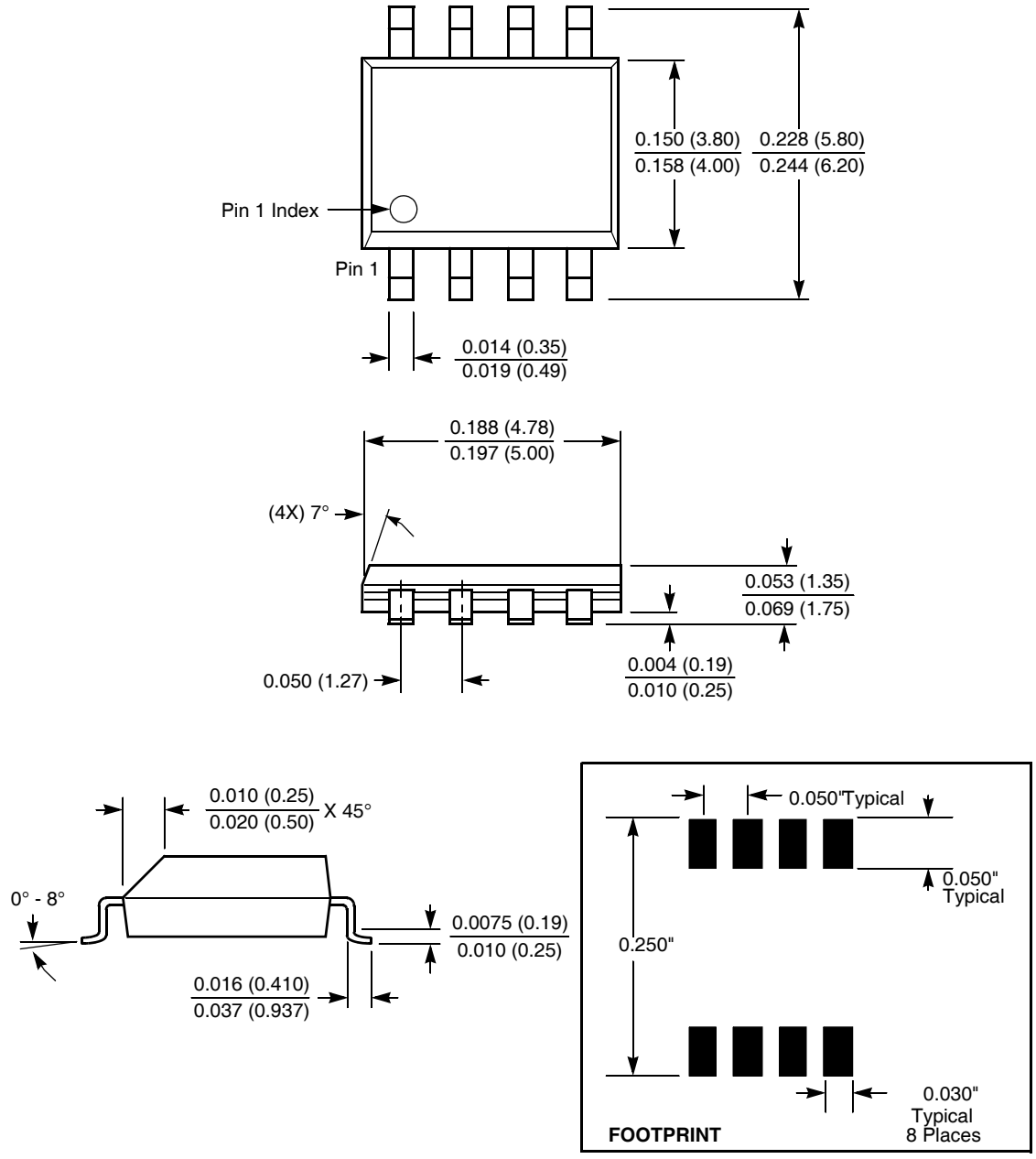
8-Lead Plastic Dual In-Line Package Type P



NOTE:
1. **ALL DIMENSIONS IN INCHES (IN PARENTHESES IN MILLIMETERS)**
2. **PACKAGE DIMENSIONS EXCLUDE MOLDING FLASH**

PACKAGING INFORMATION

8-Lead Plastic Small Outline Gull Wing Package Type S



NOTE: ALL DIMENSIONS IN INCHES (IN PARENTHESES IN MILLIMETERS)

All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems.
Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

X9511

Printer Friendly Version

Single Digitally-Controlled (XDCP™) Potentiometer (Push Button Controlled)

DS Datasheets, Related Docs & Simulations | Description | Key Features | PT Parametric Data | Related Devices

Ordering Information — RoHS/Pb-Free/Green Device

Part No.	Design-In Status	Temp.	Package	Price MSL	Price US $	
X9511WP	Active	Comm	8 Ld PDIP	N/A	1.30	Buy
X9511WPI	Active	Ind	8 Ld PDIP	N/A	1.62	Buy
X9511WPIZ	Active	Ind	8 Ld PDIP	N/A	1.62	Buy
X9511WPZ	Active	Comm	8 Ld PDIP	N/A	1.30	Buy
X9511WS	Active	Comm	8 Ld SOIC	1	1.30	Buy Sample
X9511WSC7898	Active	Comm	8 Ld SOIC	1		Buy
X9511WSC7975	Active	Comm	8 Ld SOIC	1		Buy
X9511WSI	Active	Ind	8 Ld SOIC	1	1.62	Buy
X9511WSIT1	Active	Ind	8 Ld SOIC T+R	1	1.62	Buy
X9511WSIT2	Active	Ind	8 Ld SOIC T+R	1	1.62	Buy
X9511WSIZ	Active	Ind	8 Ld SOIC	1	1.62	Buy
X9511WSIZT1	Active	Comm	8 Ld SOIC T+R	1	1.62	Buy
X9511WST1	Active	Comm	8 Ld SOIC T+R	1		Buy
X9511WST1C7975	Active	Comm	8 Ld SOIC	1		Buy
X9511WST2	Active	Comm	8 Ld SOIC T+R	1	1.36	Buy
X9511WSZ	Active	Comm	8 Ld SOIC	1	1.30	Buy Sample
X9511WSZT1	Active	Comm	8 Ld SOIC T+R	1	1.36	Buy
X9511WSIZT2	Coming Soon	Comm	8 Ld SOIC T+R	1		
X9511WSZT2	Coming Soon	Comm	8 Ld SOIC T+R	1		

The price listed is the manufacturer's suggested retail price for quantities between 100 and 999 units. However, prices in today's market are fluid and may change without notice.

MSL = Moisture Sensitivity Level - per IPC/JEDEC J-STD-020

SMD = Standard Microcircuit Drawing

Description

The Intersil X9511 is a push button controlled potentiometer that is ideal for push button controlled resistance trimming.

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The resolution of the X9511 is equal to the maximum resistance value divided by 31. As an example, for the X9511W (10kΩ) each tap point represents 323Ω.

All Intersil nonvolatile products are designed and tested for applications requiring extended endurance and data retention.

Key Features

- Push button controlled
- Low power CMOS
  - Active current, 3mA max
  - Standby current, 100µA typical
- 31 resistive elements
  - Temperature compensated
  - ±20% end to end resistance range
  - -5V to +5V range
- 32 wiper tap points
  - Wiper positioned via two push button inputs
  - Slow and fast scan modes
  - AUTOSTORE® option
  - Manual store option
  - Wiper position stored in nonvolatile memory and recalled on power-up
- 100 year wiper position data retention
- X9511W = 10kΩ
- Packages
  - 8 Ld PDIP
  - 8 Ld SOIC
- Pb-free plus anneal available (RoHS compliant)

Related Documentation

AN Application Note(s):

- A Compendium of Application Circuits for Intersil's Digitally-Controlled (XDCP) Potentiometers
- A Primer on Digitally-Controlled Potentiometers
- Application of Intersil Digitally Controlled Potentiometers (XDCP™) as Hybrid Analog/Digital Feedback System Control Elements
- DC/DC Module Trim with Digital Potentiometers
- Designing Power Supplies Using Intersil's XDCP Mixed Signal Products
- Power Supply and DC to DC Converter Control using Intersil Digitally Controlled Potentiontiometers (XDCPs)
- Putting Analog On The Bus
- Shaft Encoder Drives Multiple Intersil Digitally Controlled Potentiontiometers (XDCPs)

DS Datasheet(s):

- Single Digitally-Controlled (XDCP™) Potentiometer (Push Button Controlled)

TB Technical Brief(s):

- Converting a Fixed PWM to an Adjustable PWM

EV Evaluation Board(s):

- Evaluation Circuits for XDCP™
- Intersil_XDCP_Test_Utility_Manual_rev_3.2.3.pdf
- LabView_XDCP_Software.zip
- LabView_XDCP_Upgrade_3.2.3.zip
- Readme_XicorLabVIEW_V3.2.3.txt
- accessHW.zip

TH Technical Homepage:

- Digitally Controlled Potentiometers (DCPs) and Capacitors (DCCs)
- Precision Analog Homepage

PT **Parametric Data**

Number of DCPs	Single
Number of Taps	32
Memory Type	Non-Volatile
Bus Interface Type	Push Button
Resistance Options (kΩ)	10
V_{CC} Range (V)	4.5 to 5.5
DCP Differential Terminal Voltage (V)	10
Terminal Voltage Range V_L to V_H (V)	$-V_{CC}$ to $+V_{CC}$
Resistance Taper	Linear
Wiper Current (mA)	±1
Wiper Resistance (Ω)	40
Standby Current I_{SB} (µA)	200

Related Devices

Parametric Table

X9313	Digitally Controlled Potentiometer (XDCP™), Linear, 32 Taps, 3 Wire Interface, Terminal Voltages ± V_{CC}
X9314	Single Digitally Controlled Potentiometer (XDCP™)
X9315	Digitally Controlled Potentiometer (XDCP™)
X93154	Digitally Controlled Potentiometer (XDCP™)
X93155	Digitally Controlled Potentiometer (XDCP™)
X93156	Single Digitally Controlled Potentiometer (XDCP™), Low Noise, Low Power, 3 wire Up/Down, 32 Taps