



# FAST CMOS OCTAL BUFFER/LINE DRIVER

**IDT54/74FCT244/A/C**

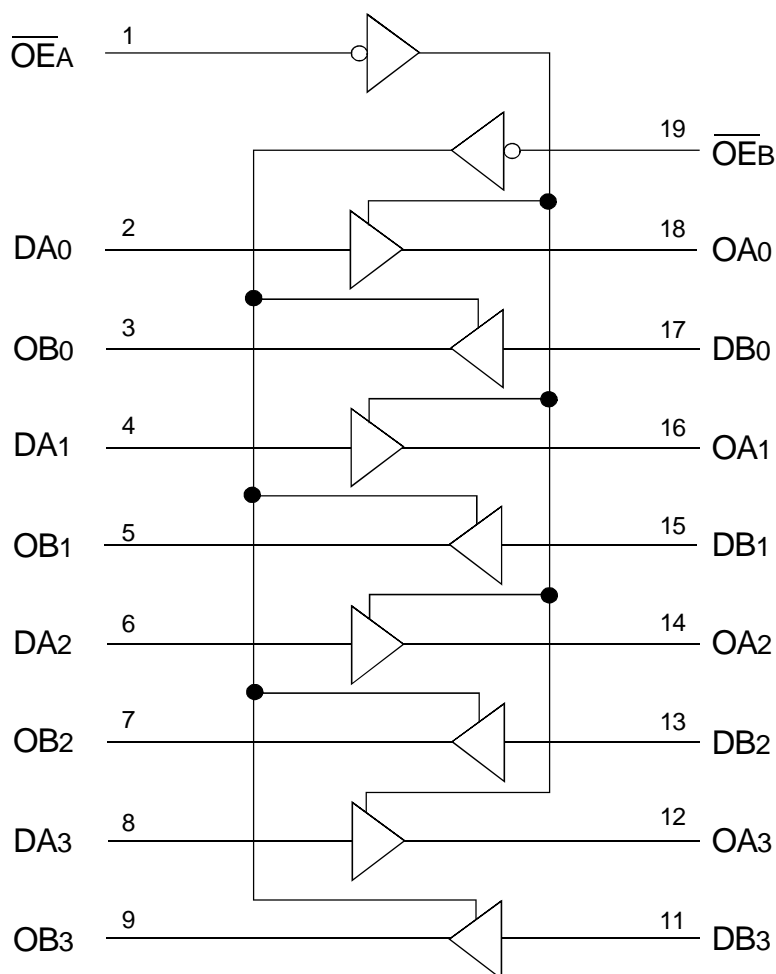
## FEATURES:

- IDT54FCT244 equivalent to FAST™ speed and drive
- IDT54/74FCT244A 25% faster than FAST
- IDT74FCT244C up to 55% faster than FAST
- I<sub>OL</sub> = 64mA (commercial) and 48mA (military)
- CMOS power levels (1mW typ. static)
- Military product compliant to MIL-STD-883, Class B
- Meets or exceeds JEDEC Standard 18 specifications
- Available in the following packages:
  - Commercial: SOIC
  - Military: CERDIP, LCC

## DESCRIPTION:

The IDT octal buffer/line drivers are built using an advanced dual metal CMOS technology. The FCT244 is designed to be employed as a memory and address driver, clock driver, and bus-oriented transmitter/receiver which provides improved board density.

## FUNCTIONAL BLOCK DIAGRAM

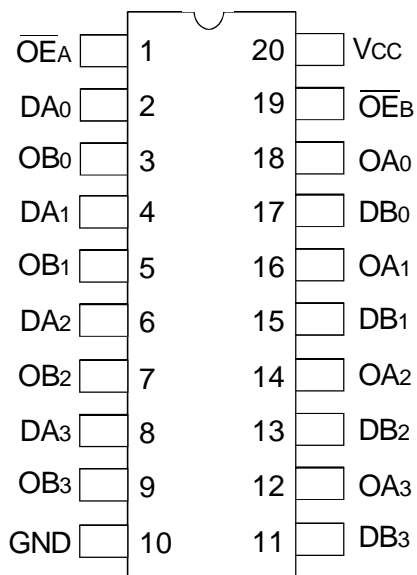


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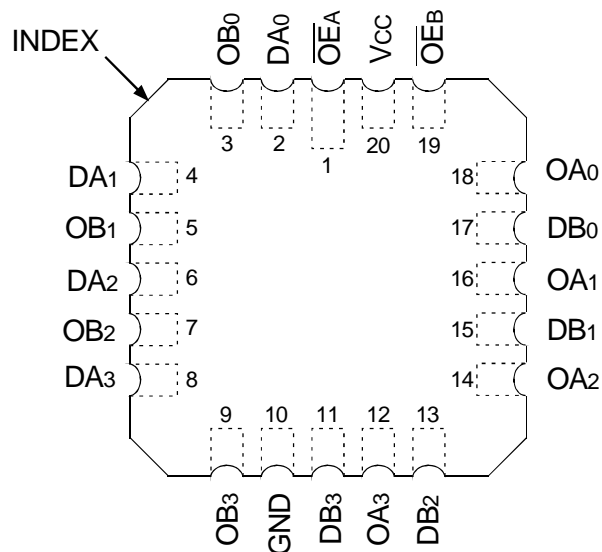
MILITARY AND COMMERCIAL TEMPERATURE RANGES

JUNE 2002

## PIN CONFIGURATION



CERDIP/ SOIC  
TOP VIEW



LCC  
TOP VIEW

## ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

| Symbol                           | Rating                               | Commercial              | Military                | Unit |
|----------------------------------|--------------------------------------|-------------------------|-------------------------|------|
| V <sub>TERM</sub> <sup>(2)</sup> | Terminal Voltage with Respect to GND | -0.5 to +7              | -0.5 to +7              | V    |
| V <sub>TERM</sub> <sup>(3)</sup> | Terminal Voltage with Respect to GND | -0.5 to V <sub>CC</sub> | -0.5 to V <sub>CC</sub> | V    |
| T <sub>A</sub>                   | Operating Temperature                | 0 to +70                | -55 to +125             | °C   |
| T <sub>BIAS</sub>                | Temperature under BIAS               | -55 to +125             | -65 to +135             | °C   |
| T <sub>STG</sub>                 | Storage Temperature                  | -55 to +125             | -65 to +150             | °C   |
| P <sub>T</sub>                   | Power Dissipation                    | 0.5                     | 0.5                     | W    |
| I <sub>OUT</sub>                 | DC Output Current                    | 120                     | 120                     | mA   |

### NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability. No terminal voltage may exceed V<sub>CC</sub> by +0.5V unless otherwise noted.
- Input and V<sub>CC</sub> terminals only.
- Output and I/O terminals only.

## CAPACITANCE (T<sub>A</sub> = +25°C, F = 1.0MHz)

| Symbol           | Parameter <sup>(1)</sup> | Conditions            | Typ. | Max. | Unit |
|------------------|--------------------------|-----------------------|------|------|------|
| C <sub>IN</sub>  | Input Capacitance        | V <sub>IN</sub> = 0V  | 6    | 10   | pF   |
| C <sub>OUT</sub> | Output Capacitance       | V <sub>OUT</sub> = 0V | 8    | 12   | pF   |

### NOTE:

- This parameter is measured at characterization but not tested.

## PIN DESCRIPTION

| Pin Names                             | Description                               |
|---------------------------------------|---|
| $\overline{OE}_A$ , $\overline{OE}_B$ | 3-State Output Enable Inputs (Active LOW) |
| D <sub>xx</sub>                       | Inputs                                    |
| O <sub>xx</sub>                       | Outputs                                   |

## FUNCTION TABLE<sup>(1)</sup>

| Inputs            |                   |   | Outputs |
|-------------------|-------------------|---|---------|
| $\overline{OE}_A$ | $\overline{OE}_B$ | D |         |
| L                 | L                 | L | L       |
| L                 | L                 | H | H       |
| H                 | H                 | X | Z       |

### NOTE:

- H = HIGH Voltage Level  
X = Don't Care  
L = LOW Voltage Level  
Z = High Impedance

## DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:  $V_{LC} = 0.2V$ ;  $V_{HC} = V_{CC} - 0.2V$

Commercial:  $T_A = 0^\circ C$  to  $+70^\circ C$ ,  $V_{CC} = 5.0V \pm 5\%$ , Military:  $T_A = -55^\circ C$  to  $+125^\circ C$ ,  $V_{CC} = 5.0V \pm 10\%$

| Symbol    | Parameter                                    | Test Conditions <sup>(1)</sup>                                       |                               | Min.     | Typ. <sup>(2)</sup> | Max.                    | Unit    |
|-----------|--|--|-------------------------------|----------|---------------------|-------------------------|---------|
| $V_{IH}$  | Input HIGH Level                             | Guaranteed Logic HIGH Level  |                               | 2        | —                   | —                       | V       |
| $V_{IL}$  | Input LOW Level                              | Guaranteed Logic LOW Level   |                               | —        | —                   | 0.8                     | V       |
| $I_{IH}$  | Input HIGH Current                           | $V_{CC} = \text{Max.}$   | $V_I = V_{CC}$                | —        | —                   | 5                       | $\mu A$ |
| $I_{IL}$  | Input LOW Current                            |  | $V_I = 2.7V$                  | —        | —                   | 5 <sup>(4)</sup>        |         |
|           |  |  | $V_I = 0.5V$                  | —        | —                   | -5 <sup>(4)</sup>       |         |
| $I_{OZH}$ | Off State (High Impedance)<br>Output Current | $V_{CC} = \text{Max.}$   | $V_O = V_{CC}$                | —        | —                   | 10                      | $\mu A$ |
|           |  |  | $V_O = 2.7V$                  | —        | —                   | 10 <sup>(4)</sup>       |         |
|           |  |  | $V_O = 0.5V$                  | —        | —                   | -10 <sup>(4)</sup>      |         |
|           |  |  | $V_O = GND$                   | —        | —                   | -10                     |         |
| $V_{IK}$  | Clamp Diode Voltage                          | $V_{CC} = \text{Min.}, I_{IN} = -18mA$                               |                               | —        | -0.7                | -1.2                    | V       |
| $I_{OS}$  | Short Circuit Current                        | $V_{CC} = \text{Max.}, V_O = GND$ <sup>(3)</sup>                     |                               | -60      | -120                | —                       | mA      |
| $V_{OH}$  | Output HIGH Voltage                          | $V_{CC} = 3V, V_{IN} = V_{LC} \text{ or } V_{HC}, I_{OH} = -32\mu A$ |                               | $V_{HC}$ | $V_{CC}$            | —                       | V       |
|           |  | $V_{CC} = \text{Min}$<br>$V_{IN} = V_{IH} \text{ or } V_{IL}$        | $I_{OH} = -300\mu A$          | $V_{HC}$ | $V_{CC}$            | —                       |         |
|           |  |  | $I_{OH} = -12mA \text{ MIL}$  | 2.4      | 4.3                 | —                       |         |
| $V_{OL}$  | Output LOW Voltage                           | $V_{CC} = 3V, V_{IN} = V_{LC} \text{ or } V_{HC}, I_{OL} = 300\mu A$ |                               | —        | GND                 | $V_{LC}$                | V       |
|           |  | $V_{CC} = \text{Min}$<br>$V_{IN} = V_{IH} \text{ or } V_{IL}$        | $I_{OL} = 300\mu A$           | —        | GND                 | $V_{LC}$ <sup>(4)</sup> |         |
|           |  |  | $I_{OL} = 48mA \text{ MIL}$   | —        | 0.3                 | 0.55                    |         |
|           |  |  | $I_{OL} = 64mA \text{ COM'L}$ | —        | 0.3                 | 0.55                    |         |

### NOTES:

1. For conditions shown as Min. or Max., use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at  $V_{CC} = 5.0V$ ,  $+25^\circ C$  ambient and maximum loading.
3. Not more than one output should be tested at one time. Duration of the test should not exceed one second.
4. This parameter is guaranteed but not tested.

## POWER SUPPLY CHARACTERISTICS

$V_{LC} = 0.2V$ ;  $V_{HC} = V_{CC} - 0.2V$

| Symbol           | Parameter   | Test Conditions <sup>(1)</sup>  |   | Min. | Typ. <sup>(2)</sup> | Max.                | Unit       |
|------------------|---|---|---|------|---------------------|---------------------|------------|
| I <sub>CC</sub>  | Quiescent Power Supply Current                    | V <sub>CC</sub> = Max.<br>V <sub>IN</sub> ≥ V <sub>HC</sub> ; V <sub>IN</sub> ≤ V <sub>LC</sub>   |   | —    | 0.2                 | 1.5                 | mA         |
| ΔI <sub>CC</sub> | Quiescent Power Supply Current<br>TTL Inputs HIGH | V <sub>CC</sub> = Max.<br>V <sub>IN</sub> = 3.4V <sup>(3)</sup>   |   | —    | 0.5                 | 2                   | mA         |
| I <sub>CCD</sub> | Dynamic Power Supply<br>Current <sup>(4)</sup>    | V <sub>CC</sub> = Max.<br>Outputs Open<br>OE <sub>A</sub> = OE <sub>B</sub> = GND<br>One Input Toggling<br>50% Duty Cycle                             | V <sub>IN</sub> ≥ V <sub>HC</sub><br>V <sub>IN</sub> ≤ V <sub>LC</sub>          | —    | 0.15                | 0.25                | mA/<br>MHz |
| I <sub>C</sub>   | Total Power Supply Current <sup>(6)</sup>         | V <sub>CC</sub> = Max.<br>Outputs Open<br>f <sub>i</sub> = 10MHz<br>50% Duty Cycle<br>OE <sub>A</sub> = OE <sub>B</sub> = GND<br>One Bit Toggling     | V <sub>IN</sub> ≥ V <sub>HC</sub><br>V <sub>IN</sub> ≤ V <sub>LC</sub><br>(FCT) | —    | 1.7                 | 4                   | mA         |
|                  |   |   | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND                                 | —    | 2                   | 5                   |            |
|                  |   | V <sub>CC</sub> = Max.<br>Outputs Open<br>f <sub>i</sub> = 2.5MHz<br>50% Duty Cycle<br>OE <sub>A</sub> = OE <sub>B</sub> = GND<br>Eight Bits Toggling | V <sub>IN</sub> ≥ V <sub>HC</sub><br>V <sub>IN</sub> ≤ V <sub>LC</sub><br>(FCT) | —    | 3.2                 | 6.5 <sup>(5)</sup>  |            |
|                  |   |   | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND                                 | —    | 5.2                 | 14.5 <sup>(5)</sup> |            |

### NOTES:

1. For conditions shown as Min. or Max., use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient.

3. Per TTL driven input (V<sub>IN</sub> = 3.4V). All other inputs at V<sub>CC</sub> or GND.

4. This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.

5. Values for these conditions are examples of ΔI<sub>CC</sub> formula. These limits are guaranteed but not tested.

6. I<sub>C</sub> = I<sub>QUIESCENT</sub> + I<sub>INPUTS</sub> + I<sub>DYNAMIC</sub>

$$I_C = I_{CC} + \Delta I_{CC} \text{ DHNT} + I_{CCD} (f_{CP}/2 + f_i N_i)$$

I<sub>CC</sub> = Quiescent Current

ΔI<sub>CC</sub> = Power Supply Current for a TTL High Input (V<sub>IN</sub> = 3.4V)

DH = Duty Cycle for TTL Inputs High

NT = Number of TTL Inputs at DH

I<sub>CCD</sub> = Dynamic Current caused by an Input Transition Pair (HLH or LHL)

f<sub>CP</sub> = Clock Frequency for register devices (zero for non-register devices)

f<sub>i</sub> = Input Frequency

N<sub>i</sub> = Number of Inputs at f<sub>i</sub>

All currents are in milliamperes and all frequencies are in megahertz.

## SWITCHING CHARACTERISTICS OVER OPERATING RANGE<sup>(1,2)</sup>

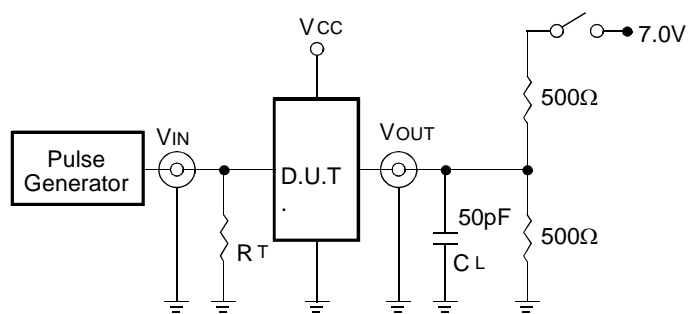
| Symbol           | Parameter           | Condition                                      | 54FCT244 |      | 54/74FCT244A |      |      |      | 74FCT244C |      | Unit |
|------------------|---------------------|--|----------|------|--------------|------|------|------|-----------|------|------|
|                  |                     |  | Mil.     |      | Com'l.       |      | Mil. |      | Com'l.    |      |      |
|                  |                     |  | Min.     | Max. | Min.         | Max. | Min. | Max. | Min.      | Max. |      |
| t <sub>PLH</sub> | Propagation Delay   | C <sub>L</sub> = 50pF<br>R <sub>L</sub> = 500Ω | 1.5      | 7    | 1.5          | 4.8  | 1.5  | 5.1  | 1.5       | 4.1  | ns   |
| t <sub>PHL</sub> | Dx to Ox            |  |          |      |              |      |      |      |           |      |      |
| t <sub>PZH</sub> | Output Enable Time  |  | 1.5      | 8.5  | 1.5          | 6.2  | 1.5  | 6.5  | 1.5       | 5.8  |      |
| t <sub>PZL</sub> | Output Disable Time |  | 1.5      | 7.5  | 1.5          | 5.6  | 1.5  | 5.9  | 1.5       | 5.2  |      |

### NOTES:

1. See test circuit and waveforms.

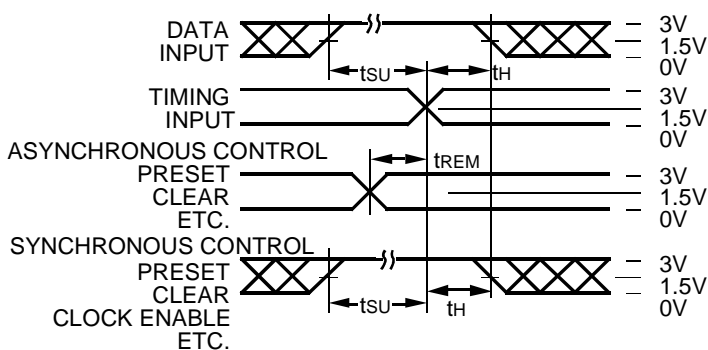
2. Minimum limits are guaranteed but not tested on Propagation Delays.

## TEST CIRCUITS AND WAVEFORMS



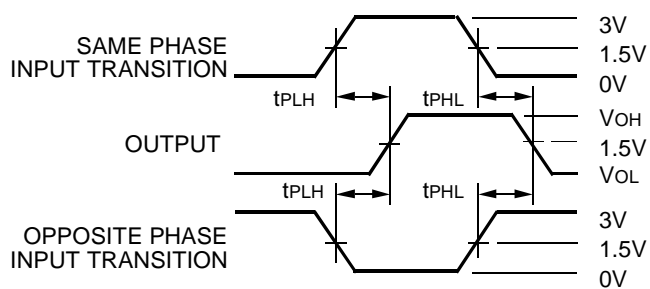
Octal Link

Test Circuits for All Outputs



Octal Link

Set-Up, Hold, and Release Times



Octal Link

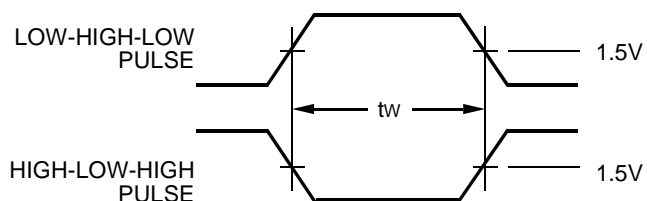
Propagation Delay

## SWITCH POSITION

| Test                                    | Switch |
|---|--------|
| Open Drain<br>Disable Low<br>Enable Low | Closed |
| All Other Tests                         | Open   |

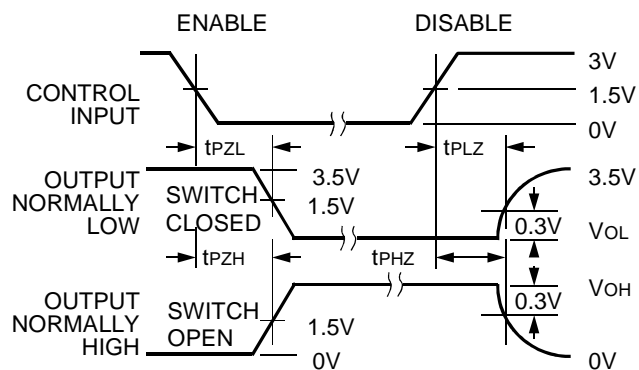
### DEFINITIONS:

CL = Load capacitance: includes jig and probe capacitance.  
RT = Termination resistance: should be equal to ZOUT of the Pulse Generator.



Pulse Width

Octal Link



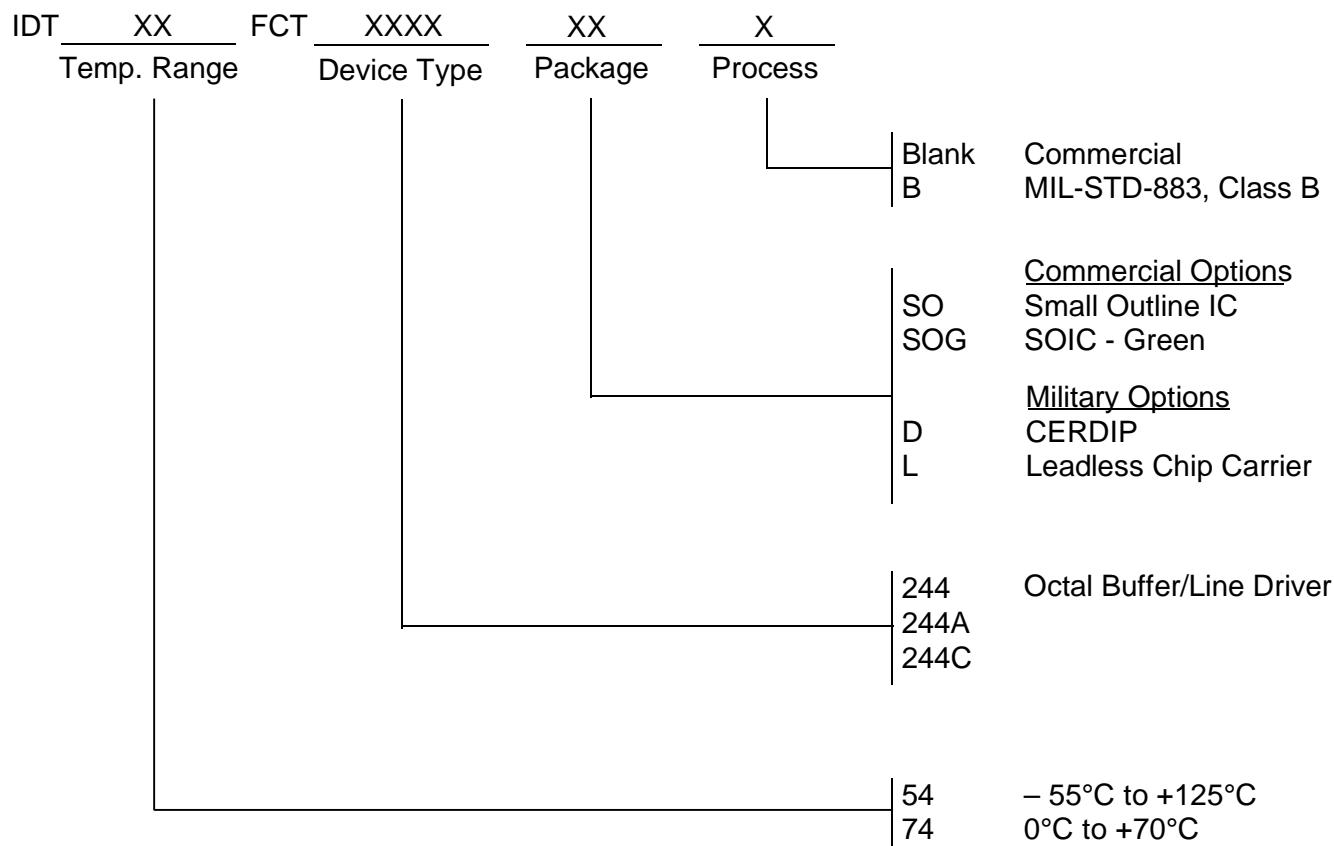
Octal Link

Enable and Disable Times

### NOTES:

1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.
2. Pulse Generator for All Pulses: Rate  $\leq 1.0\text{MHz}$ ;  $Z_o \leq 50\Omega$ ;  $t_r \leq 2.5\text{ns}$ ;  $t_f \leq 2.5\text{ns}$ .

ORDERING INFORMATION



DATA SHEET DOCUMENT HISTORY

6/21/2002 Updated according to PDNs Logic-00-07 and Logic-01-04



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