



# High Speed CMOS 16-Bit Registers (3-State)

QS74FCT16374T  
QS74FCT162374T

## FEATURES/BENEFITS

- Pin and function compatible with T.I. Widebus™ and IDT Double-Density™ families
- CMOS power levels: <math><1 \mu\text{W}</math> typical standby
- SSOP (PV) and TSSOP (PA) packages
- Low output skew: 0.5 ns  $t_{SK(O)}$
- Flow-through pinout for easy layout
- Power off disable allows hot plugging
- Extended commercial temperature:  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$
- Input hysteresis for noise immunity
- Multiple power and ground pins for low noise
- Std., A, and C speed grades: 5.2 ns  $t_{PD}$  for C

### FCT16374T

- High drive standard FCT-T outputs:  $I_{OL} = +64 \text{ mA}$ ,  $I_{OH} = -32 \text{ mA}$
- Incident switching for driving buses and large loads

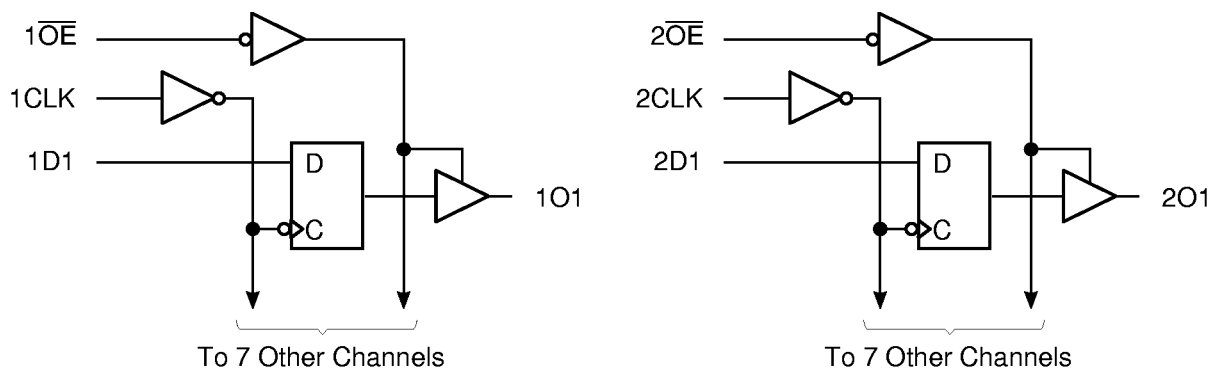
### FCT162374T

- Balanced output drivers:  $\pm 24 \text{ mA}$
- Reduced switching noise for point to point signals

## DESCRIPTION

The FCT16374 family of products are 16-bit buffered registers with three-state outputs that are ideal for driving address and data buses. The output enable ( $x\overline{OE}$ ) and clock ( $x\text{CLK}$ ) controls are organized to operate each device as two 8-bit registers, or one 16-bit register with common clock. Easy board layout is facilitated by the use of flow-through pinouts and byte enable controls provide architectural flexibility for systems designers. All outputs have ground bounce suppression circuitry (see QSI Application Note AN-01) and many power and ground pins provide low ground bounce. To accommodate hot-plug or live insertion applications, both versions of this product were designed not to load an active bus when  $V_{CC}$  is removed. In applications where bus signals are point-to-point or driving light capacitance loads, the balanced drive FCT162374 is recommended.

## FUNCTIONAL BLOCK DIAGRAM



## QS74FCT16374T, QS74FCT162374T ADVANCE INFORMATION

### PIN CONFIGURATION (All Pins Top View)

#### SSOP, TSSOP

<p>1<math>\overline{OE}</math> □ 1</p> <p>1O1 □ 2</p> <p>1O2 □ 3</p> <p>GND □ 4</p> <p>1O3 □ 5</p> <p>1O4 □ 6</p> <p>VCC □ 7</p> <p>1O5 □ 8</p> <p>1O6 □ 9</p> <p>GND □ 10</p> <p>1O7 □ 11</p> <p>1O8 □ 12</p> <p>2O1 □ 13</p> <p>2O2 □ 14</p> <p>GND □ 15</p> <p>2O3 □ 16</p> <p>2O4 □ 17</p> <p>VCC □ 18</p> <p>2O5 □ 19</p> <p>2O6 □ 20</p> <p>GND □ 21</p> <p>2O7 □ 22</p> <p>2O8 □ 23</p> <p>2<math>\overline{OE}</math> □ 24</p>	<p>48 □ 1CLK</p> <p>47 □ 1D1</p> <p>46 □ 1D2</p> <p>45 □ GND</p> <p>44 □ 1D3</p> <p>43 □ 1D4</p> <p>42 □ VCC</p> <p>41 □ 1D5</p> <p>40 □ 1D6</p> <p>39 □ GND</p> <p>38 □ 1D7</p> <p>37 □ 1D8</p> <p>36 □ 2D1</p> <p>35 □ 2D2</p> <p>34 □ GND</p> <p>33 □ 2D3</p> <p>32 □ 2D4</p> <p>31 □ VCC</p> <p>30 □ 2D5</p> <p>29 □ 2D6</p> <p>28 □ GND</p> <p>27 □ 2D7</p> <p>26 □ 2D8</p> <p>25 □ 2CLK</p>
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### PIN DESCRIPTION

Name	I/O	Description
xDx	I	Data Inputs
xOx	O	Data Outputs
xCLK	I	Clock Input
x $\overline{OE}$	I	Output Enable

### FUNCTION TABLE

$x\overline{OE}$	Inputs xCLK	xDx	Internal Q Value	Outputs xOx	Function
H	X	X	X	Hi-Z	Disable Outputs
L	↑	L	L	L	Load Input Data
L	↑	H	H	H	Enable Outputs
H	↑	L	L	Hi-Z	Load Input Data
H	↑	H	H	Hi-Z	Disable Outputs

### CAPACITANCE

$T_A = 25^\circ\text{C}$ ,  $f = 1\text{ MHz}$ ,  $V_{IN} = 0\text{V}$ ,  $V_{OUT} = 0\text{V}$

Pins	Typ	Max	Unit
All	6.0	9.0	pF

**Note:** Capacitance is characterized but not tested.

**ABSOLUTE MAXIMUM RATINGS**

Supply Voltage to Ground .....	-0.5V to +7.0V
DC Output Voltage $V_{OUT}$ .....	-0.5V to +7.0V
DC Input Voltage $V_{IN}$ .....	-0.5V to +7.0V
AC Input Voltage (for a pulse width $\leq 20$ ns) .....	-3.0V
DC Input Diode Current with $V_{IN} < 0$ .....	-20 mA
DC Output Diode Current with $V_{OUT} < 0$ .....	-50 mA
DC Output Current Max. Sink Current/Pin .....	120 mA
Maximum Power Dissipation .....	1.0 watts
$T_{STG}$ Storage Temperature .....	-65° to +150°C

**Note:** Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to this device resulting in functional or reliability type failures.

**RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Min	Max	Unit
$V_{CC}$	Supply Voltage	4.5	5.5	V
$V_{IN}$	Input Voltage	-0.5	5.5	V
$V_{OUT}$	Voltage Applied to Output or I/O	0	$V_{CC}$	V
$\Delta t/\Delta v$	Input Transition Slew Rate	—	10	ns/V
$T_A$	Operating Free Air Temperature	-40	+85	°C

**DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE**

Recommended Operating Ranges apply unless otherwise noted.

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Min	Typ <sup>(2)</sup>	Max	Unit
$V_{IH}$	Input HIGH Voltage	Logic HIGH for All Inputs	2.0	—	—	V
$V_{IL}$	Input LOW Voltage	Logic LOW for All Inputs	—	—	0.8	V
$\Delta V_T$	Input Hysteresis	$V_{TLH} - V_{THL}$ for All Inputs <sup>(4)</sup>	—	100	—	mV
$ I_{IH} $ $ I_{IL} $	Input Current Input HIGH or LOW	$V_{CC} = \text{Max.}, 0 \leq V_{IN} < V_{CC}$	—	—	1	$\mu\text{A}$
$ I_{OZ} $	Off-State Output Current (Hi-Z)	$V_{CC} = \text{Max.}, 0 \leq V_{OUT} \leq V_{CC}$	—	—	1	$\mu\text{A}$
$I_{OS}$	Short Circuit Current	$V_{CC} = \text{Max.}, V_{OUT} = \text{GND}^{(3,4)}$	-80	-140	-225	mA
$V_{IK}$	Input Clamp Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18 \text{ mA}$	—	-0.7	-1.2	V

**Notes:**

1. For conditions shown as Max or Min use appropriate value specified under Recommended Operating Conditions for the applicable device type.
2. Typical values indicate  $V_{CC} = 5.0\text{V}$  and  $T_A = 25^\circ\text{C}$ .
3. Not more than one output should be tested at one time. Duration of test should not exceed one second.
4. These parameters are guaranteed by design but not tested.

**QS74FCT16374T, QS74FCT162374T ADVANCE INFORMATION**

**OUTPUT DRIVE CHARACTERISTICS FOR FCT16374T**

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Min	Typ <sup>(2)</sup>	Max	Unit	
I <sub>O</sub>	Output Drive Current	V <sub>CC</sub> = Max, V <sub>OUT</sub> = 2.5V <sup>(3)</sup>	-50	—	-180	mA	
V <sub>OH</sub>	Output HIGH Voltage	V <sub>CC</sub> = Min.	I <sub>OH</sub> = -3 mA	2.5	—	—	V
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -15 mA	2.4	—	—	V
			I <sub>OH</sub> = -32 mA <sup>(4)</sup>	2.0	—	—	V
V <sub>OL</sub>	Output LOW Voltage	V <sub>CC</sub> = Min. V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 64 mA	—	0.2	0.55	V
I <sub>OFF</sub>	Input/Output Power Off Leakage	V <sub>CC</sub> = 0V, V <sub>IN</sub> or V <sub>OUT</sub> ≤ 4.5V	—	—	±1.0	μA	

**OUTPUT DRIVE CHARACTERISTICS FOR FCT162374T**

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Min	Typ <sup>(2)</sup>	Max	Unit	
I <sub>ODL</sub>	Output LOW Current	V <sub>CC</sub> = 5V, V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = 1.5V <sup>(3)</sup>	60	115	150	mA	
I <sub>ODH</sub>	Output HIGH Current	V <sub>CC</sub> = 5V, V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = 1.5V <sup>(3)</sup>	-60	-115	-150	mA	
V <sub>OH</sub>	Output HIGH Voltage	V <sub>CC</sub> = Min. V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -24 mA	2.4	3.3	—	V
V <sub>OL</sub>	Output LOW Voltage	V <sub>CC</sub> = Min. V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 24 mA	—	0.3	0.55	V

**Notes:**

1. For conditions shown as Max or Min use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values indicate V<sub>CC</sub> = 5.0V and T<sub>A</sub> = 25°C.
3. Not more than one output should be shorted and the duration is ≤1 second.
4. Duration of the condition should not exceed one second.

## QS74FCT16374T, QS74FCT162374T ADVANCE INFORMATION

### POWER SUPPLY CHARACTERISTICS - FCT16374T, FCT162374T

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Typ <sup>(2)</sup>	Max	Unit	
I <sub>CCQ</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = Max., Freq = 0 V <sub>IN</sub> = GND or V <sub>CC</sub>	5	500	μA	
ΔI <sub>CC</sub>	Supply Current per Input @ TTL HIGH	V <sub>CC</sub> = Max., V <sub>IN</sub> = 3.4V <sup>(3)</sup>	0.5	1.5	mA	
Q <sub>CCD</sub>	Supply Current per Input per MHz <sup>(4)</sup>	V <sub>CC</sub> = Max., Outputs Open One Bit Toggling @ 50% Duty Cycle x $\overline{OE}$ = GND	60	100	μA/ MHz	
I <sub>C</sub>	Total Power Supply Current <sup>(6)</sup>	V <sub>CC</sub> = Max., Outputs Open One Bit Toggling @ 50% Duty Cycle x $\overline{OE}$ = GND, f <sub>i</sub> = 10 MHz	V <sub>IN</sub> = V <sub>CC</sub>	0.6	1.5	mA
			V <sub>IN</sub> = GND			
		V <sub>CC</sub> = Max., Outputs Open Sixteen Bits Toggling @ 50% Duty Cycle x $\overline{OE}$ = GND, f <sub>i</sub> = 2.5 MHz	V <sub>IN</sub> = 3.4V	0.9	2.3	mA
			V <sub>IN</sub> = GND			
		V <sub>IN</sub> = V <sub>CC</sub>	2.4	4.5 <sup>(5)</sup>	mA	
		V <sub>IN</sub> = GND				
		V <sub>IN</sub> = 3.4V	6.4	16.5 <sup>(5)</sup>	mA	
		V <sub>IN</sub> = GND				

**Notes:**

- For conditions shown as Min. or Max., use the appropriate values specified under Recommended Operating Conditions for applicable device type.
- Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient.
- Per TTL driven input (V<sub>IN</sub> = 3.4V). All Other Inputs at V<sub>CC</sub> or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I<sub>CC</sub> formula. These limits are guaranteed by design but not tested.
- I<sub>C</sub> = I<sub>QUIESCENT</sub> + I<sub>INPUTS</sub> = I<sub>DYNAMIC</sub>.  
 $I_C = I_{CCQ} + \Delta I_{CC} D_H N_T + I_{CCD} (f_{CP} N_{CP} / 2 + f_i N_i)$ .  
I<sub>CCQ</sub> = Quiescent Current (I<sub>CCL</sub>, I<sub>CCCH</sub>, and I<sub>CCZ</sub>).  
ΔI<sub>CC</sub> = Power Supply Current for a TTL-High Input (V<sub>IN</sub> = 3.4V).  
D<sub>H</sub> = Duty Cycle for TTL High Inputs.  
N<sub>T</sub> = Number of TTL High Inputs.  
I<sub>CCD</sub> = Dynamic Current Caused by an Input Transition Pair (HLH or LHL).  
f<sub>CP</sub> = Clock Frequency.  
N<sub>CP</sub> = Number of Clock Inputs at f<sub>CP</sub>.  
f<sub>i</sub> = Input Frequency.  
N<sub>i</sub> = Number of Inputs at f<sub>i</sub>.

## QS74FCT16374T, QS74FCT162374T ADVANCE INFORMATION

### SWITCHING CHARACTERISTICS OVER OPERATING RANGE

Recommended Operating Ranges apply unless otherwise specified.

$C_{LOAD} = 50 \text{ pF}$ ,  $R_{LOAD} = 500\Omega$  unless otherwise noted.

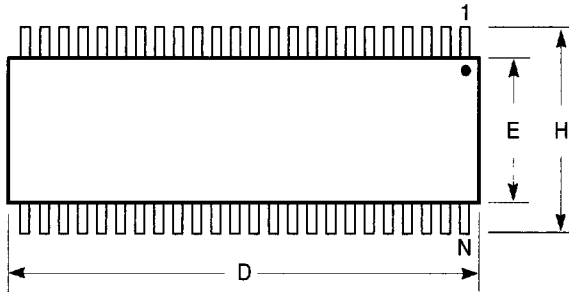
Symbol	Description <sup>(1)</sup>	FCT16374T FCT162374T		FCT16374AT FCT162374AT		FCT16374CT FCT162374CT		Unit
		Min	Max	Min	Max	Min	Max	
t <sub>PHL</sub> t <sub>PLH</sub>	Propagation Delay xCLK to xOx	2.0	10	2.0	6.5	2.0	5.2	ns
t <sub>PZH</sub> t <sub>PZL</sub>	Output Enable Time x $\overline{OE}$ to xYx	1.5	12.5	1.5	6.5	1.5	5.5	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Output Disable Time <sup>(2)</sup> x $\overline{OE}$ to xYx	1.5	8	1.5	5.5	1.5	5	ns
t <sub>s</sub>	Data Setup Time xDx to xCLK	2.0	—	2.0	—	2.0	—	ns
t <sub>H</sub>	Data Hold Time xDx to xCLK	1.5	—	1.5	—	1.5	—	ns
t <sub>w</sub>	Clock Pulse Width HIGH or LOW	7.0	—	5.0	—	5.0	—	ns
t <sub>sk(o)</sub>	Output Skew <sup>(3)</sup>	—	0.5	—	0.5	—	0.5	ns

Notes:

1. Minimums guaranteed but not tested on propagation delays. See Test Circuit and Waveforms.
2. Guaranteed by design, but not tested.
3. Skew between any two outputs of the same package switching in the same direction.  
This parameter is guaranteed by design but not tested.

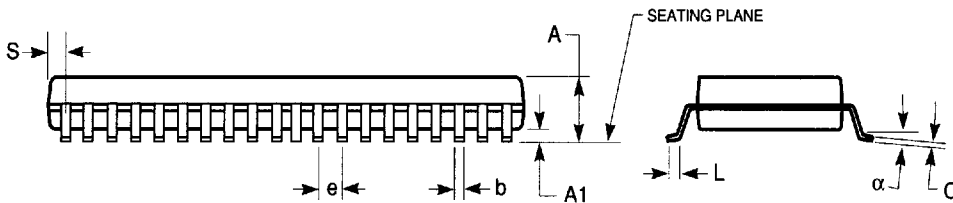
**300-MIL SSOP - Package Code PV**

**Shrink Small Outline Package  
Plastic Small Outline Gull-Wing**



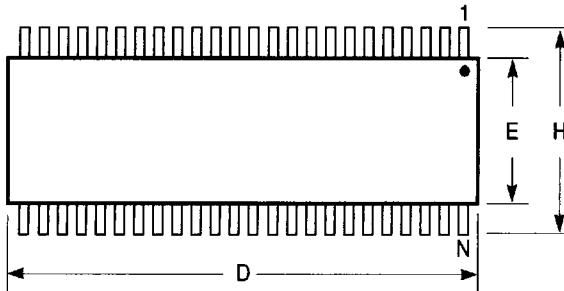
**Notes:**

1. Refer to applicable symbol list.
2. All dimensions are in inches.
3. N is the number of lead positions.
4. Dimensions D and E are to be measured at maximum material condition but do not include mold flash. Allowable mold flash is 0.006in. per side.
5. Lead coplanarity is 0.004in. maximum.



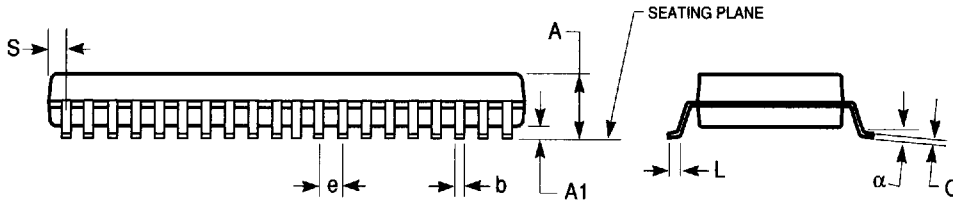
JEDEC#	MO-118AA			MO-118AB		
DWG#	PSS-48B			PSS-56B		
Symbol	Min	Nom	Max	Min	Nom	Max
A	0.095	0.102	0.110	0.095	0.102	0.110
A1	0.008	0.012	0.016	0.008	0.012	0.016
b	0.008	0.010	0.0135	0.008	0.010	0.0135
C	0.005	0.008	0.010	0.005	0.008	0.010
D	0.620	0.625	0.630	0.720	0.725	0.730
E	0.291	0.295	0.299	0.291	0.295	0.299
e	0.025 BSC			0.025 BSC		
H	0.395	0.410	0.420	0.395	0.410	0.420
L	0.020	0.030	0.040	0.020	0.030	0.040
N	48			56		
alpha	0°	5°	8°	0°	5°	8°
S	0.022	0.025	0.028	0.022	0.025	0.028

**240-MIL TSSOP - Package Code PA**  
Thin Shrink Small Outline Package  
Plastic Small Outline Gull-Wing



**Notes:**

1. Refer to applicable symbol list.
2. N is the number of lead positions.
3. Dimensions D, E, and S are to be measured at maximum material condition but do not include mold flash. Allowable mold flash is 0.006 in. per side.
4. Lead coplanarity is 0.004in. maximum.



JEDEC#	MO-153ED			MO-153EE			MO-153ED			MO-153EE		
DWG#	PSS-48C			PSS-56C			PSS-48C			PSS-56C		
Symbol	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max
A	0.039	0.043	0.047	0.039	0.043	0.047	1.00	1.10	1.20	1.00	1.10	1.20
A1	0.002	0.004	0.006	0.002	0.004	0.006	0.05	0.10	0.15	0.05	0.10	0.15
b	0.006	0.008	0.011	0.006	0.008	0.011	0.17	0.20	0.27	0.17	0.20	0.27
C	0.004	0.006	0.008	0.004	0.006	0.008	0.09	0.15	0.20	0.09	0.15	0.20
D	0.488	0.492	0.496	0.547	0.551	0.555	12.40	12.50	12.60	13.90	14.00	14.10
E	0.236	0.240	0.244	0.236	0.240	0.244	6.00	6.10	6.20	6.00	6.10	6.20
e	0.0197 BSC			0.0197 BSC			0.50 BSC			0.50 BSC		
H	0.315	0.319	0.323	0.315	0.319	0.323	8.00	8.10	8.20	8.00	8.10	8.20
L	0.018	0.024	0.030	0.018	0.024	0.030	0.45	0.60	0.75	0.45	0.60	0.75
N	48			56			48			56		
$\alpha$	0°	5°	8°	0°	5°	8°	0°	5°	8°	0°	5°	8°
S	0.015	0.020	0.025	0.006	0.010	0.014	0.38	0.50	0.65	0.15	0.25	0.35

DIMENSIONS IN INCHES

DIMENSIONS IN MILLIMETERS

7466803 0003757 T&T

QUALITY SEMICONDUCTOR, INC.