

# enhancement-type n-channel MOSFET designed for . . .

- General Purpose Amplifiers
- Analog Switches
- Digital Switching

## Performance Curves MBN See Section 4

### BENEFITS

- Low Insertion Loss  
 $R_{DS(on)} = 100 \Omega$  Maximum
- Rugged  
Zener Diode Input Protection

### ABSOLUTE MAXIMUM RATINGS (25°C)

Drain-to-Source Voltage .....	30 V
Gate-to-Source Voltage.....	30 V
Gate-to-Drain Voltage .....	30 V
Drain Current .....	50 mA
Gate Zener Current .....	$\pm 0.1$ mA
Storage Temperature .....	-65 to 150°C
Operating Junction Temperature .....	-55 to +125°C
Total Device Dissipation (Derate 2.25 mW/°C to 125°C) .....	225 mW

TO-72  
See Section 6



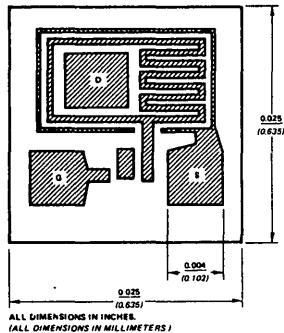
### ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)

M116

2

Characteristic		M116		Unit	Test Condition
		Min	Max		
S T A T I C 8 9 10 11 12 13	I <sub>GSS</sub>	Gate-Body Leakage	100	pA	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = V <sub>BS</sub> = 0
	V <sub>GS(th)</sub>	Gate Threshold Voltage	1		V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 10 μA, V <sub>BS</sub> = 0
	BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	30		I <sub>D</sub> = 1 μA, V <sub>GS</sub> = V <sub>BS</sub> = 0
	BV <sub>SDS</sub>	Source-Drain Breakdown Voltage	30		I <sub>S</sub> = 1 μA, V <sub>GD</sub> = V <sub>BD</sub> = 0
	BV <sub>GBS</sub>	Gate-Body Breakdown Voltage	30		I <sub>G</sub> = 10 μA, V <sub>SB</sub> = V <sub>DB</sub> = 0
	I <sub>D(off)</sub>	Drain Cutoff Current	10		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = V <sub>BS</sub> = 0
	I <sub>S(off)</sub>	Source Cutoff Current	10		V <sub>SD</sub> = 20 V, V <sub>GD</sub> = V <sub>BD</sub> = 0
	r <sub>DS(on)</sub>	Drain Source ON Resistance	100	Ω	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 100 μA, V <sub>BS</sub> = 0
			200		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 100 μA, V <sub>BS</sub> = 0
	C <sub>iss</sub>	Input Capacitance	10	pF	V <sub>GB</sub> = 0, V <sub>DB</sub> = 10 V, V <sub>BS</sub> = 0
	C <sub>gs</sub>	Gate-Source Capacitance	2.5		V <sub>GB</sub> = V <sub>DB</sub> = 0
	C <sub>gd</sub>	Gate-Drain Capacitance	2.5		Body Guarded
	C <sub>db</sub>	Drain-Body Capacitance	7		V <sub>GB</sub> = 0, V <sub>DB</sub> = 10 V

MBN



## enhancement-type n-channel MOSFET designed for . . .

**B**  
**Siliconix**

- Audio Amplifiers
- Analog Circuits
- Digital Switching Circuits
- Commutating Circuits

### BENEFITS:

- Integrated Zener Clamp Protects the Gate
- Normally OFF

### TYPE

Single  
Single

### PACKAGE

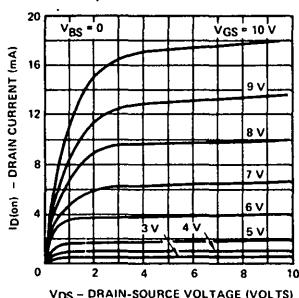
TO-72  
Chip

### PRINCIPAL DEVICES

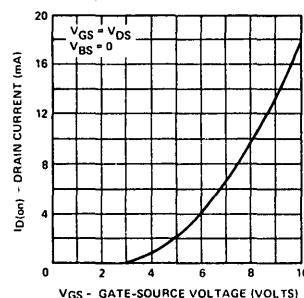
M116  
M116CHP

## PERFORMANCE CURVES (25°C unless otherwise noted)

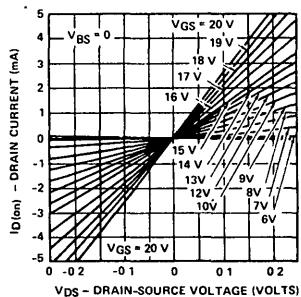
Output Characteristics



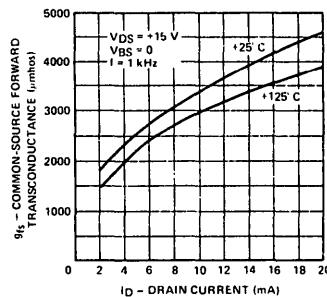
Transfer Characteristic



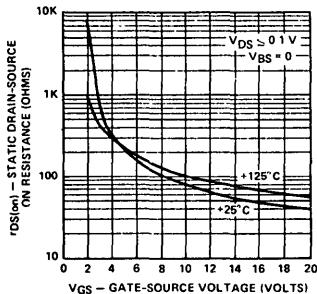
Low Voltage Output Characteristics



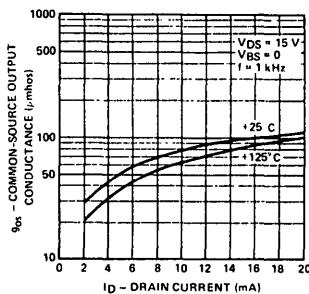
Forward Transconductance vs Drain Current



Drain-Source ON State Resistance vs Gate-Source Bias

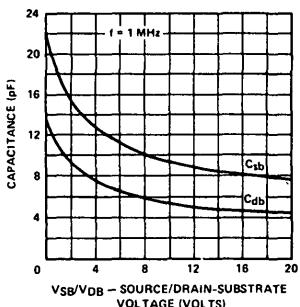


Output Conductance vs Drain Current

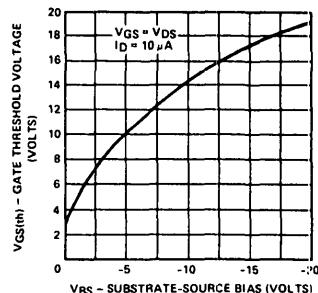


## PERFORMANCE CURVES (Cont'd) (25°C unless otherwise noted)

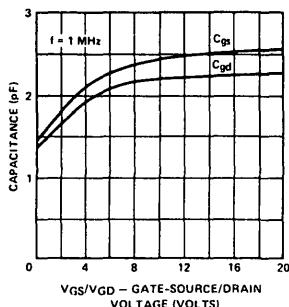
Substrate Capacitance vs Voltage



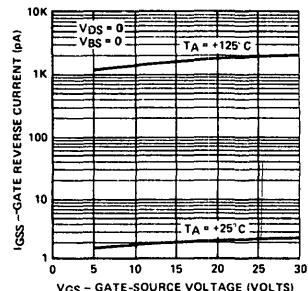
Gate Threshold Voltage vs Substrate Bias



Gate Capacitance vs Voltage



Gate Leakage Current vs Gate-Source Bias



Source-Drain Leakage Currents vs Voltage

