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VOLTAGE PROTECTION FOR 2-, 3-, OR 4-CELL Li-lon BATTERIES (2nd-LEVEL PROTECTION)

FEATURES

- 2-, 3-, or 4-Cell Secondary Protection
- Low Power Consumption I_{CC} < 2 μA [VCELL_(ALL) < V_(PROTECT)]
- Fixed High Accuracy Overvoltage Protection Threshold
 - bq29410 = 4.35 V
 - bq29411 = 4.40 V
 - bq29412 = 4.45 V
 - bq29413 = 4.50 V
 - bq29414 = 4.55 V
 - bq29415 = 4.60 V
 - bq29419 = 4.30 V
- Programmable Delay Time of Detection
- High Power Supply Ripple Rejection
- Stable During Pulse Charge Operation

APPLICATIONS

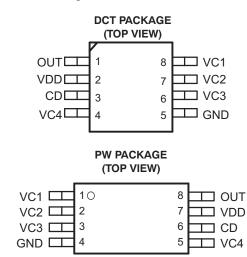
- 2nd-Level Overvoltage Protection in Li-lon Battery Packs in:
 - Notebook Computers
 - Portable Instrumentation
 - Portable Equipment

DESCRIPTION

The bq2941x is a secondary overvoltage protection IC for 2-, 3-, or 4-cell lithium-ion battery packs that incorporates a high-accuracy precision overvoltage detection circuit. It includes a programmable delay circuit for overvoltage detection time.

FUNCTION

Each cell in a multiple-cell pack is compared to an internal reference voltage. If one cell reaches an overvoltage condition, the protection sequence begins. The bq2941x device starts charging an external capacitor through the CD pin. When the CD pin voltage reaches 1.2 V, the OUT pin changes from a low level to a high level.





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.





These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ORDERING INFORMATION⁽¹⁾

т	V (2)		PAC	CKAGE ⁽³⁾	
T _A	V _(PROTECT) ⁽²⁾	MSOP (DCT)	SYMBOL	SSOF	(PW)
	4.30 V	bq29419DCTR	CJQ	bg29419PWG4	bq29419PWRG4
	4.30 V	bq29419DCTT	CJQ	bq29419FWG4	bq29419FVVKG4
		bq29410DCT3R			
	4.35 V	bq29410DCTR	CJG	bq29410PW bq29410PWG4	bq29410PWR bg29410PWRG4
		bq29410DCTT		542011011101	54251101111101
		bq29411DCT3R			
	4.40 V	bq29411DCTR	CJH	bq29411PW bq29411PWG4	bq29411PWR bg29411PWRG4
		bq29411DCTT		54251111101	5425111111101
-40°C to 110°C		bq29412DCT3R			
	4.45 V	bq29412DCTR	CJJ	bq29412PW bq29412PWG4	bq29412PWR bq29412PWRG4
		bq29412DCTT		54204121 ***	54254121 WICO4
	4.50 V	bq29413DCTR	CJk	h~20442DW	h~20442DWD
	4.50 V	bq29413DCTT	CJK	bq29413PW	bq29413PWR
	4.55 V	bq29414DCTR	CJL	h~20444DW/	h~20444DWD
	4.55 V	bq29414DCTT	CJL	bq29414PW	bq29414PWR
	4.60 V	bq29415DCTR	CJM	h~20445DW	h~2044EDWD
	4.00 V	bq29415DCTT	CJIVI	bq29415PW	bq29415PWR

⁽¹⁾ For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.

ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range unless otherwise noted (1)(2)

		UNIT
Supply voltage range	VDD	–0.3 V to 28 V
Input voltage range	VC1, VC2, VC3, VC4	–0.3 V to 28 V
Input voltage range	VC1 TO VC2, VC2 TO VC3, VC3 TO VC4, VC4 TO GND	−0.3 V to 8 V
Output voltogo rongo	OUT	–0.3 V to 28 V
Output voltage range	CD	–0.3 V to 28 V
Continuous total power di	ssipation	See Dissipation Rating Table
Storage temperature rang	ge, T _{stg}	−65°C to 150°C
Lead temperature (solder	ing, 10 s)	300°C

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

PACKAGE DISSIPATION RATINGS

PACKAGE	T _A = 25°C POWER RATING	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 85°C POWER RATING
DCT	412 mW	3.3 mW/°C	264 mW	214 mW
PW	525 mW	4.2 mW/°C	336 mW	273 mW

⁽²⁾ Contact your local Texas Instruments representative or sales office for alternative overvoltage threshold options.

⁽³⁾ The "R" suffix indicates tape-and-reel packaging.

⁽²⁾ All voltages are with respect to ground of this device except the differential voltage of VC1-VC2, VC2-VC3, VC3-VC4, and VC4-GND.





RECOMMENDED OPERATING CONDITIONS

			MIN	NOM	MAX	UNIT
V_{DD}	Supply voltage		4		25	V
\/	Input voltage range	VC1, VC2, VC3, VC4	0		25	V
VI	Input voltage range	VCn – VC (n=1), (n=1, 2, 3), VC4 – GND	0		5	v
t _{d(CD)}	Delay time capacitance			0.22		μF
R _{IN}	Voltage-monitor filter re-	sistance	100	1k		Ω
C _{IN}	Voltage-monitor filter ca	pacitance	0.01	0.1		μF
R_{VD}	Supply-voltage filter res	istance	0		1	kΩ
C_{VD}	Supply-voltage filter cap	pacitance		0.1		μF
T _A	Operating ambient temp	perature range	-40		110	°C

ELECTRICAL CHARACTERISTICS

over recommended operating free-air temperature range, $T_A = 25$ °C (unless otherwise noted)

	PARAMETER	TEST CONDITION	MIN	NOM	MAX	UNIT
		T _A = 25°C		25	35	
$V_{(OA)}$	Overvoltage detection accuracy	$T_A = -20$ °C to 85°C		25	50	mV
	accuracy	$T_A = -40$ °C to 110°C			80	
		bq29410		4.35		
		bq29411		4.40		
		bq29412		4.45		
V _(PROTECT)	Overvoltage detection voltage	bq29413		4.50		V
	detection voltage	bq29414		4.55		
		bq29415		4.60		
		bq29419		4.30		
\ /	Overvoltage detection	bq29410/11/12/13/14/15		320		
V_{hys}	hysteresis	bq29419	250	320	450	mV
I _{IN}	Input current	V2, V3 , VC4 input ,V _{DD} = VC1 VC1 = VC2 = VC3 = VC4 = 3.5 V (see Figure 1)			0.3	μΑ
t _{D1}	Overvoltage detection delay time	V _{DD} = VC1, CD = 0.22 μF	1	1.5	2	s
I _(CD_dis)	CD GND clamp current	V _{DD} = VC1, CD = 1 V	5	12		μΑ
1	Cupply current	V _{DD} = VC1, VC1-VC2 = VC2-VC3 = VC3-VC4 = VC4-GND = 3.5 V (see Figure 1)		2	3	
I _{CC}	Supply current	V _{DD} = VC1, VC1-VC2 = VC2-VC3 = VC3-VC4 = VC4-GND = 2.3 V (see Figure 1)		1.5	2.5	μA
V	OUT pin drive voltage	$VC1-VC2 = VC2-VC3 = VC3-VC4 = VC4-GND = V_{(PROTECT)}Max, V_{DD} = 14 V, I_{OH} = 0 mA$		7		V
V _(OUT)	Oo i piii urive voltage	$ \begin{array}{l} VC1 = VC2 = VC3 = VC4 = V_{(PROTECT)}Max, \\ V_{DD} = 4.3 \text{ V, } T_{A} = 0^{\circ}\text{C to } 70^{\circ}\text{C, } I_{OH} = 40 \mu\text{A} \end{array} $	1.5	2	2.5	V
I _{OH}	High-level output current	OUT = 3 V, VC1-VC2 = VC2-VC3 = VC3-VC4 = VC4-GND = V _(PROTECT) Max, V _{DD} = 14 V			-1	mA
I _{OL}	Low-level output current	OUT = 0.1 V, V _{DD} = VC1, VC1-VC2 = VC2-VC3 = VC3-VC4 = VC4-GND = 3.5 V	5			μΑ



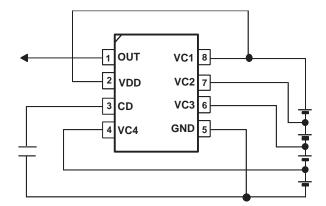


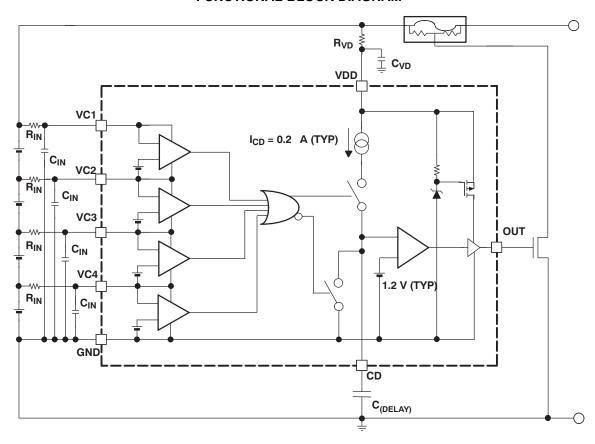
Figure 1. I_{CC} , I_{IN} Measurement (DCT Package)

Terminal Functions

	TERMINAL		
MSOP (DCT)	TSSOP (PW)	NAME	DESCRIPTION
8	1	VC1	Sense voltage input for most positive cell
7	2	VC2	Sense voltage input for second most positive cell
6	3	VC3	Sense voltage input for third most positive cell
5	4	GND	Ground pin
4	5	VC4	Sense voltage input for least positive cell
3	6	CD	An external capacitor is connected to determine the programmable delay time
2	7	VDD	Power supply
1	8	OUT	Output



FUNCTIONAL BLOCK DIAGRAM



OVERVOLTAGE PROTECTION

When one of the cell voltages exceeds $V_{(PROTECT)}$, an internal current source begins to charge the capacitor, $C_{(DELAY)}$, connected to the CD pin. If the voltage at the CD pin, V_{CD} , reaches 1.2 V, the OUT pin is activated and transitions high. An externally connected NCH FET is activated and blows the external fuse in the positive battery rail; see the functional block diagram.

If all cell voltages fall below $V_{(PROTECT)}$ before the voltage at pin CD reaches 1.2 V, the delay time does not run out. An internal switch clamps the CD pin to GND and discharges the capacitor, $C_{(DELAY)}$, and secures the full delay time for the next occurring overvoltage event.

Once the pin OUT is activated, it transitions back from high to low after all battery cells reach V_(PROTECT) - V_{hvs.}

DELAY TIME CALCULATION

The delay time is calculated as follows:

$$t_{d} = \frac{\left[1.2 \text{ V} \times \text{C}_{(DELAY)}\right]}{\text{I}_{CD}}$$
$$C_{(DELAY)} = \frac{\left[t_{d} \times \text{I}_{CD}\right]}{1.2 \text{ V}}$$

Where $I_{(CD)} = CD$ current source = 0.18 μ A



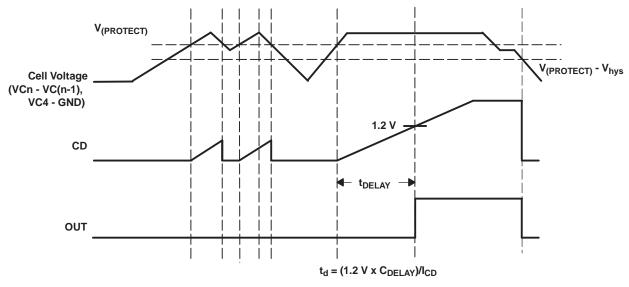


Figure 2. Timing for Overvoltage Sensing

APPLICATION INFORMATION

BATTERY CONNECTIONS

The following diagrams show the DCT package device in different cell configurations.

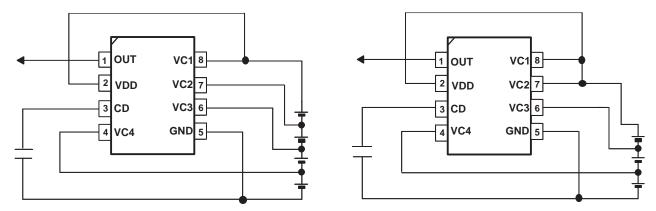


Figure 3. 4-Series Cell Configuration

Figure 4. 3-Series Cell Configuration (Connect together VC1 and VC2)



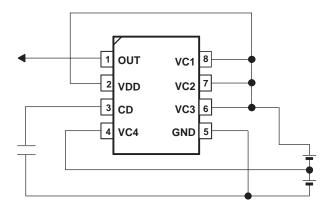


Figure 5. 2-Series Cell Configuration

CELL CONNECTIONS

To prevent incorrect output activation, the following connection sequences must be used.

4-Series Cell Configuration

- $VC1(=VDD) \rightarrow VC2 \rightarrow VC3 \rightarrow VC4 \rightarrow GND$ or
- GND \rightarrow VC4 \rightarrow VC3 \rightarrow VC2 \rightarrow VC1(=VDD)

3-Series Cell Configuration

- $VC1(=VC2=VDD) \rightarrow VC3 \rightarrow VC4 \rightarrow GND$ or
- GND \rightarrow VC4 \rightarrow VC3 \rightarrow VC1(=VC2=VDD)

2-Series Cell Configuration

- $VC1(=VC2=VC3=VDD) \rightarrow VC4 \rightarrow GND$ or
- GND → VC4 → VC1(=VC2=VC3=VDD)





9-Aug-2013

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Sample
BQ29410DCT3R	ACTIVE	SM8	DCT	8	3000	Pb-Free (RoHS)	CU SNBI	Level-1-260C-UNLIM	-40 to 110	CJG W	Sample
BQ29410DCT3RE6	ACTIVE	SM8	DCT	8	3000	Pb-Free (RoHS)	CU SNBI	Level-1-260C-UNLIM	-40 to 110	CJG W	Sample
BQ29410DCTR	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJG W	Sample
BQ29410DCTRG4	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJG W	Sample
BQ29410DCTT	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJG W	Sample
BQ29410DCTTG4	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJG W	Sampl
BQ29410PW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	29410	Sampl
BQ29410PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	29410	Sampl
BQ29410PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29410	Sampl
BQ29411DCT3R	ACTIVE	SM8	DCT	8	3000	Pb-Free (RoHS)	CU SNBI	Level-1-260C-UNLIM	-40 to 110	CJH W	Sampl
BQ29411DCT3RE6	ACTIVE	SM8	DCT	8	3000	Pb-Free (RoHS)	CU SNBI	Level-1-260C-UNLIM	-40 to 110	CJH W	Sampl
BQ29411DCTR	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJH W	Sampl
BQ29411DCTRG4	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJH W	Sampl
BQ29411DCTT	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJH W	Samp
BQ29411DCTTG4	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJH W	Samp
BQ29411PW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	29411	Samp
BQ29411PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	29411	Samp





9-Aug-2013

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Sample
BQ29411PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29411	Sample
BQ29412DCT3R	ACTIVE	SM8	DCT	8	3000	Pb-Free (RoHS)	CU SNBI	Level-1-260C-UNLIM	-40 to 110	M CJJ	Sample
BQ29412DCT3RE6	ACTIVE	SM8	DCT	8	3000	Pb-Free (RoHS)	CU SNBI	Level-1-260C-UNLIM	-40 to 110	M CJJ	Sample
BQ29412DCT3T	PREVIEW	,		8		TBD	Call TI	Call TI	-40 to 110		
BQ29412DCTR	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	M C11	Sample
BQ29412DCTRG4	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	M C11	Sample
BQ29412DCTT	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	M C11	Sample
BQ29412DCTTG4	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	M C11	Sample
BQ29412PW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	29412	Sample
BQ29412PWG4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29412	Sample
BQ29412PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		29412	Sample
BQ29412PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR		29412	Sample
BQ29413DCTR	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJK W	Sample
BQ29413DCTRG4	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJK W	Sample
BQ29413DCTT	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJK W	Sample
BQ29413DCTTG4	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJK W	Sample
BQ29413PW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29413	Sample
BQ29413PWG4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29413	Sample





9-Aug-2013

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Sample
BQ29413PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29413	Sample
BQ29413PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29413	Sample
BQ29414DCTR	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJL W	Sample
BQ29414DCTRG4	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJL W	Sample
BQ29414DCTT	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJL W	Sample
BQ29414DCTTG4	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJL W	Sample
BQ29414PW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	2914	Sample
BQ29414PWG4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	2914	Sample
BQ29414PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	2914	Sample
BQ29414PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	2914	Sample
BQ29415DCTR	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJM W	Sample
BQ29415DCTRG4	ACTIVE	SM8	DCT	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJM W	Sample
BQ29415DCTT	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJM W	Sample
BQ29415DCTTG4	ACTIVE	SM8	DCT	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 110	CJM W	Sample
BQ29415PW	ACTIVE	TSSOP	PW	8		TBD	Call TI	Call TI	-40 to 110	2915	Sampl
BQ29415PWG4	ACTIVE	TSSOP	PW	8		TBD	Call TI	Call TI	-40 to 110	2915	Sampl
BQ29415PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	2915	Sampl
BQ29415PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	2915	Samp



PACKAGE OPTION ADDENDUM

9-Aug-2013

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)		(3)		(4/5)	
BQ29419PW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29419	Samples
BQ29419PWG4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29419	Samples
BQ29419PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29419	Samples
BQ29419PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 110	29419	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

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PACKAGE OPTION ADDENDUM

9-Aug-2013

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

PACKAGE MATERIALS INFORMATION

www.ti.com 11-Jun-2013

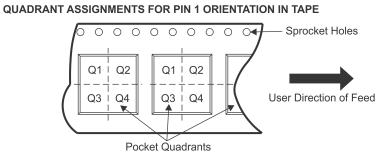
TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

— Reel viidii (vv i)



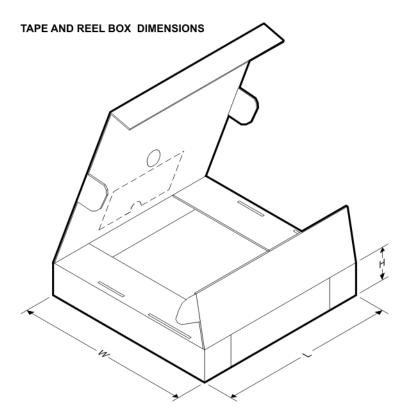
*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
BQ29410DCT3R	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29410DCTR	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29410DCTT	SM8	DCT	8	250	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29410PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29410PWRG4	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29411DCT3R	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29411DCTR	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29411DCTT	SM8	DCT	8	250	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29411PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29411PWRG4	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29412DCTR	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29412DCTT	SM8	DCT	8	250	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29412PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29412PWRG4	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29413DCTR	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29413DCTT	SM8	DCT	8	250	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29413PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29414DCTR	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3

PACKAGE MATERIALS INFORMATION

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Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
BQ29414DCTT	SM8	DCT	8	250	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29414PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29415DCTR	SM8	DCT	8	3000	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29415DCTT	SM8	DCT	8	250	180.0	13.0	3.35	4.5	1.55	4.0	12.0	Q3
BQ29415PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
BQ29419PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BQ29410DCT3R	SM8	DCT	8	3000	182.0	182.0	20.0
BQ29410DCTR	SM8	DCT	8	3000	182.0	182.0	20.0
BQ29410DCTT	SM8	DCT	8	250	182.0	182.0	20.0
BQ29410PWR	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29410PWRG4	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29411DCT3R	SM8	DCT	8	3000	182.0	182.0	20.0
BQ29411DCTR	SM8	DCT	8	3000	182.0	182.0	20.0
BQ29411DCTT	SM8	DCT	8	250	182.0	182.0	20.0
BQ29411PWR	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29411PWRG4	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29412DCTR	SM8	DCT	8	3000	182.0	182.0	20.0



PACKAGE MATERIALS INFORMATION

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Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BQ29412DCTT	SM8	DCT	8	250	182.0	182.0	20.0
BQ29412PWR	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29412PWRG4	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29413DCTR	SM8	DCT	8	3000	182.0	182.0	20.0
BQ29413DCTT	SM8	DCT	8	250	182.0	182.0	20.0
BQ29413PWR	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29414DCTR	SM8	DCT	8	3000	182.0	182.0	20.0
BQ29414DCTT	SM8	DCT	8	250	182.0	182.0	20.0
BQ29414PWR	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29415DCTR	SM8	DCT	8	3000	182.0	182.0	20.0
BQ29415DCTT	SM8	DCT	8	250	182.0	182.0	20.0
BQ29415PWR	TSSOP	PW	8	2000	367.0	367.0	35.0
BQ29419PWR	TSSOP	PW	8	2000	367.0	367.0	35.0

DCT (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion
- D. Falls within JEDEC MO-187 variation DA.

DCT (R-PDSO-G8)

PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



PW (R-PDSO-G8)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M—1994.
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
- E. Falls within JEDEC MO-153



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