

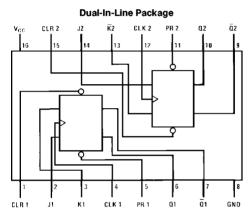
# DM54109 Dual Positive-Edge-Triggered J-K Flip-Flops with Preset, Clear, and Complementary Outputs

# **General Description**

This device contains two independent positive-edge-triggered J- $\overline{K}$  flip-flops with complementary outputs. The J and  $\overline{K}$  data is accepted by the flip-flop on the rising edge of the clock pulse. The triggering occurs at a voltage level and is not directly related to the transition time of the rising edge of

the clock. The data on the J and  $\overline{K}$  inputs may be changed while the clock is high or low as long as setup and hold times are not violated. A low logic level on the preset or clear inputs will set or reset the outputs regardless of the logic levels of the other inputs.

# **Connection Diagram**



Order Number DM54109J or DM54109W See NS Package Number J16A or W16A

TL/F/6537-1

## **Function Table**

Inputs					Outputs	
PR	CLR	CLK	J	K	Q	Q
L	Н	Х	Х	Х	Н	L
н	L	X	X	Х	L	Н
L	L	X	X	Х	H*	H*
н	н	↑	L	L	L	Н
Н	н	↑	Н	L	Toggle $Q_0  \overline{Q}_0$	
Н	н	↑	L	Н	$Q_0$	$\overline{Q}_0$
Н	н	↑	Н	Н	Н	L
н	н	L	X	Х	$Q_0$	$\overline{Q}_0$

H = High Logic Level

L = Low Logic Level

 $\uparrow$  = Rising Edge of Pulse.

\* = This configuration is nonstable; that is, it will not persist when preset and clear inputs return to their inactive (high) level.

 ${\bf Q}_0={\sf The}$  output logic level of Q before the indicated input conditions were established.

Toggle = Each output changes to the complement of its previous level on each active transition of the clock pulse.

## **Absolute Maximum Ratings (Note)**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage Input Voltage 5.5V Operating Free Air Temperature Range

-55°C to +125°C -65°C to +150°C

DM54

Storage Temperature Range

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter			DM54109		
Symbol			Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage		4.5	5	5.5	٧
V <sub>IH</sub>	High Level Input V	oltage	2			٧
V <sub>IL</sub>	Low Level Input Voltage				0.8	V
Гон	High Level Output Current				-1.2	mA
loL	Low Level Output Current				16	mA
fclk	Clock Frequency (Note 6)		0		30	MHz
t <sub>W</sub>	Pulse Width (Note 6)	Clock High	20			- ns
		Clock Low	20			
		Preset Low	20			
		Clear Low	20			
t <sub>SU</sub>	Input Setup Time (Notes 1 & 6)		15↑			ns
t <sub>H</sub>	Input Hold Time (Notes 1 & 6)		10↓			ns
TA	Free Air Operating Temperature		-55		125	°C

# **Electrical Characteristics** over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 2)	Max	Units
V <sub>I</sub>	Input Clamp Voltage	$V_{CC} = Min, I_{I} = -12 \text{ mA}$				<b>-1.5</b>	>
V <sub>OH</sub>	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{IL} = Max, V_{IH} = Min$		2.4	3.4		>
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$			0.2	0.4	٧
lį	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
I <sub>IH</sub>	High Level Input Current	$V_{CC} = Max$ $V_{I} = 2.4V$	J,K			40	μΑ
			Preset			80	
			Clock			80	
			Clear			160	
նլլ	Low Level Input Current	$V_{CC} = Max$ $V_{I} = 0.4V$ (Note 5)	J, K			-1.6	mA
			Preset			-3.2	
			Clock			-3.2	
			Clear			-4.8	
Ios	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 3)		-30		-85	mA
Icc	Supply Current	V <sub>CC</sub> = Max (Note 4)			20	30	mA

Note 1: The symbol ( 1) indicates the rising edge of the clock pulse is used for reference.

Note 2: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

Note 3: Not more than one output should be shorted at a time.

Note 4: With all outputs open, ICC is measured with the Q and  $\overline{Q}$  outputs high in turn. At the time of measurement the clock input grounded.

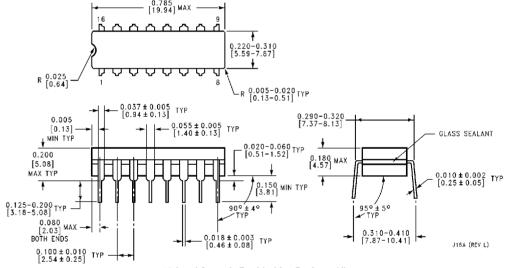
Note 5: Clear is tested with preset high and preset is tested with clear high.

Note 6:  $T_A = 25^{\circ}C$  and  $V_{CC} = 5V$ .

Switching Characteristics	at $V_{CC} = 5V$ and $T_{\Delta} = 25^{\circ}C$	(See Section 1 for Test Waveforms and Output Load)
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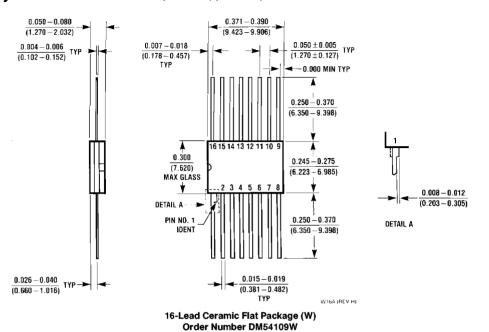
Symbol	Parameter	From (Input) To (Output)	R <sub>L</sub> = C <sub>L</sub> =	Units	
		To (Output)	Min	Max	
f <sub>MAX</sub>	Maximum Clock Frequency		30		MHz
<sup>†</sup> PLH	Propagation Delay Time Low to High Level Output	Preset to Q		14	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Preset to Q		29	ns
<sup>t</sup> PLH	Propagation Delay Time Low to High Level Output	Clear to Q		14	ns
<sup>†</sup> PHL	Propagation Delay Time High to Low Level Output	Clear to Q		25	ns
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	Clock to Q or Q		18	ns
<sup>t</sup> PHL	Propagation Delay Time High to Low Level Output	Clock to $Q$ or $\overline{Q}$		28	ns

# Physical Dimensions inches (millimeters)



16-Lead Ceramic Dual-In-Line Package (J) Order Number DM54109J NS Package Number J16A

## Physical Dimensions inches (millimeters) (Continued)



NS Package Number W16A

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